# Force Computers SYS68K-CPU-30BE16-REV-3

**INSTALLATION & COMMISSIONING GUIDE / EXACT-MODEL ENGINEERING FILE**

Force Computers SYS68K-CPU-30BE16-REV-3 is identified as a vmebus cpu board for the Force Computers SYS68K VMEbus platform. The complete marking and installed interface arrangement must be matched before replacement.

## 01 Job scope and safe-work boundary

| | |
|---|---|
| **Offered assembly** | Force Computers SYS68K-CPU-30BE16-REV-3 |
| **Installed system** | Force Computers SYS68K VMEbus platform |
| **Work boundary** | Follow the site procedure for stored energy, field power and electrostatic handling. |
| **As-found record** | Send the complete SYS68K-CPU-30BE16-REV-3 marking, quantity and installed-system context. |

## Readiness controls

**Verify the complete identity**
Check every token in SYS68K-CPU-30BE16-REV-3 against the offered label and approved record.

**Isolate the work boundary**
Follow the site procedure for stored energy, field power and electrostatic handling.

**Record connections**
Photograph and label carriers, terminals, cables and keying before removal.

**Preserve configuration**
Archive applicable Force Computers SYS68K VMEbus platform settings before replacement.

## Related product references

- Fanuc A20B-3300-0281/04B
- General Electric IC695PNC001
- General Electric DS200TBCAG1AAB
- Hardy 1746-WS
- HMS AB5030-B
- ABB DO801 3BSE020510R1
- Bently Nevada 3300/47
- B&R 8V1022.001-2

## Engineering record prompts

| | |
|---|---|
| **Connector record** | Photograph every used connector, cable label, keying feature and shield termination. |
| **Host record** | Identify the carrier, rack, drive, chassis or operator station that owns the installed position. |
| **Configuration record** | Archive the applicable project, address, parameter, firmware or hardware-setting evidence. |

## 02 Replacement sequence

| | |
|---|---|
| **01 Record** | Send the complete SYS68K-CPU-30BE16-REV-3 marking, quantity and installed-system context. |
| **02 Verify** | Compare SYS68K-CPU-30BE16-REV-3 with the installed label and applicable Force Computers documentation. |
| **03 Isolate** | Follow the site procedure for stored energy, field power and electrostatic handling. |
| **04 Compare** | Check connectors, terminal blocks, carriers and cable condition before blaming the module. |
| **05 Reconnect** | Photograph and label carriers, terminals, cables and keying before removal. |
| **06 Configure** | Archive applicable Force Computers SYS68K VMEbus platform settings before replacement. |
| **07 Prove** | Use an approved functional test and retain as-left diagnostic and acceptance evidence. |
| **08 Handover** | Retain the offered and removed SYS68K-CPU-30BE16-REV-3 markings, connection photographs, configuration evidence, diagnostics, functional-test results and open exceptions. |

## Stop conditions

**Identity mismatch**
Stop if any complete-model token or revision marker differs.

**Interface mismatch**
Stop if keying, connector count or installed position does not match.

**Missing baseline**
Stop if the installed configuration or cable map cannot be recovered.

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## Engineering record prompts

| | |
|---|---|
| **Host record** | Identify the carrier, rack, drive, chassis or operator station that owns the installed position. |
| **Configuration record** | Archive the applicable project, address, parameter, firmware or hardware-setting evidence. |
| **Supply-scope record** | List terminal blocks, carriers, plugs, cables and accessories separately in the quotation. |
| **Baseline result** | Record initial indications, diagnostics and the controlled-system condition before work. |

## 03 Interface restoration and application fit

| | |
|---|---|
| **Complete identity** | Use SYS68K-CPU-30BE16-REV-3 as one complete reference in the quotation, service record and acceptance file. |
| **Installed position** | Record the exact host position and functional ownership within the Force Computers SYS68K VMEbus platform. |
| **Physical execution** | Compare artwork, connectors, keying, terminal hardware and orientation with the removed assembly. |
| **Configuration boundary** | Preserve the applicable project, address, firmware or hardware-setting record before removal. |
| **Acceptance evidence** | Retain initial and as-left diagnostics plus the controlled functional-test result. |
| **Unstated values** | Do not assign electrical or compatibility values that are not established by the exact-model evidence. |

## Application acceptance

**Installed-system replacement**
Compare the exact code, host position and connected interfaces with the removed item.

**Planned maintenance**
Capture the as-found label, indications and configuration before disturbing the installation.

**Controlled commissioning**
Restore documented connections and prove the intended signal, communication or control response.

**Spare qualification**
Keep revision, configuration and acceptance evidence with the spare-part record.

## Applicable technical data

| | |
|---|---|
| **Complete order code** | SYS68K-CPU-30BE16-REV-3 |
| **Product type** | VMEbus CPU Board |
| **System context** | Force Computers SYS68K VMEbus platform |
| **Board family** | SYS68K/CPU-30 |
| **Memory execution marker** | BE16 |
| **Hardware revision** | REV-3 |

## Related product references

Hardy 1746-WS

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Fanuc A20B-3300-0281/04B

## 04 Commissioning and fault isolation

| | |
|---|---|
| **Identity** | Confirm the complete marking, artwork and installed position. |
| **Interface map** | Restore connectors and cables only from the approved as-found record. |
| **Controlled power-up** | Observe host and unit indications before enabling the process function. |
| **Functional proof** | Test the intended vmebus cpu board role and retain diagnostics with the handover. |

## Diagnostic questions

| | |
|---|---|
| **What configuration boundary matters when commissioning SYS68K-CPU-30BE16-REV-3?** | Use the approved Force Computers SYS68K VMEbus platform configuration for SYS68K-CPU-30BE16-REV-3. Physical fit alone does not establish electrical, firmware or project equivalence. |
| **How can a suspected SYS68K-CPU-30BE16-REV-3 fault be separated from an external fault?** | Check host power, mating hardware, field wiring and the applicable signal or communication path before controlled substitution of SYS68K-CPU-30BE16-REV-3. |
| **What should be verified after installing SYS68K-CPU-30BE16-REV-3?** | Confirm normal indications, interface continuity and the intended vmebus cpu board response for SYS68K-CPU-30BE16-REV-3 in an approved test state. |
| **Can SYS68K-CPU-30BE16-REV-3 be replaced by a similar-looking unit?** | Appearance is not an equivalence criterion for SYS68K-CPU-30BE16-REV-3; compare the complete order code, revision, connectors and installed configuration. |

## Result record matrix

| | |
|---|---|
| **Baseline** | Record the pre-test state and host position. |
| **Stimulus** | Apply only the approved command, signal or diagnostic action. |
| **Observation** | Capture unit indication, host diagnostic and field response together. |
| **Disposition** | Accept, correct and retest, or record the exception with its owner. |

## Related product references

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## 05 Handover and service record

**Identity evidence**
Offered and removed markings plus installed position.

**Configuration evidence**
As-found and as-left project, address or setting record.

**Test evidence**
Diagnostics, controlled functional result and open exceptions.

**Supply evidence**
Included items and separately supplied accessories confirmed in the quotation.

## Close-out checks

| | |
|---|---|
| **Identity closed** | The as-left record names SYS68K-CPU-30BE16-REV-3 without a shortened family reference. |
| **Interfaces closed** | Every restored connection is traceable to the pre-work record. |
| **Function closed** | The intended vmebus cpu board role has a recorded controlled-test result. |
| **Exceptions closed** | Outstanding issues have an owner and documented action. |

## Ordering checks

| | |
|---|---|
| **Match the complete marking** | Compare SYS68K-CPU-30BE16-REV-3 with the installed label and applicable Force Computers documentation. |
| **Check the offered item before ordering** | Request photos of the offered SYS68K-CPU-30BE16-REV-3 and compare markings, interfaces and included items with the installed unit. |

## Related product references

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Hardy 1746-WS

## Engineering record prompts

| | |
|---|---|
| **Baseline result** | Record initial indications, diagnostics and the controlled-system condition before work. |
| **Identity decision** | Approve only the complete order code and observed execution; do not shorten the family name. |
| **Interface decision** | Confirm connector count, orientation, pin ownership and field wiring against the as-found map. |

**Discuss the complete SYS68K-CPU-30BE16-REV-3 marking with Apter Power.**

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Scan the WhatsApp QR to share nameplate and installed-system evidence.