5-4.15. LAN Communications Card (DS215SLCC)

The DS215SLCC LAN Communications Card (SLCC) provides isolated and non-isolated input circuits for communications to the drive. The SLCC connects to the NTB/3TB or STBA, SDCC, and DCFB or SDCI boards via 2PL; to the SDCC card via 3PL; and to the optional LTB board via IOPL. The SLCC also connects to the Programmer.

The SLCC features an alphanumeric display controller that connects to the Programmer. Refer to Chapter 8 for information about the Programmer.

The SLCC also includes onboard software contained in two EPROMs, designated U6 and U7.

NOTE

The DS*200*SLCC card does not include EPROMs U6 and U7 (the sockets are empty). To order a replacement board including these two memory chips, specify a DS*215*SLCC card.

The SLCC has three group numbers. The G1 version of the SLCC includes circuits used to connect the drive to DLAN (drive local area network) or DLAN+ (ARCNET) networks; the G2 version includes only the DLAN interface circuits; and the G4 version includes neither the DLAN nor the ARCNET circuits.

Section 6-16 defines I/O points for the SLCC card.

5-4.15.1. CONFIGURABLE HARDWARE. The SLCC includes Berg-type jumpers and hard-wired jumpers. Figure 5-19 shows the locations of the SLCC card jumpers. Table 5-14 lists and defines these items.

5-4.15.2. REPLACING/INSERTING SOFTWARE. The SLCC must include the onboard software stored in EPROMs U6 and U7 to function in the drive. When replacing the SLCC card, the onboard software must be transferred from the old card to the new. Carefully remove each of the two EPROM chips from the old card and insert it into the same socket on the new card.

CAUTION

To prevent component damage caused by static electricity, treat all boards with static sensitive handling techniques. Use a grounding strap when handling boards or components. Store boards in anti-static bags or boxes.

To prevent damage to components, use the proper chip extraction/insertion tool when performing the following procedure.

To prevent damage to memory chips, ensure that chips are properly oriented when inserting them into sockets.

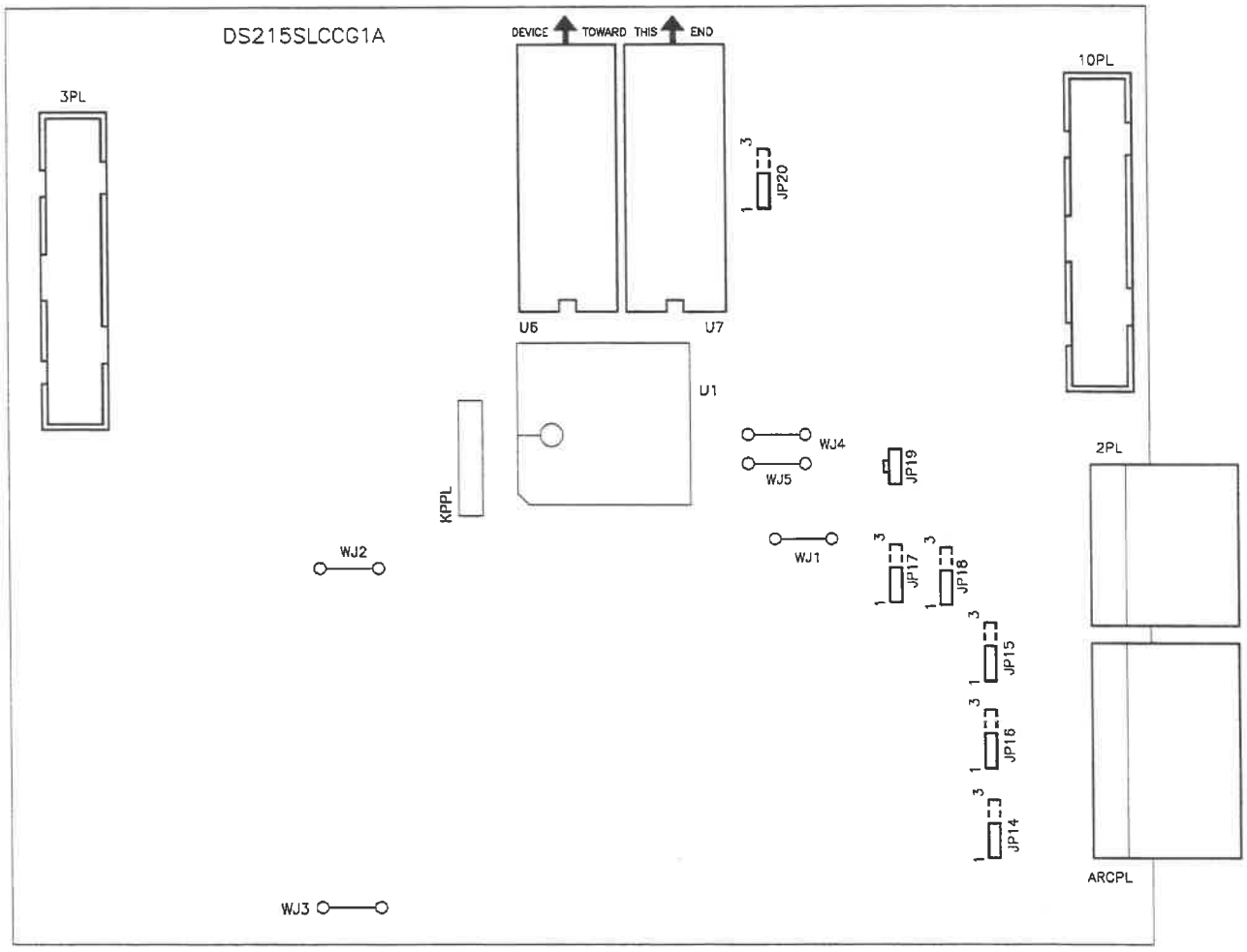

Figure 5-19. SLCC Card Layout

Table 5-14. SLCC Card Adjustable Hardware

Revision	Name	Description
Jumpers JP14 through JP18 are not present on DS215SLCCG4 cards.		
All	JP14	*Groups 1 and 2 only* – This jumper selects the RS-422 DLAN drivers or the isolated DLAN circuit 1.2 Isolated DLAN circuit 2.3 RS422 drivers and receivers
All	JP15	*Groups 1 and 2 only* – This jumper selects the RS-422 DLAN drivers or the isolated DLAN circuit 1.2 Isolated DLAN circuit 2.3 RS-422 drivers and receivers
All	JP16	*Groups 1 and 2 only* – This jumper selects the RS-422 DLAN drivers or the isolated DLAN circuit 1.2 Isolated DLAN circuit 2.3 RS422 drivers and receivers
All	JP17	*Groups 1 and 2 only* – This jumper puts the DLAN termination resistors in the DLAN circuit The termination jumpers should be added to the drives located at the end of a daisy-chain 422 LAN. Never exceed 5 sets of termination resistors in a 422 DLAN circuit. 1.2 Termination resistors in 2.3 Termination resistors out
All	JP18	*Groups 1 and 2 only* – This jumper puts the DLAN termination resistors in the DLAN circuit The termination jumpers should be added to the drives located at the end of a daisy-chain 422 LAN. Never exceed 5 sets of termination resistors in a 422 DLAN circuit. 1.2 Termination resistors in 2.3 Termination resistors out
All	JP19	*Groups 1, 2, and 4* – This jumper connects the crystal to the processor This jumper should be in place except during manufacturing testing. 1.2 Normal running condition 2.3 Manufacturing testing
All	JP20	*Groups 1, 2, and 4* – This jumper sets up the EPROM sockets for either EPROMs or Flash PROMs 1.2 EPROM setting 2.3 Flash PROM setting
All	WJ1	*Groups 1, 2, and 4* – This jumper redirects the Ready Line when there is no ARCNET module This jumper should be in place when there is no ARCNET module. 1.2 No ARCNET module (DS215SLCCG2, G4) 0 ARCNET module present (DS215SLCCG1)
ACC–Pres	WJ2	*Groups 1, 2, and 4* – This jumper connects the LRX signal to ground through a 470-ohm resistor This jumper should be in place for G4 only. 0 LRX signal is not connected to ground (DS215SLCCG1, G2) 1.2 LRX signal is connected to ground (DS215SLCCG4)
ACC–Pres	WJ3	*Groups 1, 2, and 4* – This jumper connects the T2CLK signal to ground through a 470-ohm resistor This jumper should be in place for G4 only. 0 T2CLK signal is not connected to ground (DS215SLCCG1, G2) 1.2 T2CLK signal is connected to ground (DS215SLCCG4)
ACC–Pres	WJ4	*Groups 1, 2, and 4* – This jumper connects the input signal to ground through a 470-ohm resistor This jumper should be in place for G4 only. 0 Input signal is not connected to ground (DS215SLCCG1, G2) 1.2 Input signal is connected to ground (DS215SLCCG4)
ACC–Pres	WJ5	*Groups 1, 2, and 4* – This jumper connects the input signal to ground through a 470-ohm resistor This jumper should be in place for G4 only. 0 Input signal is not connected to ground (DS215SLCCG1, G2) 1.2 Input signal is connected to ground (DS215SLCCG4)