Siemens 6ES5460-4UA12

01 Capture range and mode settings

1. Record the two range-submodule identities, both mode-switch blocks, address and sensor wiring. Identify current-loop, voltage and resistance circuits separately, including any compensation arrangement.

2. ### Range and mode settings — acceptance

   Each four-channel group has a known electrical input circuit and a matching program interpretation before hardware is disturbed.

Stage record	Capture range and mode settings observations and acceptance result
Analog inputs	8
Range-module grouping	4 channels per submodule

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02 Check sensor references

1. Trace the sensor return and excitation paths. Review the Pt100 or two-wire-transmitter common-reference arrangement and confirm that the actual potential differences remain within the module limits.

2. ### Sensor references — acceptance

   No unsupported isolation assumption or accidental shared return remains in the proposed connection.

Stage record	Check sensor references observations and acceptance result
Digital representation	13-bit two’s complement or 12 bits plus sign
Conversion method	Integrating dual slope

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03 Reinstate the measurement hardware

1. Isolate the field circuit as required, then restore the module, submodules and front connector according to the installation procedure. Select the recorded integration frequency and digital representation; verify the gain selection and test-current control wiring.

2. ### Measurement hardware — acceptance

   Hardware range, sensor polarity and software interpretation agree; unused inputs are terminated for the actual submodule type.

Stage record	Reinstate the measurement hardware observations and acceptance result
Integration at 50 / 60 Hz	20 ms / 16⅔ ms
Coding time at 50 / 60 Hz	60 ms / 50 ms per value

04 Verify the analog conversion

1. Apply traceable reference signals at zero and suitable nonzero points. Allow the integrating conversion and channel scan to settle; control thermal offsets and disable test current when making the specified parallel measurement.

2. ### Analog conversion — acceptance

   The complete input path responds monotonically and agrees with the intended scale without unexplained channel interactions.

Stage record	Verify the analog conversion observations and acceptance result
Eight-channel cycle at 50 Hz	Approximately 0.48 s
Permissible common-mode potential	60 V AC / 75 V DC maximum

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05 Prove fault handling and hand over

1. Test the supported overflow and open-circuit response with the process secured. Remove temporary signal sources, restore each sensor and record the final range, switch settings and observed diagnostics.

2. ### Fault handling and hand over — acceptance

   The running program receives both valid measurements and the intended fault indications, with no test jumper or forced value left active.

Technical basis: SIMATIC S5 Analog Modules, C79000-B8576-C300-06.

Related product navigation

- Siemens 6ES7332-5HB01-0AB0
- Siemens 6ES7323-1BL00-0AA0
- Siemens 6ES7332-5HD01-0AB0

Related products for separate application assessment; verify the platform and interface. No interchangeability is implied.