# Siemens 6DD1611-0AG0

## 01 Coupling Memory Module / system role

### Product function and operating context

Siemens 6DD1611-0AG0 supports multi-CPU coordination by making communication memory available between processors while retaining the required position in the subrack bus sequence.

The MM4 is used where several CPU modules exchange data in one subrack or share access to a communication interface. Install it between the first and second CPU modules; its position is part of the communication architecture.

### Integration checkpoints

- Multi-CPU data exchange
- Shared serial-interface access
- Buffered communication data
- Rack-fault supervision

| | |
|---|---|
| Processor coordination | Shared data exchange between CPU modules |
| Bus integration | Memory and termination for both backplane buses |
| Retention path | Subrack battery supports memory during power loss |

The two bus memories support the configured data exchange. The subrack backup arrangement preserves data during power loss, while the system-error relay provides a separate hardware indication that must be interpreted with the CPU states.

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## 02 Technical operating envelope

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|---|---|
| Shared-memory capacity | 2 × 2 MB |
| Backplane interfaces | L bus and C bus |
| Occupied slots | 1 |
| Supply voltage | 5 V DC nominal |
| Supply tolerance | 4.75–5.25 V |

| | |
|---|---|
| Typical current | 800 mA |
| System-error contact voltage | 60 V DC maximum |
| System-error contact current | 0.5 A maximum |
| System-error switching power | 20 W maximum |

Ratings apply to the stated hardware execution and operating conditions; companion hardware and configured functions require separate compatibility checks.

### Slot relationship

The shared-memory module belongs between the first and second CPU modules. Preserve that relationship when altering the rack layout.

### Clock-function separation

The MM4 supplies memory and a system-error relay. A buffered radio-clock requirement belongs to the separate MM3 arrangement and its CPU configuration.

### Backup and error contact

Assess subrack battery continuity separately from normal supply health. Test the error contact with its external circuit within all voltage, current and power limits.

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## 03 System integration and applications

### Multi-CPU data exchange

Provide shared memory for configured interprocessor connections.

### Shared serial-interface access

Support the memory path when several CPUs access a CS7 interface.

### Buffered communication data

Retain configured data through the subrack backup system.

### Rack-fault supervision

Integrate the system-error contact into the approved monitoring circuit.

### Application and compatibility boundary

The MM4 does not provide the MM3 radio-clock function. Where both large shared memory and a buffered clock are required, the supported combined arrangement uses the appropriate separate modules and configuration.

### Available unit verification

Check the MM4 marking, bus connectors and system-error relay connection. Confirm coupling memory module and any separately listed terminal accessories in the quotation. Evaluate the actual interfaces and included accessories against the installed arrangement.

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## 04 Engineering selection decisions

### Why does the rack position of 6DD1611-0AG0 matter to a multi-CPU system?

It must sit between the first and second CPU modules. Preserve that bus relationship rather than moving the module to an arbitrary unused slot.

### Does 6DD1611-0AG0 include the radio clock associated with MM3 hardware?

No. MM4 supplies the larger shared memory and system-error relay; the MM3 clock is a separate function and must not be assumed to exist on MM4.

### Can 6DD1611-0AG0 replace a memory module without checking the configured CPU connections?

No. Review the shared-data organization and each CPU connection, including any common access to a communications interface.

### What should be checked if data is lost from 6DD1611-0AG0 after a power interruption?

Inspect the subrack backup path and battery condition in addition to the normal supply. A healthy powered rack does not prove backup continuity.

### How should the error relay on 6DD1611-0AG0 be incorporated into an external circuit?

Respect its voltage, current and switching-power limits simultaneously, and validate the contact interpretation against the system fault response.

### Why is a memory-capacity comparison insufficient when selecting 6DD1611-0AG0?

Bus interfaces, slot relationship, backup behavior and configured data exchange also determine suitability. Capacity alone does not establish interchangeability.

### What test can reveal an incomplete data path through 6DD1611-0AG0?

Exercise the configured exchanges between the relevant CPUs and check that each receiver obtains the intended changing values, rather than accepting only a healthy power indication.

### How should an MM3 and 6DD1611-0AG0 be assessed when both are installed?

Confirm that the application uses MM4 for shared memory and the separate MM3 for its supported clock function; retain the intended CPU configuration for both roles.

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## 05 Configuration boundary and references

### Lifecycle and support

The current manufacturing and manufacturer-support stage for this exact model is not confirmed.

### Replacement assessment

Match the MM4 shared-memory role, CPU placement, configured connections and backup arrangement. Do not transfer MM3 clock capabilities to this module.

### Available unit details

Offered reference: Siemens 6DD1611-0AG0. Condition: New, original product. Check the MM4 marking, bus connectors and system-error relay connection. Confirm coupling memory module and any separately listed terminal accessories in the quotation; companion equipment is included only when itemized.

Document roles: The original excerpt retains manufacturer tables and drawings; this brief addresses selection and system context; the separate installation guide covers field procedures and acceptance.

Technical basis: SIMADYN D Hardware, Coupling Memory Modules.

### Related product navigation

- Siemens 6ES7511-1AK02-0AB0
- Siemens 6ES7405-0KA02-0AA0
- Siemens 6ES7214-1BG40-0XB0

Related products for separate application assessment; verify the platform and interface. No interchangeability is implied.

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