Siemens 6DD1611-0AG0

01 Map the CPU memory relationship

Record the first and second CPU positions, MM4 placement and configured interprocessor connections. Identify any communication interface accessed by more than one CPU.

Cpu memory relationship — acceptance

The shared-memory position and each data producer and consumer are known before removal.

Stage record	Map the CPU memory relationship observations and acceptance result
Shared-memory capacity	2 × 2 MB
Backplane interfaces	L bus and C bus

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02 Protect the retained data

Back up the configuration and assess the subrack battery path. Plan the power interruption with the process secured and preserve any application values required for restart.

Retained data — acceptance

Recovery does not depend solely on the old memory module or an unchecked backup battery.

Stage record	Protect the retained data observations and acceptance result
Occupied slots	1
Supply voltage	5 V DC nominal

03 Restore bus and relay connections

Follow the SIMADYN D installation procedure, preserving the MM4 slot relationship and connector engagement. Reinstate the system-error contact wiring without changing its external load.

Bus and relay connections — acceptance

Both backplane interfaces and the error-contact circuit match the approved rack configuration.

Stage record	Restore bus and relay connections observations and acceptance result
Supply tolerance	4.75–5.25 V
Typical current	800 mA

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04 Exercise CPU data exchange

Start the system under controlled conditions and stimulate the configured data exchanges. Check changing values at every intended receiver and review associated CPU diagnostics.

Cpu data exchange — acceptance

The application demonstrates complete producer-to-consumer communication without stale or misassigned data.

Stage record	Exercise CPU data exchange observations and acceptance result
System-error contact voltage	60 V DC maximum
System-error contact current	0.5 A maximum

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05 Verify fault and recovery behavior

Test the approved system-error response and the intended restart sequence. Record the final rack map, communication checks and backup condition for the maintenance handover.

Fault and recovery behavior — acceptance

Shared data and the error indication behave consistently with the application design after restoration.

Technical basis: SIMADYN D Hardware, Coupling Memory Modules.

Related product navigation

- Siemens 6ES7134-4GB11-0AB0
- Siemens 6ES7193-6AR00-0AA0
- Siemens 6ES7235-0KD22-0XA0

Related products for separate application assessment; verify the platform and interface. No interchangeability is implied.