Spectrum Controls 1746SC-CTR4

01 Class III allocation

1 Class III allocation procedure

Use this stage for 1746SC-CTR4 at its assigned installation position. Record processor type, slot, module ID and the sixteen-word input/output map. Back up configuration before disabling or removing the counter module.

2 Acceptance of class iii allocation

The processor supports Class III operation and the slot is configured for CTR4 rather than CTR8. Record the observed result and any unresolved difference before progressing to the next applicable stage.

Stage record	Class III allocation observations and acceptance result
Differential inputs	4
Quadrature channels	2

02 Pulse-source matching

1 Pulse-source matching procedure

Compare source level, pulse width, common-mode conditions and channel grouping. Verify quadrature pairing or external-enable wiring before applying a signal.

2 Acceptance of pulse-source matching

Each source is assigned to the intended counting or quadrature function with compatible electrical levels. Record the observed result and any unresolved difference before progressing to the next applicable stage.

Stage record	Pulse-source matching observations and acceptance result
External enable inputs	2
Maximum input frequency	50 kHz

03 Filter and scaling setup

1 Filter and scaling setup procedure

Restore the selected input filter, numeric range and K/M/R scaling. Keep raw pulse checks separate from the engineering-unit conversion.

2 Acceptance of filter and scaling setup

Representative pulses are accepted without loss and the converted count or rate has the intended units. Record the observed result and any unresolved difference before progressing to the next applicable stage.

Stage record	Filter and scaling setup observations and acceptance result
Selectable DC levels	5, 12 or 24 V DC
Module ID	10200

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04 Limits and timing test

1 Limits and timing test procedure

Exercise enable, reset, preset and relevant alarm or overflow states using controlled input conditions. Include the selected rate-mode response in the test.

2 Acceptance of limits and timing test

The program handles status flags, limits and update timing without sign or rollover errors. Record the observed result and any unresolved difference before progressing to the next applicable stage.

Stage record	Limits and timing test observations and acceptance result
Image allocation	16 input and 16 output words
Rack current, 5 V	175 mA

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05 Application acceptance

1 Application acceptance procedure

Compare module image data with the machine outcome, then remove test forcing and restore the approved operating sequence. Retain the final slot and channel records.

2 Acceptance of application acceptance

The controller acts correctly on the measured values and the module is no longer in a test configuration. Record the observed result and any unresolved difference before progressing to the next applicable stage.

Technical basis: Spectrum Controls SLC 500 Counter/Flowmeter Input Module User Manual, 0300181-09 Rev. A0.

Related product navigation

- Allen-Bradley 1746-NI8
- Allen-Bradley 1746-NO4I
- Allen-Bradley 1746-OB16

Related products for separate application assessment; verify the platform and interface. No interchangeability is implied.