B&R X20CP1484

01 X20 Controller CPU / system role

Product function and operating context

B&R X20CP1484 combines application execution with a separate I/O processor so that the configured machine logic can exchange data through local X2X and real-time POWERLINK paths.

The CPU uses Celeron-266-compatible processing with 32 MB DRAM and 1 MB SRAM. A removable CompactFlash card holds the application arrangement and is a separate selection; its contents are part of the installed controller configuration.

Functional path

CompactFlash application	CPU and I/O processor	POWERLINK/X2X interfaces	Configured machine I/O

Processing	Celeron 266 compatible with I/O processor
Memory	32 MB DRAM and 1 MB SRAM
Connectivity	Ethernet, POWERLINK, X2X and serial

One modular interface slot supplements the built-in RS232, 10/100 Ethernet, POWERLINK V1/V2, two USB 1.1 ports and X2X Link master. Ethernet and POWERLINK have distinct roles even though both use RJ45 connections.

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02 Technical operating envelope

Processor class	Celeron 266 compatible
DRAM	32 MB
SRAM	1 MB
Application storage	Removable CompactFlash
Modular interface slots	1
Ethernet	10/100 Mbit/s

POWERLINK	V1/V2, 100 Mbit/s
USB ports	2 × USB 1.1 Type A
Serial interface	RS232, up to 115.2 kbit/s
Local I/O interface	X2X Link master
Supply voltage	24 V DC, −15%/+20%
Cooling	Fan-free

Values apply to X20CP1484, the 32 MB DRAM variant. The X20CP1484-1 memory configuration and other processor variants must not be substituted.

Operating interpretation

The separate I/O processor and processor-class rating do not establish an unconditional machine-cycle time; the application and communication load still determine execution behavior.

Configuration boundary

Retain the CompactFlash image, exact 32 MB CPU variant, interface option, supply arrangement and network roles when replacing the controller.

How many additional interface modules can X20CP1484 accept?

It provides one modular interface slot. The three-slot arrangement of an X20CP348x controller does not apply.

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03 System integration and applications

X20 machine control

Execute the approved application with local X2X I/O.

POWERLINK systems

Exchange configured real-time process data through the dedicated POWERLINK interface.

Ethernet integration

Connect the controller's separate Ethernet service to the engineered network.

Modular communication extension

Use the single option slot for a supported additional interface.

System and supply context

B&R X20CP1484 operates within the following defined system context. Check X20CP1484 without the -1 suffix and retain the application card identity.

Application boundary

The applications below describe its technical role. Select associated hardware and configuration for the actual installation.

Where is the removable application storage for X20CP1484?

The controller uses a CompactFlash slot. The card and its approved application contents must be checked separately from the CPU hardware.

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04 Engineering selection decisions

Does X20CP1484 have the same DRAM as X20CP1484-1?

No. X20CP1484 has 32 MB DRAM, while the -1 variant has a different 64 MB configuration.

Can the Ethernet and POWERLINK ports of X20CP1484 be interchanged?

No. Their communication roles and configurations differ, despite similar RJ45 connectors.

Which interface connects local X20 I/O to X20CP1484?

The integrated X2X Link master provides the local I/O communication path.

What does an OPERATIONAL POWERLINK state mean for X20CP1484?

It indicates that PDO mapping is active and cyclic data is being evaluated. Earlier ready states do not establish the same process-data behavior.

Confirm POWERLINK operation	Check that the network reaches the required OPERATIONAL state and cyclic data is evaluated. Investigate an earlier ready state before relying on process-data exchange.
Retain the tested controller image	Save the card image, interface option, network settings and I/O results. Keep the exact X20CP1484 identity with the tested machine configuration.

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05 Configuration boundary and references

Lifecycle and support

The current manufacturing and factory-support phase of X20CP1484 is unconfirmed. Apter Power availability is assessed separately from manufacturer lifecycle status.

Replacement assessment

Retain the CompactFlash image, exact 32 MB CPU variant, interface option, supply arrangement and network roles when replacing the controller.

Document roles

Use the original manufacturer excerpt for its preserved technical tables and drawings. This product brief explains selection and system context. The separate installation guide covers preparation, connection, tests and handover; use it together with the applicable manufacturer instructions.

Technical basis

B&R X20CP148x and X20CP348x Data Sheet, V2.16.

Related product navigation

- B&R 8BVI0055HWD0.000-1
- B&R 8BVI0014HWD0.000-1

These are related references, not confirmed substitutes or an automatically approved assembly.

Product enquiries