B&R X20CP1484

01 Original CPU identity and application storage

Identify the unsuffixed CPU

Check X20CP1484 and its 32 MB DRAM configuration. Do not select the -1 memory variant or a three-slot CPU merely because the family designation is similar.

Back up the CompactFlash application

Retain the approved card image and controller project before service. Identify the card separately from the CPU so the intended runtime and application are restored together.

Processor class	Celeron 266 compatible
DRAM	32 MB
SRAM	1 MB
Application storage	Removable CompactFlash

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02 Communication ports and DC supply

Map the communication ports

Label Ethernet, POWERLINK, RS232, USB and X2X connections according to their actual roles. Similar connectors do not make the communication paths interchangeable.

Check the two supply functions

Verify the CPU/X2X supply and the separately protected I/O feed. Apply the 24 V −15%/+20% range and the required protection for the installed station.

Ethernet	10/100 Mbit/s
USB ports	2 × USB 1.1 Type A
Serial interface	RS232, up to 115.2 kbit/s
Supply voltage	24 V DC, −15%/+20%

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03 Interface option and network settings

Restore the interface option

Record the module in the single expansion slot and its project configuration. Preserve the assigned interface function when moving to the replacement CPU.

Verify network parameters

Restore the Ethernet and POWERLINK settings appropriate to each port. Confirm addresses and network role before reconnecting the controller to the operating machine.

Modular interface slots	1
Local I/O interface	X2X Link master

04 Runtime, storage and POWERLINK behavior

Observe runtime and storage status

Check CPU, CompactFlash and battery indications at startup. A powered controller must also load the intended application successfully before output operation is accepted.

Confirm POWERLINK operation

Check that the network reaches the required OPERATIONAL state and cyclic data is evaluated. Investigate an earlier ready state before relying on process-data exchange.

POWERLINK	V1/V2, 100 Mbit/s
Cooling	Fan-free

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05 Local I/O tests and retained image

Exercise X2X and serial paths

Test representative local I/O and the installed RS232 endpoint with outputs controlled. Confirm that both communication and application mapping are restored.

Retain the tested controller image

Save the card image, interface option, network settings and I/O results. Keep the exact X20CP1484 identity with the tested machine configuration.

Technical basis: B&R X20CP148x and X20CP348x Data Sheet, V2.16.

Related product navigation

B&R 5ACPCI.ETH3-01

B&R 5AC901.BUPS-01

These are related references, not confirmed substitutes or an automatically approved assembly.