Yaskawa JEPMC-CP200

01 Motion Controller CPU / system role

Product function

Yaskawa JEPMC-CP200 executes the MP920 control application and coordinates the selected I/O and motion modules through the system mounting-base architecture.

Selected configuration

CPU-01 uses 2 MB flash and 2 MB battery-backed SRAM. The battery maintains SRAM data, while the FLASH mode switch controls copying the flash program into RAM during the applicable startup sequence.

Functional path

MP920 application	CPU-01 execution	Mounting-base modules	Machine I/O and motion

Memory	2 MB flash and 2 MB SRAM
Communications	Two RS-232C ports
Diagnostics	Separate system, program, battery and port states

Two female 9-pin RS-232C ports support MEMOBUS, no-protocol and MELSEC communication at 9.6, 14.4 or 19.2 kbit/s. Their pin assignments are specific to this controller and must not be assumed to match a standard PC serial cable.

JEPMC-CP200

02 Technical operating envelope

Flash memory	2 MB
SRAM	2 MB, battery backed
Current consumption	980 mA
Serial ports	2 × RS-232C, female 9-pin D-sub
Serial rates	9.6 / 14.4 / 19.2 kbit/s

Serial protocols	MEMOBUS; no protocol; MELSEC
Status-contact behavior	Closed normally; open on abnormal operation
Status-contact rating	0.5 A at 24 V DC; 0.5 A at 125 V AC
Mode control	8-position DIP switch
Module status	RDY, RUN, ALM, ERR, BAT ALM, PRT1, PRT2

CPU-01 JEPMC-CP200 specifications apply. CPU-02 memory and current values do not apply to this model; motion capability depends on the installed MP920 modules and program.

Operating interpretation

The two serial ports use special pin assignments. Pin 7 is signal ground; using a PC-style cable without checking the wiring can prevent communication or misconnect signals.

Configuration boundary

Preserve CPU-01 identity, MP920 application, module configuration, battery-backed data and the flash/RAM startup selection together.

How are RDY and RUN different on JEPMC-CP200?

RDY indicates normal system operation, while RUN indicates execution of the user program. They describe different controller states.

JEPMC-CP200

03 System integration and applications

MP920 machine control

Execute the configured machine application through the MP920 modular architecture.

Serial operator interfaces

Exchange data with a correctly wired and configured MEMOBUS or other supported serial endpoint.

Program and data retention

Use flash and battery-backed SRAM according to their distinct startup and retention roles.

Machine-status supervision

Integrate the RDY-related status contact and front-panel diagnostics into service checks.

System and supply context

Yaskawa JEPMC-CP200 operates within the following defined system context. Match JEPMC-CP200 CPU-01 and the installed application/module configuration.

Application boundary

The applications below describe its technical role. Select associated hardware and configuration for the actual installation.

Can a generic PC serial cable be assumed suitable for JEPMC-CP200?

No. Its two 9-pin RS-232C connectors use specific pin assignments, including signal ground on pin 7. Use the applicable wiring arrangement.

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04 Engineering selection decisions

Which memory in JEPMC-CP200 depends on battery backup?

The 2 MB SRAM is battery backed. The 2 MB flash provides separate nonvolatile program storage.

What does the FLASH switch do on JEPMC-CP200?

When selected for the applicable startup, it copies the program from flash memory into RAM. It is not an instruction to save the current RAM program to flash.

What is the normal state of the RLY OUT contact on JEPMC-CP200?

The status contact is closed during normal operation and opens during abnormal operation, within its specified switching rating.

Do all DIP-switch changes on JEPMC-CP200 take effect immediately?

No. Most switches take effect at power-on or startup after reset; local and master reset have their own behavior. Apply the prescribed mode-change procedure.

Observe the startup indications	Check RDY, RUN, ALM, ERR and BAT ALM separately. Determine whether the system is ready and whether the intended user program is actually running.
Retain the restored image and state	Save the tested program image, port settings, switch positions, battery status and RLY OUT behavior. Keep these details with the exact JEPMC-CP200 service record.

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05 Configuration boundary and references

Lifecycle and support

The current manufacturing and factory-support phase of JEPMC-CP200 is unconfirmed. Apter Power availability is assessed separately from manufacturer lifecycle status.

Replacement assessment

Preserve CPU-01 identity, MP920 application, module configuration, battery-backed data and the flash/RAM startup selection together.

Document roles

Use the original manufacturer excerpt for its preserved technical tables and drawings. This product brief explains selection and system context. The separate installation guide covers preparation, connection, tests and handover; use it together with the applicable manufacturer instructions.

Technical basis

Yaskawa MP920 Machine Controller Design and Maintenance Manual, SIEZ-C887-2.1.

Related product navigation

- Yaskawa CPCR-FR01RB-T3L
- Siemens 6ES7214-1AD23-0XB0

These are related references, not confirmed substitutes or an automatically approved assembly.

Product enquiries