Yaskawa JEPMC-CP200

01 MP920 application and switch state

1 Preserve the MP920 application

Back up the program, parameters and retained data before CPU service. Distinguish the flash image from the battery-backed SRAM state so that a startup copy cannot restore an unintended version.

2 Record the DIP-switch positions

Capture all eight switch positions, including FLASH, MULTI and RUN. Do not treat initialization or reset switches as ordinary operating selections.

Flash memory	2 MB
SRAM	2 MB, battery backed
Mode control	8-position DIP switch

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02 Battery retention and cable assignment

1 Inspect the backup battery

Check BAT ALM and the approved battery-service procedure. Maintain the required data protection before interrupting the SRAM backup circuit.

2 Verify the serial cable pinout

Compare both ends of each RS-232C cable with the CPU-01 pin assignments. A standard 9-pin connector shape does not prove electrical compatibility.

SRAM	2 MB, battery backed
Serial ports	2 × RS-232C, female 9-pin D-sub

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03 Serial protocol and status-contact load

1 Restore port communication settings

Match baud rate and the selected MEMOBUS, no-protocol or MELSEC mode at both endpoints. Keep programming and operator-interface connections clearly identified.

2 Check RLY OUT loading

Verify that the external status circuit remains within 0.5 A at the applicable 24 V DC or 125 V AC rating. Confirm its intended response to an open abnormal-status contact.

Serial rates	9.6 / 14.4 / 19.2 kbit/s
Serial protocols	MEMOBUS; no protocol; MELSEC
Status-contact behavior	Closed normally; open on abnormal operation
Status-contact rating	0.5 A at 24 V DC; 0.5 A at 125 V AC

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04 Module seating and startup indications

1 Seat the CPU on the mounting base

Align the mounting-base locating features and connector before tightening the module fixings. Retain the approved module arrangement and avoid forcing a misaligned connection.

2 Observe the startup indications

Check RDY, RUN, ALM, ERR and BAT ALM separately. Determine whether the system is ready and whether the intended user program is actually running.

Current consumption	980 mA
Module status	RDY, RUN, ALM, ERR, BAT ALM, PRT1, PRT2

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05 Motion response and retained controller image

1 Verify module and motion response

Use the approved machine test to check I/O and motion-module communication with outputs controlled. A CPU communication response alone does not establish correct machine motion.

2 Retain the restored image and state

Save the tested program image, port settings, switch positions, battery status and RLY OUT behavior. Keep these details with the exact JEPMC-CP200 service record.

Technical basis: Yaskawa MP920 Machine Controller Design and Maintenance Manual, SIEZ-C887-2.1.

Related product navigation

Siemens 6ES7214-1HG40-0XB0

Siemens 6ES7215-1HG40-0XB0

These are related references, not confirmed substitutes or an automatically approved assembly.