01 / Product and system dashboard

HONEYWELL

K2LCN-8 51401551-801

LCN processor board

Product role

Honeywell K2LCN-8 51401551-801 provides the LCN node-processing function in a matched legacy control-system configuration.

K2LCN
Node processing

>

LCN I/O
Network interface

>

Host system
Configured node service

Engineering scope

The identity and address-selection information applies to K2LCN-8 51401551-801. Host-slot and peripheral relationships describe the WDA History Module arrangement; other hosts require their own installation mapping.

02 / Verified identity and technical relationships

Property	Applicable fact	Property	Applicable fact
Processor family	K2LCN	Removed jumper	Binary 1
Function	LCN node processing	Address parity	Odd, including parity position
Address block	TS2	History Module slot	Front slot 1; WDA arrangement
Board coordinate	9C	Address selection	Processor or LCN I/O board, not both
Address weighting	1, 2, 4, 8, 16, 32, 64		

Configuration boundary

Match the complete hardware marking and installed configuration to the applicability stated in this product summary.

03 / Applications and system integration

History-node processing

Maintain the configured LCN node processing path used by the host History Module; disks and peripheral controllers remain separate assemblies.

Network addressing

Retain the assigned LCN address and the single active address-selection location in the installed node.

Host restoration

Match the processor board, memory arrangement, I/O board and software personality before restoring the node.

Controlled maintenance

Record the existing jumpers and host configuration, then verify node communication after replacement.

Functional context

The TS2 address block is located at board coordinate 9C. Its seven weighted positions represent 1 through 64 in binary weighting. Removing a jumper sets a binary 1, and the total count of removed positions, including parity, must be odd. Preserve the installed node address rather than copying an example setting.

04 / Service and installation decisions

Preserve complete identity

Match the full part number, board revision and installed node personality before replacement.

Record address configuration

Record all binary address and parity positions before removing the original board.

Use one address location

Select the address on one approved board only; neutralize the unused selection location as specified for that hardware.

Match memory hardware

Confirm the required memory assembly separately; do not infer supplied memory from a related processor designation.

Isolate and protect

Follow the host shutdown procedure and electrostatic handling controls before extraction.

Verify node recovery

Check node identity, network communication and intended host services before returning the system to normal operation.

05 / Reference basis and related portfolio

Lifecycle and supply

A current manufacturer lifecycle phase has not been confirmed. Request current support and replacement guidance using the complete K2LCN-8 51401551-801 identity. Apter Power availability is confirmed separately in the quotation.

Reference basis

Honeywell History Module Service, HM13-501, December 2002; Dual Node Module Service, LC13-510, September 1995.

Apter Power technical summary. Apply the original instructions for the actual installation.

Related industrial automation products

Navigation only; no substitution or compatibility claim.

Honeywell SPS5785 51198651-100

Honeywell 51304386-150 MC-PPIX02

Honeywell 51305072-400

Discuss your installed configuration

Send the full unit marking and connection details.

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