01 / Identity and job scope

HONEYWELL

51401946-100

LCN processor board

Product role

Honeywell 51401946-100 provides the LCN node-processing function in a matched legacy control-system configuration.

K4LCN
Node processing

>

LCN I/O
Network interface

>

Host system
Configured node service

Applicability

The identity and address-selection information applies to 51401946-100. Host-slot and peripheral relationships describe the WDA History Module arrangement; other hosts require their own installation mapping.

Hardware and configuration

K4LCN provides the LCN-side processing function in the WDA History Module architecture. The module also uses a separate Smart Peripheral Controller and drive-interface hardware for its storage path. The 51401946-100 processor board and its daughter-memory assembly are separate service items. The bare board number does not establish the installed memory capacity or the inclusion of a daughterboard.

Planning basis

This Apter Power guide supports identification and engineering planning. Apply the complete manufacturer and machine-builder procedures for the actual installation.

02 / Configuration and interface readiness

Item	Configuration relationship
Processor family	K4LCN
Function	LCN node processing
Memory assembly	Separate daughterboard
Address block	TS2
Board coordinate	21E
Address weighting	1, 2, 4, 8, 16, 32, 64
Removed jumper	Binary 1

Technical context

The TS2 address block is located at board coordinate 21E. Its seven weighted positions represent 1 through 64 in binary weighting. Removing a jumper sets a binary 1, and the total count of removed positions, including parity, must be odd. Preserve the installed node address rather than copying an example setting.

What does a removed address jumper mean on 51401946-100?

A removed jumper represents binary 1. The weighted address positions are 1, 2, 4, 8, 16, 32 and 64.

What parity rule applies to address selection on 51401946-100?

The total number of removed jumpers, including the parity position, must be odd.

03 / Installation and connection decisions

Preserve complete identity

Match the full part number, board revision and installed node personality before replacement.

Record address configuration

Record all binary address and parity positions before removing the original board.

Use one address location

Select the address on one approved board only; neutralize the unused selection location as specified for that hardware.

Match memory hardware

Confirm the required memory assembly separately; do not infer supplied memory from a related processor designation.

Installation context

The LCN address can be selected on the processor or on the associated LCN I/O board, with only one location active. The unused selection must be neutralized according to its jumper or switch implementation. In the WDA History Module, the processor occupies front slot 1 and the matching I/O board sits in the rear card cage; mechanical compliance, memory and software configuration must remain compatible.

Can both boards select the LCN address for 51401946-100?

Use a single approved address-selection location. When the processor selects the address, remove the LCN I/O jumpers or set the CLCN switches to zero as specified for the installed hardware.

Which slot is used by 51401946-100 in a WDA History Module?

The documented WDA History Module arrangement places the processor in front slot 1. This is a host-specific installation relationship, not a universal slot rule.

04 / Functional acceptance planning

Application	Controlled review
History-node processing	Maintain the configured LCN node processing path used by the host History Module; disks and peripheral controllers remain separate assemblies.
Network addressing	Retain the assigned LCN address and the single active address-selection location in the installed node.
Host restoration	Match the processor board, memory arrangement, I/O board and software personality before restoring the node.
Controlled maintenance	Record the existing jumpers and host configuration, then verify node communication after replacement.

What memory boundary should be retained for 51401946-100?

The 51401946-100 processor board and its daughter-memory assembly are separate service items. The bare board number does not establish the installed memory capacity or the inclusion of a daughterboard.

Does 51401946-100 include the complete disk interface?

No. The History Module uses separate peripheral-controller and drive-interface assemblies. Match those components and the host configuration independently.

Release boundary

Check node identity, network communication and intended host services before returning the system to normal operation.

Record
Keep the original configuration

>

Test
Observe controlled response

>

Release
Authorize normal operation

05 / Technical handover and support

Retained information

Keep the complete 51401946-100 marking, host identity, connection photographs, configuration backup and controlled-test results in the service record.

Support planning

Confirm current support and replacement guidance with Honeywell for the installed identity and configuration. Apter Power availability and dispatch are separate quotation matters.

Reference basis

Honeywell History Module Service, HM13-501, December 2002.

Apter Power technical summary. Apply the original instructions for the actual installation.

Related industrial automation products

Navigation only; no substitution or compatibility claim.

Honeywell K2LCN-4 51402615-400

Honeywell K2LCN-8 51401551-801

Honeywell MU-KBFT02 80366198-200

Discuss your installed configuration

Send the full unit marking and connection details.

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