Table 8-44. Redundant Analog Input Sets for S100 I/O

Description	Consists of	Article No.
With redundancy: 32 channels, resolution 12 bits, single- ended 0 to +10 V or 0 to +20 mA, shunt 250 Ω 0.05%	2 x DSAI 133 2 x DSTA 002A 2 x DSTK 222L3 or 2 x DSAI 133A 2 x DSTA 002B 2 x DSTK 222L3	3BSE019915R1

Table 8-45. Analog Output Sets for S100 I/O

Description	Consists of	Article No.
4 channels, resolution 12 bits, 0 to ±10 V or 0 to ±20 mA, galvanic isolation	DSAO 110 DSTA 160 DSTK 223L3	3BSE019916R1
8 channels, resolution 12 bits, 0 to ±10 V or 0 to ±20 mA	DSAO 120 DSTA 170 DSTK 223L3	3BSE019917R1
16 channels, resolution 8 bits, 0 to +10 V or 0 to +20 mA	DSAO 130 DSTA 180 DSTK 221L3	3BSE019918R1
8 channels, resolution 12 bits, 0 to ±10 V or 0 to ±20 mA, galvanic isolation	DSAO 120A DSTA 171 DSTK 221L3	3BSE020419R1
16 channels, resolution 12 bits, 0 to +20 mA	DSAO 130A DSTA 181 DSTK 221L3	3BSE020420R1

Table 8-46. Analog Input/Output Sets for S100 I/O

Description	Consists of	Article No.
8 input channels, resolution 12 bits, single-ended, 0 to +10 V or 0 to +20 mA, shunt 250 Ω 0.05% **8 output channels, resolution 12 bits**, 0 to +20 mA or 0 to +10 V (over shunt 500 Ω 0.1% on DSTA 001A or DSTA 001B)	DSAX 110 DSTA 001A DSTK 223L3 or DSAX 110A DSTA 001B DSTK 223L3	3BSE019919R1