B&R 3CP360.60-1

01 Function and selected configuration

Product function

B&R 3CP360.60-1 connects its processor, retained application and network interfaces to the automation task. Correct operation depends on retaining the installed hardware selection and the application settings together.

Selected configuration

The processor operates in the SYSTEM 2005 main rack beside the supply, occupying slots 3 and 4. Its floating-point unit and background I/O processor support control execution, while the single aPCI slot accommodates a supported IF7xx interface module.

Functional path

Application settings	CPU Module	Pentium; 266 MHz / configured interface	Diagnostics / process value

Processor	Pentium; 266 MHz
DRAM	32 MB
User SRAM	496 kB

Memory includes 32 MB DRAM, 496 kB user SRAM and 256 kB for retained variables. CompactFlash stores the application and must be selected separately. Battery monitoring and CPU, CompactFlash and Ethernet indications distinguish retention and operating conditions.

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02 Technical operating envelope

Processor	Pentium; 266 MHz
DRAM	32 MB
User SRAM	496 kB
Retentive variables	256 kB
L2 cache	512 kB
Interface expansion	1 aPCI slot for IF7xx

Ethernet	10/100 Mbit/s; RJ45
Serial interface	RS232; up to 115.2 kbit/s
USB	1 USB 1.1 Type A
Basic power	14 W; excludes card/interface module
Minimum task cycle	400 microseconds
Operating temperature	0 to 60 degrees C

Software representation does not redefine converter resolution or processor timing. Apply the exact revision and operating-mode restrictions with the installed load or sensor arrangement.

Signal interpretation

Memory includes 32 MB DRAM, 496 kB user SRAM and 256 kB for retained variables. CompactFlash stores the application and must be selected separately. Battery monitoring and CPU, CompactFlash and Ethernet indications distinguish retention and operating conditions.

Interface boundary

The built-in interfaces comprise an RS232 programming/application connection, isolated 10/100 Mbit/s Ethernet and USB 1.1. The basic 14 W consumption excludes additional memory-card and interface-module loads. Preserve the application, runtime and communication assignments when replacing the CPU.

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03 System integration and applications

Configured machine function

The processor operates in the SYSTEM 2005 main rack beside the supply, occupying slots 3 and 4. Its floating-point unit and background I/O processor support control execution, while the single aPCI slot accommodates a supported IF7xx interface module.

Interface continuity

Restore the RS232, Ethernet, USB and optional aPCI connections by their retained identity. Keep Ethernet isolation and the non-isolated serial/USB reference arrangement in mind.

Diagnostic acceptance

Test the required Ethernet and serial paths and compare application cycle behavior with the retained baseline. Processor clock alone is not a complete cycle-time test.

Controlled restoration

Verify the affected I/O exchanges and interlocks with the machine in its approved test state. Do not release the system on a successful network connection alone.

System and supply context

3CP360.60-1 operates within its compatible B&R rack or X20 arrangement. Storage, bus modules, terminal blocks and field devices are separate unless included in the quotation.

Application boundary

Apply the exact hardware and software configuration to the assigned machine function. A similar connector or base family is insufficient to establish replacement compatibility.

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04 Engineering selection decisions

Where can 3CP360.60-1 be installed in a SYSTEM 2005 rack?

It uses main-rack slots 3 and 4 beside the supply. It is not an expansion-rack CPU installation.

Which memory in 3CP360.60-1 holds user and retained data?

User SRAM is 496 kB and the retained-variable capacity is 256 kB. These are distinct from its 32 MB DRAM.

How fast is the Ethernet interface on 3CP360.60-1?

It supports 10/100 Mbit/s using a shielded RJ45 connection, with a maximum 100 m segment between stations.

What is the shortest task-class cycle supported by 3CP360.60-1?

The shortest task-class cycle is 400 microseconds. Complete application timing also depends on program and I/O workload.

Restore the storage selection	Fit the supported CompactFlash containing the correct application and runtime. A blank or unrelated card does not establish a valid replacement configuration.
Verify boot and retention status	Observe CPU, battery and CompactFlash indicators after controlled energization. Resolve boot or retention faults before enabling process outputs.

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05 Configuration boundary and references

Lifecycle and support

The current manufacturing and factory-support phase of 3CP360.60-1 is not confirmed. Apter Power availability is assessed separately from manufacturer lifecycle status.

Replacement assessment

Preserve the complete model, actual hardware revision, installed interfaces and application settings. Validate the required function before releasing automatic operation.

Document roles

Use the original manufacturer excerpt for its preserved technical tables and drawings. This product brief explains selection and system context. The separate installation guide covers preparation, connection, tests and handover; it does not replace manufacturer instructions.

Technical basis

B&R SYSTEM 2005 User Manual V4.0, CP340/CP360/CP380/CP382 technical extract.

Related product navigation

- B&R 3CP340.60-2
- B&R X20CP1486

These are related references, not confirmed substitutes or an automatically approved assembly.

Product enquiries