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01 Define the service boundary

1 Preserve the executing project

Save the control application, Automation Runtime identity, interface assignments and network settings. Identify the removable CompactFlash used by the installed machine.

2 Record retained-data requirements

Identify the variables that must survive replacement and the backup-battery condition. Retentive data requirements are separate from a copy of the executable project.

Processor	Pentium; 266 MHz
DRAM	32 MB
User SRAM	496 kB

02 Prepare the installation

1 Check the main-rack allocation

Confirm slots 3 and 4 are assigned to the CP360 and verify the adjacent supply. Include memory-card and aPCI loads beyond the basic 14 W CPU consumption.

2 Prepare safe CPU exchange

Place the controlled equipment in its approved maintenance state and isolate according to the system procedure. Protect the CPU and card from electrostatic discharge.

Retentive variables	256 kB
L2 cache	512 kB
Interface expansion	1 aPCI slot for IF7xx

03 Restore connections and settings

1 Restore the storage selection

Fit the supported CompactFlash containing the correct application and runtime. A blank or unrelated card does not establish a valid replacement configuration.

2 Reconnect each interface

Restore the RS232, Ethernet, USB and optional aPCI connections by their retained identity. Keep Ethernet isolation and the non-isolated serial/USB reference arrangement in mind.

Ethernet	10/100 Mbit/s; RJ45
Serial interface	RS232; up to 115.2 kbit/s
USB	1 USB 1.1 Type A

04 Verify the operating response

1 Verify boot and retention status

Observe CPU, battery and CompactFlash indicators after controlled energization. Resolve boot or retention faults before enabling process outputs.

2 Confirm communication and timing

Test the required Ethernet and serial paths and compare application cycle behavior with the retained baseline. Processor clock alone is not a complete cycle-time test.

Functional test	Known input or controlled output
Error response	Status and application reaction agree

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05 Release and retain the evidence

1 Exercise actual control functions

Verify the affected I/O exchanges and interlocks with the machine in its approved test state. Do not release the system on a successful network connection alone.

2 Retain the recovered baseline

Record the CPU, card, runtime, interface module and test results. Keep the verified project and required retained-data backup with the machine record.

Technical basis: B&R SYSTEM 2005 User Manual V4.0, CP340/CP360/CP380/CP382 technical extract.

Related product navigation

B&R X20CP1485

B&R X20CP1484-1

These are related references, not confirmed substitutes or an automatically approved assembly.