Product dashboard

GE IS200TTURH1B is a Mark VI primary turbine-protection input terminal board used with the VTUR protection processor. It terminates speed, potential-transformer and shaft-monitoring circuits used by the primary trip and synchronization functions.

Exact model	Product type	System family	Functional role
IS200TTURH1B	Primary Turbine Protection Input Terminal	Mark VI Primary Turbine Protection	Protection Input Board

PARAMETER	DOCUMENTED VALUE
Model	IS200TTURH1B
Platform	Mark VI
Function	Primary turbine-protection input terminal board
Associated processor	VTUR
Speed inputs	Passive magnetic pickup circuits
Voltage inputs	Generator and bus PT circuits
Shaft monitoring	Voltage and current circuits
Architecture	Simplex or TMR protection arrangements

Release basis: verify the exact identity, retained host relationship, connected interfaces and controlled functional result.

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IS200TTURH1B

System overview

The IS200TTURH1B forms the field-wiring boundary for the Mark VI primary turbine-protection chain, keeping speed, synchronization and shaft-monitoring signals organized before they reach VTUR.

Primary overspeed inputs

Terminates passive magnetic pickup circuits used by VTUR for turbine-speed protection.

Automatic synchronization

Carries generator and bus potential-transformer inputs used by synchronization functions.

Shaft monitoring

Routes shaft-voltage and shaft-current measurement circuits into the primary protection subsystem.

Simplex or TMR integration

Supports the documented VTUR cabling arrangement selected for the installed redundancy architecture.

SYSTEM POSITION	RECORD
Installed identity	IS200TTURH1B
Host relationship	Mark VI Primary Turbine Protection
Controlled function	termination of primary speed, synchronization and shaft-monitoring signals for VTUR

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IS200TTURH1B

Technical data map

PARAMETER	VERIFIED VALUE
Model	IS200TTURH1B
Platform	Mark VI
Function	Primary turbine-protection input terminal board
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Speed inputs	Passive magnetic pickup circuits
Voltage inputs	Generator and bus PT circuits
Shaft monitoring	Voltage and current circuits
Architecture	Simplex or TMR protection arrangements

CONTROL BAND	VERIFICATION RECORD
Identity	Exact model and complete label
Mechanical	Form, seating and retention
Interfaces	Power, wiring, communication and protection
Configuration	Settings, project and revision
Release	termination of primary speed, synchronization and shaft-monitoring signals for VTUR

Technical evidence record: GEH-6421 Mark VI Turbine Control System Guide, Volume II

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IS200TTURH1B

Applications and precautions

1. Confirm the IS200TTURH1B revision, VTUR processor and installed primary-trip-board arrangement.
2. Record every magnetic pickup, PT, shaft-monitoring and breaker-related terminal before isolation.
3. Capture the simplex or TMR cable map, including each JR, JS and JT connector used by the position.
4. Place the turbine and generator-breaker circuits in the approved maintenance state.
5. Protect keyed connectors, terminal-block labels and chassis-ground shield terminations during handling.
6. Restore pickup polarity, PT phase, shaft circuits, jumpers and relay wiring exactly as documented.
7. Review VTUR diagnostics, measured speed, PT values and synchronization permissives after energization.
8. Perform the controlled primary-trip and synchronization checks before service handover.

Complete each check against the installed record; hold release when any identity, connection, configuration or diagnostic result differs.

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IS200TTURH1B

Buyer and engineering FAQ

What is the purpose of GE IS200TTURH1B?

It is the Mark VI terminal board that brings primary turbine-protection field inputs to the VTUR processor.

Which processor is associated with GE IS200TTURH1B?

It is used with the VTUR primary turbine-protection processor in the documented Mark VI architecture.

Which speed signals terminate on GE IS200TTURH1B?

Passive magnetic speed-pickup circuits terminate on the board for turbine-speed measurement and overspeed protection.

Which voltage signals are handled by GE IS200TTURH1B?

The board terminates generator and bus potential-transformer circuits used by the synchronization functions.

Related Apter Power pages

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