Yokogawa ADV151-P00 S2 ATD5A-00

32-Channel Digital Input Module with KS Cable Interface Adapter

01 / SCOPE, COMPONENT REGISTER AND CHANNEL BASELINE

This guide restores the complete ADV151-P00 S2 and ATD5A-00 field-connection path while keeping the module, adapter, cable and external termination as separately identified components.

Capture every component tag → **Isolate field and system work** → **Restore the entire signal path**

Module register

Record ADV151-P00 S2, node, slot and redundant partner.

Adapter register

Record ATD5A-00, connector orientation and retention.

Cable register

Record AKB331 at both ends and its routed side.

Termination register

Record AED5D or relay-board type and channel position.

Channel record	Inputs 1-32 and normal states
Common record	Groups 1-16 and 17-32 with identical polarity
Diagnostic record	Node, module and channel status before work
Release record	Permitted tests, witnesses and every exception

Scope lock

Do not change the terminal-board architecture, cable type or point assignments as part of a replacement-only work order.

Decision gate	All four component identities and the complete signal path are known before isolation begins.

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02 / ISOLATION AND PATH-SPECIFIC LABELING

Control external 24 V DC

Identify each field source and approved isolation point before touching the adapter or board.

Label shared interfaces

A single adapter or cable serves many channels; mark it before disconnection.

Preserve common polarity

Both sixteen-channel groups must retain the same externally supplied polarity.

Use grounded ESD control

Protect the ADV151 backplane connection and all module/adapter cable pins.

ADV151 maximum input	30.0 V DC
Documented ON band	18 to 26.4 V DC
Documented OFF band	5.0 V DC or less
Module current	Maximum 500 mA at 5 V DC
Dedicated cable	AKB331
Adapter role	External KS cable interface

Unknown adapter tag	Stop before reconnecting the field path.
Unknown cable identity	Do not substitute a visually similar lead.
Mixed common polarity	Correct the wiring before energization.
Unapproved board change	Obtain engineering review and updated drawings.

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03 / CONTROLLED REMOVAL AND ASSEMBLY RESTORATION

1. Capture final state	Save node diagnostics and all used input states.
2. Place process in maintenance	Control affected logic and externally powered circuits.
3. Label the path	Mark module, slot, adapter, cable ends, board and common groups.
4. Disconnect under isolation	Remove shared connectors without straining pins or conductors.
5. Inspect every interface	Check backplane, adapter, cable and external board for damage.
6. Restore exact identities	Reinstall the approved components in their recorded positions.
7. Reconnect channels and commons	Return the signal path without crossing redundant sides.

Module acceptance

Complete tag, seating and node assignment match the baseline.

Adapter acceptance

ATD5A-00 tag, orientation and retention are correct.

Cable acceptance

AKB331 identity, routing and strain relief are intact.

Board acceptance

External termination and every channel/common position match the drawing.

Pre-energization hold point

Release field power only after a second-person check confirms the full component chain and same-polarity commons.

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04 / COMMISSIONING THE COMPLETE CABLED PATH

Check	Acceptance evidence	If not
Module recognition	Expected ADV151 position is healthy.	Check tag, seating and node/project assignment.
Cable continuity	Each documented channel reaches the correct external point.	Trace adapter, AKB331 and board terminals.
OFF proof	Permitted input reads OFF at <=5.0 V DC.	Trace source, common, cable and configuration.
ON proof	Permitted input reads ON at 18-26.4 V DC.	Confirm field voltage and channel mapping.
Common polarity	Both groups use the same 24 V DC polarity.	Correct wiring before broader tests.
Redundant side	Module, adapter and cable remain assigned to the correct side.	Resolve any crossed-path evidence.

Test at the path endpoints

Use permitted measurements that distinguish field-board, cable and module-side faults.

Interpret shared failures correctly

Multiple lost channels can indicate a common, connector or cable problem rather than module logic.

Retain every exception

Open channel or connector defects remain under an identified owner and release restriction.

Witness final states

Record source voltage, displayed state, diagnostics and responsible approval.

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05 / HANDOVER AND RETAINED ASSEMBLY RECORD

As-left module	ADV151-P00 S2 tag, node and slot
As-left adapter	ATD5A-00 tag and connector orientation
As-left cable	AKB331 identity, both ends and route
As-left termination	AED5D or relay-board identity and positions
Commissioning evidence	OFF/ON proof, commons and diagnostics
Exceptions	Owner and restriction for every unresolved item

Technical reference record

Yokogawa Digital I/O Modules

GS 33J60F50-01EN; selected pages 1, 6 and 8 confirm ADV151 input data, physical form and the /D5A00 ATD5A option.

Related Apter Power catalog references

Catalog navigation only. These entries do not establish compatibility, substitution or approval of a changed installation.

Yokogawa ATA4S-00 S2 | View Yokogawa ATA4S-00 S2 page

Yokogawa EC401-10 S2 | View Yokogawa EC401-10 S2 page

Yokogawa ADV151-P00 S2 | View Yokogawa ADV151-P00 S2 page

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Send the exact model, quantity and requested condition for a quotation.

ADV151-P00 S2 ATD5A-00