Yokogawa ADV151-P00 S2

32-Channel 24 V DC Digital Input Module

01 / SCOPE, CHANNEL BASELINE AND EXACT IDENTITY

This guide restores an ADV151-P00 S2 input position without changing the thirty-two channel assignments, the two common groups, field polarity or the installed connection architecture.

Capture inputs and commons → **Isolate the field circuits** → **Restore states and diagnostics**

Record the complete tag

Capture ADV151-P00 S2, node, slot and any redundant partner.

Export the channel map

Save inputs 1-32, normal states, point names and alarm/control use.

Trace the two commons

Identify channel 1-16 and channel 17-32 common conductors and their polarity.

Capture the connection path

Record direct ATB terminal hardware or the ATD5A/AKB331 external-board route.

Process evidence	Approved maintenance state for every affected point
Electrical evidence	Field supply, common polarity and permitted test points
System evidence	Node health, channel diagnostics and redundancy status
Release evidence	Witnessed state check and retained as-left record

Acceptance boundary

No unrecorded wiring change	Every conductor and common returns to its saved position.
No borrowed family data	Only ADV151 input characteristics govern the checks.
No unexplained diagnostic	Every node or channel alarm is resolved or formally retained.

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Control external 24 V DC

Do not assume the FIO node isolates every field conductor; identify and control each external source.

Label every point

Mark conductor, input number and common group before loosening terminals or cable connectors.

Preserve common polarity

Both 16-channel groups must use the same externally supplied 24 V DC polarity.

Protect the module

Use grounded ESD handling and protect backplane, terminal and cable contacts from contamination.

Rated input	24 V DC sink/source
ON band	18 to 26.4 V DC
OFF band	5.0 V DC or less
Maximum boundary	30.0 V DC
Input current	4.1 mA +/-20% per channel
Module demand	Maximum 500 mA at 5 V DC

Stop conditions

One common has opposite polarity	Correct the design or wiring before energization.
Connection hardware is unidentified	Trace the terminal, adapter, cable and remote board.
Tag does not confirm P00 S2	Resolve the procurement identity before replacement.
Test point is not process-approved	Do not force an input for convenience.

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1. Final baseline	Capture current channel states and node diagnostics.
2. Controlled isolation	Place affected logic in the approved state and isolate external power.
3. Conductor or cable record	Photograph and label inputs, commons, adapter and cable ends.
4. Module removal	Protect adjacent hardware and the backplane connector.
5. Exact seating	Install the verified ADV151-P00 S2 in the recorded node and slot.
6. Field restoration	Return every channel and common to its documented position and polarity.

Direct pressure-clamp path

Retain ATB5S or ATB5D identity and every terminal position.

External terminal-board path

Retain ATD5A, AKB331 and the remote-board identity as one assembly.

Redundant position

Keep module, slot, common groups and field path assigned to the correct side.

No adjacent-family substitution

ADV161, ADV157 and ADV551 have different channel or functional data.

Pre-energization gate

Mechanical	Module seated and retained; connectors undamaged
Wiring	Inputs 1-32 and commons match the record
Polarity	Both groups use the same approved polarity
Configuration	Channel names, states and redundant assignment unchanged

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04 / COMMISSIONING AND DIAGNOSTIC ACCEPTANCE

Check	Acceptance evidence	If not
Module recognition	Expected ADV151 position is healthy.	Check tag, seating, node compatibility and project assignment.
OFF-state proof	Permitted point reads OFF at <=5.0 V DC.	Trace field voltage, common, terminal and configuration.
ON-state proof	Permitted point reads ON at 18-26.4 V DC.	Trace source, polarity, conductor and point assignment.
Common polarity	Both 16-channel groups have identical supply polarity.	Correct wiring before broader channel tests.
Channel mapping	All used inputs correspond to the saved point list.	Resolve crossed conductors or project discrepancies.
Redundant behavior	Both approved sides show the intended state and diagnostics.	Keep the application under maintenance control.

Do not chase one symptom with multiple changes

Separate field, wiring, common, module and software evidence before replacing additional hardware.

Record permitted test conditions

A passing state is credible only when the source voltage and common reference are known.

Review inactive channels

Unused inputs should not introduce unexplained diagnostics or unsafe unterminated conductors.

Release with witnesses

Retain the measured values, final diagnostics and responsible approval.

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As-left identity	Complete ADV151-P00 S2 tag, node and slot
Channel record	Inputs 1-32, point names and normal states
Common record	Two group conductors and shared polarity
Connection record	Terminal or ATD5A/AKB331/remote-board path
Commissioning record	Permitted ON/OFF proof and diagnostics
Exceptions	Owner and release restriction for every open item

Technical reference record

Yokogawa Digital I/O Modules

GS 33J60F50-01EN; selected pages 1, 6 and 8 for ADV151 characteristics, physical form and ordering evidence.

Related Apter Power catalog references

Catalog navigation only. These entries do not establish compatibility, substitution or approval of a changed installation.

Yokogawa ATA4D-00 S2 | View Yokogawa ATA4D-00 S2 page

Yokogawa EC401-10 S2 | View Yokogawa EC401-10 S2 page

Yokogawa AFV10D S2 | View Yokogawa AFV10D S2 page

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Send the exact model, quantity and requested condition for a quotation.

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