

Emerson

KJ3222X1-BA1 12P2532X072 VE4033S2B1

DeltaV Redundant 8-Channel 4-20 mA HART Analog Input Assembly

This reference defines the engineering boundary of KJ3222X1-BA1 12P2532X072 VE4033S2B1 using manufacturer-authored technical evidence and the exact supplied identity.

System position

Exact identity KJ3222X1-BA1 12P2532X072	System family DeltaV M-series Traditional I/O	Functional role Redundant Input Assembly	Release basis Recorded test and diagnostics
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Control the identity boundary

Treat KJ3222X1-BA1 12P2532X072 VE4033S2B1, its documented DeltaV Redundant 8-Channel 4-20 mA HART Analog Input Assembly role and the receiving DeltaV M-series Traditional I/O position as one controlled identity.

IDENTITY / RESOURCE	DOCUMENTED VALUE
Exact assembly	KJ3222X1-BA1 12P2532X072 VE4033S2B1
I/O function	Analog input with HART
Channels	8
Nominal signal	4 to 20 mA
Full signal range	1 to 22.5 mA with over-range checking
Resolution	16-bit A/D converter

Technical evidence record: Emerson DeltaV M-series Traditional I/O product data sheet, March 2026

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Interfaces and integration

Trace the installed signal path

Integration review proceeds through mechanical, electrical, configuration and diagnostic interfaces before a replacement decision is released.

Exact assembly	KJ3222X1-BA1 12P2532X072 VE4033S2B1
I/O function	Analog input with HART
Channels	8

Define the receiving position

The host review links the physical slot, companion hardware and retained engineering revision to the offered item.

Host	DeltaV M-series Traditional I/O
Primary role	redundant acquisition of eight 4-20 mA field-current
Acceptance	Recorded diagnostic response

Interface control matrix

CONTROL BAND	VERIFICATION RECORD
Identity	Model, designation and complete label
Mechanical	Position, seating and retention
Electrical	Loop supply and field conductors
Processing	Range, limits and correction
Supervision	Supply, range and wire-break states
Release	Controlled test with retained result

Configuration ownership

Keep assigned channel values under the approved project record.

Acceptance ownership

Name the retained result that authorizes handover of the measurement path.

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Technical resource bands

Only capabilities tied to the exact execution appear here; operating assignments remain configuration-controlled.

PARAMETER	VALUE
Exact assembly	KJ3222X1-BA1 12P2532X072
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Resolution	16-bit A/D converter

PARAMETER	VALUE
Repeatability	0.05% of span
Redundant LocalBus current	175 mA typical; 250 mA maximum per card
Field circuit power	300 mA maximum at 24 V DC (+10%)
Redundancy	Automatic recognition and switchover on a two-wide
Termination	VE4033S2B1 redundant standard I/O termination
Calibration	None required

Label decision

Accept the stated part number only when the complete installed identity agrees.

Circuit decision

Confirm the recorded field-circuit arrangement before reconnecting any loop.

Processing decision

Restore channel processing from the approved project rather than a neighboring position.

Evidence decision

Use diagnostic observations and a controlled response to support final handover.

Engineering boundary

A similar faceplate or matching channel total does not prove interchangeability. Resolve the host position, connection scheme and controlled revision before use.

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Controls and service logic

Identity control

Preserves the complete KJ3222X1-BA1 12P2532X072 VE4033S2B1 execution.

Host integration

Matches the installed DeltaV M-series Traditional I/O position.

Interface control

Retains power, signal, keying and protective connections.

Release control

Uses diagnostics and a functional test to close the work.

Service decision path

STEP	REQUIRED OUTCOME
1. Identify	Resolve label, designation and receiving position
2. Compare	Review circuit type, processing and supervision
3. Restore	Rebuild the recorded connection and configuration state
4. Demonstrate	Retain diagnostic and functional acceptance evidence

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Engineering decisions

Which field interface does KJ3222X1-BA1 12P2532X072 VE4033S2B1 place under configuration control?

KJ3222X1-BA1 12P2532X072 VE4033S2B1 is documented for redundant acquisition of eight 4-20 mA field-current channels with HART pass-through.

How does KJ3222X1-BA1 12P2532X072 VE4033S2B1 separate product capability from site-specific settings?

Match the entire model string, the installed position, keying, terminal hardware and approved system record before changeout.

What must remain unchanged when KJ3222X1-BA1 12P2532X072 VE4033S2B1 is moved into the approved position?

The verified value for channels is 8.

Which diagnostic review is appropriate after energizing KJ3222X1-BA1 12P2532X072 VE4033S2B1?

Its function depends on the documented receiving position and connected hardware; similar appearance does not establish interchangeability.

Why is the suffix in KJ3222X1-BA1 12P2532X072 VE4033S2B1 part of the controlled identity?

Record the full label, host position, wiring, connected devices, active settings and as-found diagnostics before isolation.

How should an engineer verify KJ3222X1-BA1 12P2532X072 VE4033S2B1 against the host drawing?

Restore the documented interfaces, review diagnostics and functionally verify redundant acquisition of eight 4-20 mA field-current channels with HART

Related Apter Power pages

Emerson KJ3002X1-BG2 12P1731X062

Emerson KJ3002X1-BB1 12P0683X082

Emerson KJ3001X1-BB1 12P0550X142

Technical evidence record: Emerson DeltaV M-series Traditional I/O product data sheet, March 2026

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