# Emerson

# KJ3222X1-BA1 12P2532X092 VE4003S2B2

## DeltaV 8-Channel 4-20 mA HART Analog Input Assembly

This reference defines the engineering boundary of KJ3222X1-BA1 12P2532X092 VE4003S2B2 using manufacturer-authored technical evidence and the exact supplied identity.

## System position

| Exact identity | System family | Functional role | Release basis |
|---|---|---|---|
| KJ3222X1-BA1 12P2532X092 | DeltaV M-series Traditional I/O | Analog Input Assembly | Recorded test and diagnostics |

## Control the identity boundary

Treat KJ3222X1-BA1 12P2532X092 VE4003S2B2, its documented DeltaV 8-Channel 4-20 mA HART Analog Input Assembly role and the receiving DeltaV M-series Traditional I/O position as one controlled identity.

| IDENTITY / RESOURCE | DOCUMENTED VALUE |
|---|---|
| **Exact assembly** | KJ3222X1-BA1 12P2532X092 VE4003S2B2 |
| **I/O function** | Analog input with HART |
| **Channels** | 8 |
| **Nominal signal** | 4 to 20 mA |
| **Full signal range** | 1 to 22.5 mA with over-range checking |
| **Resolution** | 16-bit A/D converter |

Technical evidence record: Emerson DeltaV M-series Traditional I/O product data sheet, March 2026


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# Interfaces and integration

## Trace the installed signal path

Integration review proceeds through mechanical, electrical, configuration and diagnostic interfaces before a replacement decision is released.

| | |
|---|---|
| **Exact assembly** | KJ3222X1-BA1 12P2532X092 VE4003S2B2 |
| **I/O function** | Analog input with HART |
| **Channels** | 8 |

## Define the receiving position

The host review links the physical slot, companion hardware and retained engineering revision to the offered item.

| | |
|---|---|
| **Host** | DeltaV M-series Traditional I/O |
| **Primary role** | acquisition of eight 4-20 mA field-current channels with |
| **Acceptance** | Recorded diagnostic response |

### Interface control matrix

| CONTROL BAND | VERIFICATION RECORD |
|---|---|
| **Identity** | Model, designation and complete label |
| **Mechanical** | Position, seating and retention |
| **Electrical** | Loop supply and field conductors |
| **Processing** | Range, limits and correction |
| **Supervision** | Supply, range and wire-break states |
| **Release** | Controlled test with retained result |

## Configuration ownership

Keep assigned channel values under the approved project record.

## Acceptance ownership

Name the retained result that authorizes handover of the measurement path.


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# Technical resource bands

Only capabilities tied to the exact execution appear here; operating assignments remain configuration-controlled.

| PARAMETER | VALUE |
|---|---|
| Exact assembly | KJ3222X1-BA1 12P2532X092 |
| I/O function | Analog input with HART |
| Channels | 8 |
| Nominal signal | 4 to 20 mA |
| Full signal range | 1 to 22.5 mA with over-range checking |
| Resolution | 16-bit A/D converter |

| PARAMETER | VALUE |
|---|---|
| Repeatability | 0.05% of span |
| LocalBus current | 120 mA typical; 150 mA maximum in simplex |
| Field circuit power | 300 mA maximum at 24 V DC (+10%) |
| HART scan time | 600 to 800 ms typical per enabled channel |
| Termination | Fused I/O termination block |
| Calibration | None required |

### Label decision

Accept the stated part number only when the complete installed identity agrees.

### Circuit decision

Confirm the recorded field-circuit arrangement before reconnecting any loop.

### Processing decision

Restore channel processing from the approved project rather than a neighboring position.

### Evidence decision

Use diagnostic observations and a controlled response to support final handover.

## Engineering boundary

A similar faceplate or matching channel total does not prove interchangeability. Resolve the host position, connection scheme and controlled revision before use.

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# Controls and service logic

### Identity control

Preserves the complete KJ3222X1-BA1 12P2532X092 VE4003S2B2 execution.

### Host integration

Matches the installed DeltaV M-series Traditional I/O position.

### Interface control

Retains power, signal, keying and protective connections.

### Release control

Uses diagnostics and a functional test to close the work.

## Service decision path

| STEP | REQUIRED OUTCOME |
| --- | --- |
| **1. Identify** | Resolve label, designation and receiving position |
| **2. Compare** | Review circuit type, processing and supervision |
| **3. Restore** | Rebuild the recorded connection and configuration state |
| **4. Demonstrate** | Retain diagnostic and functional acceptance evidence |

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# Engineering decisions

### How can technicians prove that KJ3222X1-BA1 12P2532X092 VE4003S2B2 was restored correctly?

KJ3222X1-BA1 12P2532X092 VE4003S2B2 is documented for acquisition of eight 4-20 mA field-current channels with HART pass-through.

### When should KJ3222X1-BA1 12P2532X092 VE4003S2B2 be rejected as a substitute despite a similar appearance?

Match the entire model string, the installed position, keying, terminal hardware and approved system record before changeout.

### What acceptance record closes work on KJ3222X1-BA1 12P2532X092 VE4003S2B2?

The verified value for channels is 8.

### Which field interface does KJ3222X1-BA1 12P2532X092 VE4003S2B2 place under configuration control?

Its function depends on the documented receiving position and connected hardware; similar appearance does not establish interchangeability.

### How does KJ3222X1-BA1 12P2532X092 VE4003S2B2 separate product capability from site-specific settings?

Record the full label, host position, wiring, connected devices, active settings and as-found diagnostics before isolation.

### What must remain unchanged when KJ3222X1-BA1 12P2532X092 VE4003S2B2 is moved into the approved position?

Restore the documented interfaces, review diagnostics and functionally verify acquisition of eight 4-20 mA field-current channels with HART pass-through before

## Related Apter Power pages

- Related industrial automation product
- Related industrial automation product
- Related industrial automation product

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