

Siemens TI-405-15ABM

Technical Reference

TECHNICAL IDENTITY

Product function	buffered ASCII communication coprocessor
System family	SIMATIC TI 405
Complete identity	TI-405-15ABM
Technical scope	Signal, power, communication and configuration characteristics

VALIDATED SPECIFICATIONS

Alternate identifier	405-15ABM
Equivalent family reference	F4-CP128-1
Program memory	128 kB
Serial ports	3 buffered ports
Processor frequency	26 MHz
Port mix	1 RS-232; 2 RS-232/422/485
Maximum baud rate	115.2 kbit/s
Acceptance focus	Confirm end-to-end response

MODEL-SPECIFIC VERIFICATION POINTS

Installed execution	Match the complete TI-405-15ABM marking and suffix.
Signal boundary	Confirm the source and destination assigned to the buffered ASCII communication coprocessor path.
Configuration baseline	Retain range, scaling, address and diagnostic behavior.
Acceptance result	Verify stable status plus the expected end-to-end response.

ApterPower product support
Scan for model-specific assistance.



Functional architecture

SIGNAL AND SYSTEM BOUNDARY

Input boundary Confirm the installed source and electrical class before connecting TI-405-15ABM.	Processing role The module performs the buffered ASCII communication coprocessor function inside SIMATIC TI 405.
Output boundary Match the receiving channel, load or communication endpoint.	Configuration boundary Retain range, scaling, addressing and status handling assigned to this position.

PARAMETER INTERPRETATION

Alternate identifier	405-15ABM — verify this value against the installed interface.
Equivalent family reference	F4-CP128-1 — verify this value against the installed interface.
Program memory	128 kB — verify this value against the installed interface.
Serial ports	3 buffered ports — verify this value against the installed interface.
Processor frequency	26 MHz — verify this value against the installed interface.
Port mix	1 RS-232; 2 RS-232/422/485 — verify this value against the installed interface.
Maximum baud rate	115.2 kbit/s — verify this value against the installed interface.
Acceptance focus	Confirm end-to-end response — verify this value against the installed interface.

DATA PATH CHECKS

Source Stable source condition with correct common or reference.	Conversion Expected module status and configured engineering range.
Transfer Correct backplane, network or rack ownership.	Destination Expected controller value, output response or operator indication.

Interfaces and compatibility

INTERFACE REQUIREMENTS

Electrical class	Match the installed voltage, current or communication standard.
Channel ownership	Retain every configured channel and terminal assignment.
Reference potential	Restore commons, shields and protective grounding as designed.
Connector fit	Inspect keys, latches and mating contacts before insertion.
Software mapping	Keep the original address, tag ownership and diagnostic mapping.
System state	Use the approved maintenance state during replacement.
Compatibility	Confirm the receiving rack and surrounding module execution.
Response	Prove the complete path under a controlled input or command.

INTEGRATION CONTEXTS

Installed-base maintenance Restore the assigned buffered ASCII communication coprocessor position without changing the surrounding system design.	Loop and channel service Retain terminal assignments, scaling and channel ownership during controlled replacement.
Fault isolation Separate module, field wiring and configuration causes during troubleshooting.	Commissioning Prove the complete signal path before returning the process to service.

DIAGNOSTIC FOCUS

Status indication Review ready, fault and communication indications.	Static value Compare the resting value with the retained process reference.
Dynamic value Apply an approved change and observe proportional tracking.	Alarm path Confirm expected alarm, interlock and operator indications.

Technical verification plan

BENCH AND FIELD CHECKS

Identity check	Read the complete TI-405-15ABM marking and suffix.
Unpowered inspection	Check terminal condition, contamination and mechanical damage.
Continuity check	Confirm conductors, commons and shields against the saved connection map.
Power check	Verify supply level and polarity before module insertion.
Configuration check	Compare retained range, scaling, address and status settings.
Static test	Confirm a stable idle value or state.
Dynamic test	Drive a controlled change through the full signal path.
Release test	Verify diagnostics, alarms, interlocks and operator visibility.

ACCEPTANCE CRITERIA

Mechanical Module seated, keyed and latched without strain.	Electrical Supply, polarity, reference and shielding verified.
Logical Address, range, scaling and channel ownership restored.	Functional End-to-end response agrees with the process reference.

ENGINEERING LIMITS

Substitution	Do not substitute a nearby suffix without verifying every interface.
Field modification	Do not alter calibrated or keyed hardware.
Mixed systems	Confirm rack, terminal and software compatibility before energization.
Release authority	Return the loop or controller only after the acceptance test passes.

Technical selection summary

MODEL-SPECIFIC DECISION POINTS

Alternate identifier	405-15ABM must agree with the intended buffered ASCII communication coprocessor position.
Equivalent family reference	F4-CP128-1 must agree with the intended buffered ASCII communication coprocessor position.
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Maximum baud rate	115.2 kbit/s must agree with the intended buffered ASCII communication coprocessor position.
Acceptance focus	Confirm end-to-end response must agree with the intended buffered ASCII communication coprocessor position.

RELATED SIEMENS TECHNICAL PAGES

Siemens 6ES7431-7KF00-6AA0	https://www.apterpower.com/Siemens/Siemens-Simatic-S7-400/6es7431-7kf00-6aa0
Siemens 6GK1143-0TA00	https://www.apterpower.com/Siemens/Siemens-Simatic-Net/6gk1143-0ta00

SELECTION CHECKLIST

Model	TI-405-15ABM
Platform	SIMATIC TI 405
Assigned role	buffered ASCII communication coprocessor
Interfaces	Match source, destination and terminal hardware
Configuration	Match range, scaling, address and diagnostics
Acceptance	Confirm static, dynamic and alarm response