1

01 Readiness and operating boundary

Before isolation, reconcile the complete label with the host drawing and capture the as-found installation.

Capture the installed identity

Confirm the complete KJ3222X1-BA1 12P2532X092 VE4003S2B2 marking and documented hardware role before work.

Exact assembly	KJ3222X1-BA1 12P2532X092 VE4003S2B2
I/O function	Analog input with HART
Channels	8

Prove the work boundary

Place the connected process and controller hardware in the approved maintenance state.

Identity	KJ3222X1-BA1 12P2532X092 VE4003S2B2
Host	DeltaV M-series Traditional I/O
Acceptance	Isolation and label record

PAGE TRACE	REQUIRED ENTRY
Inspector	_____
Result	Pass / hold / corrective action
Record	Date, diagnostic state and release reference

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KJ3222X1-BA1 12P2532X092 V

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02 Configuration and interface setup

Translate retained records into a controlled map of position, connections, keying and configuration.

Build the channel map

Record the host position, companion parts, cable direction, terminal assignments and active configuration.

I/O function	Analog input with HART
Channels	8
Nominal signal	4 to 20 mA

Classify every field circuit

Create a point-by-point map of every affected connector, terminal and configuration assignment.

Identity	KJ3222X1-BA1 12P2532X092 VE4003S2B2
Host	DeltaV M-series Traditional I/O
Acceptance	Channel map approved

PAGE TRACE	REQUIRED ENTRY
Inspector	_____
Result	Pass / hold / corrective action
Record	Date, diagnostic state and release reference

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03 Installation and service sequence

Install the exact hardware and rebuild each interface without changing the documented architecture.

Restore the physical assembly

Isolate every documented power, signal and stored-energy source before disconnecting hardware.

Channels	8
Nominal signal	4 to 20 mA
Full signal range	1 to 22.5 mA with over-range checking

Apply controlled settings

Protect keyed connectors, electronic contacts and labeled field conductors during handling.

Identity	KJ3222X1-BA1 12P2532X092 VE4003S2B2
Host	DeltaV M-series Traditional I/O
Acceptance	Assembly and settings restored

PAGE TRACE	REQUIRED ENTRY
Inspector	_____
Result	Pass / hold / corrective action
Record	Date, diagnostic state and release reference

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04 Verification and fault isolation

Energize under controlled conditions and observe all available device, carrier and system diagnostics.

Observe supervised states

Restore only the recorded power, wiring, grounding, shielding and configuration arrangement.

Nominal signal	4 to 20 mA
Full signal range	1 to 22.5 mA with over-range checking
Resolution	16-bit A/D converter

Exercise fault indications

Energize in a controlled sequence and review device, carrier, controller and field diagnostics.

Identity	KJ3222X1-BA1 12P2532X092 VE4003S2B2
Host	DeltaV M-series Traditional I/O
Acceptance	Supervision response recorded

PAGE TRACE	REQUIRED ENTRY
Inspector	_____
Result	Pass / hold / corrective action
Record	Date, diagnostic state and release reference

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5

05 Acceptance and service handover

Release the equipment only after identity, installation and functional evidence are recorded together.

Compile the acceptance package

Function-test acquisition of eight 4-20 mA field-current channels with HART pass-through and retain the acceptance record before service release.

Exact assembly	KJ3222X1-BA1 12P2532X092 VE4003S2B2
I/O function	Analog input with HART
Channels	8

Authorize the handover

Attach the as-left diagnostic state and named release owner to the handover record.

Identity	KJ3222X1-BA1 12P2532X092 VE4003S2B2
Host	DeltaV M-series Traditional I/O
Acceptance	Release evidence complete

Related industrial automation product

Related industrial automation product

Related industrial automation product

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