HIMA F8621A

Coprocessor Module

01 / PREPARATION AND PHYSICAL WORK

This field guide organizes the work for HIMA F8621A. It is an Apter Power engineering aid, not a substitute for the applicable operating manual or the approved plant work procedure.

PES master communication program → **F8621A coprocessor** → **PES slave relationships**

Preparation decisions

01 / Capture the master relationships

Retain the master communication program, participating PES slaves and data-exchange assignment before removal.

02 / Map the host-specific buffer

For H41q identify backplane battery support; for H51q identify F7131 buffering. Include that support path in the maintenance boundary.

Baseline to compare before disturbance

Check	Baseline / acceptance evidence
Host assignment	H41q or H51q and associated CPU are identified
Reserved slot	H51q coprocessor occupies a documented reserved position
Program/EPROM	Master program and operating-system EPROM match the baseline

03 / Record the interface setup

Preserve the operating-system EPROM identity and RS-485 switch positions as well as the physical rack position.

04 / Follow the central-module dependency

Disconnect communication cabling and remove the associated central module before withdrawing the coprocessor, following the system procedure.

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05 / Reinstall in its reserved position

Check switches, insert and secure the coprocessor without its cable, then restore communication cabling and the associated central module.

06 / Prove master/slave exchange

Review diagnostics and verify the required PES relationships against the retained communication project before releasing the function.

Commissioning and fault-isolation logic

Run the approved test with the process in its permitted state. The expected result is the recorded acceptance condition below; no additional trip thresholds or test voltages are inferred by this guide.

Test subject	Expected evidence	If the check fails
Host assignment	H41q or H51q and associated CPU are identified	Do not choose a battery or slot procedure from the other host.
Reserved slot	H51q coprocessor occupies a documented reserved position	Correct placement against the assembly plan.
Program/EPROM	Master program and operating-system EPROM match the baseline	Resolve identity/version mismatch before communication tests.
RS-485 setup	Switch settings agree with the retained bus design	Correct settings without altering the network design.
PES exchange	Every intended slave path passes the approved communication test	Isolate the missing path and review diagnostics.

Retain both the test stimulus and the observed response. Keep unsuccessful results and diagnostic messages in the work record; record the final successful test separately after the cause is resolved.

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As-left configuration	Exact module identity, occupied position and restored connection or project assignment.
Functional evidence	Results for the model-specific test subjects on page 2, including any corrected fault.
Release decision	Responsible engineering review and the approved return-to-service state; unresolved exceptions remain on hold.

Technical reference record

H41q/H51q System Catalogue, HI 800 263 E

PDF pp. 33, 36, 45, 47, 52; slot rules, replacement, startup and buffering

Related Apter Power catalog references

Catalog navigation only. These entries do not establish compatibility, substitution or approval of a changed installation.

HIMA F7553 | View HIMA F7553 page

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CONTACT APTER POWER

WHATSAPP / PHONE +86 180 3023 5313

EMAIL sales13@apterpower.com

WEBSITE www.apterpower.com

Send the exact model, quantity and requested condition for a quotation.

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