HIMA F8621A

Coprocessor Module

A coprocessor that holds the master program for communication between PES systems. Its program and buffering path must be preserved independently of the associated central processor.

PES master communication program → **F8621A coprocessor** → **PES slave relationships**

Identity and capability

System families	H41q and H51q
Function	Master program for inter-PES communication
H41q installation	One coprocessor or communication module right of each central module
H51q maximum	3 coprocessors per central module
H51q reserved positions	10/11/12 or 17/18/19

Integration and service

H41q buffering	Backplane batteries
H51q buffering	Batteries on F7131
Pre-start checks	Operating-system EPROM and RS-485 switch positions
Removal dependency	Associated central module removed first

HIMA F8621A

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Required integration outcomes

Subject	Required outcome
Host assignment	H41q or H51q and associated CPU are identified
Reserved slot	H51q coprocessor occupies a documented reserved position
Program/EPROM	Master program and operating-system EPROM match the baseline
RS-485 setup	Switch settings agree with the retained bus design
PES exchange	Every intended slave path passes the approved communication test

Use the approved site procedure for trip thresholds, hazardous-area approval and permission to work. This dossier does not assign plant-specific limits.

Engineering distinctions

Coprocessor vs Ethernet option

F8621A is responsible for the master communication program. A neighboring Ethernet option does not replace that program-bearing role.

H41q vs H51q buffering

The same coprocessor has different external buffering arrangements in the two systems. Identify the host before planning battery work.

Slot capacity

H51q permits three coprocessor positions per CPU, not every communication position. Retain reserved-slot placement and redundant correspondence.

Program restoration

A mechanically correct module is not a working master unless the correct program, slave relationships and interface settings are retained.

Applied engineering case

If the central processor starts but inter-PES exchange is absent, separate the communication master program, RS-485 settings and reserved slot from the CPU application. This division helps avoid replacing a working central module to address a coprocessor configuration or buffering problem.

HIMA F8621A

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Technical reference record

H41q/H51q System Catalogue, HI 800 263 E

PDF pp. 33, 36, 45, 47, 52; slot rules, replacement, startup and buffering

Companion reading

The preserved manufacturer excerpt supplies the original technical listing and system context. The separate installation guide covers the ordered work sequence, commissioning evidence and fault checks.

Related Apter Power catalog references

Catalog navigation only. These entries do not establish compatibility, substitution or approval of a changed installation.

HIMA F8620 | View HIMA F8620 page

HIMA F8620/11 | View HIMA F8620/11 page

HIMA F7553 | View HIMA F7553 page

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Send the exact model, quantity and requested condition for a quotation.

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