Emerson KJ3222X1-BA1 12P2532X122 VE4033S2B1

Complete Product Information

Emerson KJ3222X1-BA1 12P2532X122 VE4033S2B1 combines an eight-channel 4-20 mA HART analog-input card with the documented redundant standard i/o termination block execution.

WHAT THIS PRODUCT DOES

Emerson KJ3222X1-BA1 12P2532X122 VE4033S2B1 combines an eight-channel 4-20 mA HART analog-input card with the documented redundant standard i/o termination block execution.

Product characteristic 1

Acquires eight independent 4-20 mA process signals.

Product characteristic 2

Supports HART digital communication on each analog loop.

Product characteristic 3

Provides channel-to-channel and system isolation.

Product characteristic 4

Uses the redundant standard i/o termination block configuration identified by the complete assembly code.

PRODUCT-PAGE TECHNICAL SNAPSHOT

Input channels	8
Input signal	4 to 20 mA with HART
Channel isolation	Each channel optically isolated from the system and other channels
Input range	2 to 22 mA full signal range
Terminal execution	Redundant standard I/O termination block
DeltaV support	Traditional M-series I/O

IMPORTANT NOTICE

Match the complete KJ3222X1-BA1 12P2532X122 VE4033S2B1 marking and documented interfaces before installation, then verify the intended technical response.

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Scan the approved WhatsApp code for model-specific support.

Features and technical data

TECHNICAL DATA SHOWN ON THE PRODUCT PAGE

Input channels	8
Input signal	4 to 20 mA with HART
Channel isolation	Each channel optically isolated from the system and other channels
Input range	2 to 22 mA full signal range
Terminal execution	Redundant standard I/O termination block
DeltaV support	Traditional M-series I/O

FEATURE INTERPRETATION

Product identity

Acquires eight independent 4-20 mA process signals.

System role

Supports HART digital communication on each analog loop.

Service context

Provides channel-to-channel and system isolation.

Return-to-service focus

Uses the redundant standard i/o termination block configuration identified by the complete assembly code.

SIGNAL OR CONTROL PATH

Field signal → **Termination** → **Input module** → **Control database**

KJ3222X1-BA1 12P2532X122 VE4033S2B1 must remain matched to its documented system interface, mating hardware and controlled configuration.

PRODUCT-PAGE TECHNICAL QUESTIONS

Which input channels applies to KJ3222X1-BA1 12P2532X122 VE4033S2B1?	The documented input channels is 8.

Common applications

The application groups below reproduce the model-specific uses presented on the product page and connect each use to a practical verification point.

Pressure transmitter inputs

Reads 4-20 mA measurement and HART device data.

Flow measurement loops

Connects intelligent flow instruments to DeltaV I/O.

Level and temperature transmitters

Accepts standard two-wire HART transmitter signals.

Redundant process measurement

Supports the documented termination architecture for critical inputs.

APPLICATION VERIFICATION MAP

Pressure transmitter inputs	Confirm the documented system position and the required operating function.
Flow measurement loops	Compare the restored connection or control path with the approved record.
Level and temperature transmitters	Check the physical interface and the retained configuration before startup.
Redundant process measurement	Record diagnostics and the final controlled response before release.

INFORMATION CARRIED INTO THE WORK RECORD

Identity record

Complete marking and assigned system position.

Connection record

Connector, conductor and interface arrangement as found.

Configuration record

Controlled settings, application data and option state.

Acceptance record

Observed status, response and release decision.

IMPORTANT NOTICE

Use the complete product identifier for every application review; a related product link is not evidence of interchangeability.

APPLICATION QUESTIONS

What channel isolation should be checked on KJ3222X1-BA1 12P2532X122 VE4033S2B1?	The documented channel isolation is Each channel optically isolated from the system and other channels.
Why does input range matter when selecting KJ3222X1-BA1 12P2532X122 VE4033S2B1?	The documented input range is 2 to 22 mA full signal range.
Where does KJ3222X1-BA1 12P2532X122 VE4033S2B1 use its stated terminal execution?	The documented terminal execution is Redundant standard I/O termination block.

Product precautions and questions

EIGHT MODEL-SPECIFIC PRECAUTIONS

01	Match the complete card, 12P and VE identifiers before work.
02	Record all eight loop assignments and HART device tags.
03	Remove field power before changing terminal-block connections.
04	Preserve polarity, shields and the keyed terminal execution.
05	Inspect the card and termination block for incomplete engagement.
06	Restore configured ranges and damping without copying another channel.
07	Verify analog values and HART device identities after startup.
08	Confirm diagnostics, alarms and controller quality before release.

FREQUENTLY ASKED TECHNICAL QUESTIONS

Which input channels applies to KJ3222X1-BA1 12P2532X122 VE4033S2B1?	The documented input channels is 8.
How is KJ3222X1-BA1 12P2532X122 VE4033S2B1 specified for input signal?	The documented input signal is 4 to 20 mA with HART.
What channel isolation should be checked on KJ3222X1-BA1 12P2532X122 VE4033S2B1?	The documented channel isolation is Each channel optically isolated from the system and other channels.
Why does input range matter when selecting KJ3222X1-BA1 12P2532X122 VE4033S2B1?	The documented input range is 2 to 22 mA full signal range.

IMPORTANT NOTICE	Confirm the complete KJ3222X1-BA1 12P2532X122 VE4033S2B1 identity, mating interface and controlled system position before return to service.

RELEASE CHECKLIST

Identity accepted	The complete product marking and assigned system position agree.
Interfaces accepted	Connections, retention and controlled configuration match the recorded condition.
Response accepted	Diagnostics and the intended technical response have been documented.

FAQ, configuration control and related models

ADDITIONAL TECHNICAL QUESTIONS

Where does KJ3222X1-BA1 12P2532X122 VE4033S2B1 use its stated terminal execution?	The documented terminal execution is Redundant standard I/O termination block.
What value is documented for the deltav support of KJ3222X1-BA1 12P2532X122 VE4033S2B1?	The documented deltav support is Traditional M-series I/O.
Which records should be retained before removing KJ3222X1-BA1 12P2532X122 VE4033S2B1?	Record the complete marking, installed position, wiring, keying and controlled configuration before removal.
Which acceptance checks should follow installation of KJ3222X1-BA1 12P2532X122 VE4033S2B1?	Verify physical engagement, channel status, diagnostics and the intended signal or communication response before release.

CONFIGURATION CONTROL RECORD

Configuration identity	Emerson KJ3222X1-BA1 12P2532X122 VE4033S2B1
Technical boundary	Apply only the functions and interfaces shown for this product or its identified system family.
Selection control	Match the complete ordering string and installed interfaces before purchase or installation.

RELATED MODELS ON APTERPOWER

Emerson PR6423/000-000 CON011	Related product reference. Review the complete ordering string before selection.
Emerson CON041 PR6424/000-121	Related product reference. Compare the required interface and system position.
Emerson 1B30035H01	Related product reference. Confirm the execution independently for the intended task.

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