Honeywell K2LCN-4

LCN Processor Board / Exact identity and technical profile

This reference isolates the exact published characteristics and engineering boundary of Honeywell K2LCN-4 without borrowing values from a neighboring model.

Exact model	K2LCN-4
Part number	51401551-400
Board function	High-performance/density LCN processor
Memory size	4 megawords
Node family	TDC 3000 five/ten-slot module
Rear companion	LCN I/O board

TECHNICAL MARKER

K2LCN processor

TECHNICAL MARKER

4 megawords

TECHNICAL MARKER

Part 51401551-400

TECHNICAL MARKER

LCN I/O companion path

Technical boundary

The exact model and its published data govern this reference. Similar family names, suffixes or physical appearance do not establish the same electrical or software behavior.

Identity evidence	Read K2LCN-4 from the installed label and procurement record
Function evidence	Confirm the published lcn processor board role in the TDC 3000 Five/Ten-Slot Node system
Configuration evidence	Retain the project, mapping, range, address or interface assigned to this position
Release evidence	Link final diagnostics to a witnessed field or operator response

TECHNICAL DECISION

Accept K2LCN-4 only when its identifier and installed-system role agree.

CHANGE BOUNDARY

Any redesign beyond the documented K2LCN-4 position requires separate engineering approval.

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LCN Processor Board / Published characteristics

This matrix groups the published K2LCN-4 characteristics that control identification, configuration and engineering review. Each entry remains tied to the exact lcn processor board record.

Exact model	K2LCN-4
Part number	51401551-400
Board function	High-performance/density LCN processor
Memory size	4 megawords
Node family	TDC 3000 five/ten-slot module
Rear companion	LCN I/O board
Redundant-pair rule	Use compatible processor types
Excluded replacement	HMPU cannot be replaced by K2LCN under the cited procedure
Firmware record	Kernel and LCN firmware retained
Commissioning basis	Node load, LCN paths and diagnostics

INTERPRETATION

Use every K2LCN-4 value with the exact installed configuration.

DESIGN RECORD

Retain the drawing, project and channel or interface assignments that make K2LCN-4 data actionable.

Nameplate check	Manufacturer and full reference agree with the controlled record
Range check	Published operating or signal data agree with the configured use
Interface check	Backplane, terminal, cable, network or memory arrangement is documented
Limit check	Loading, channel count, supply or lifecycle constraint is reviewed
Evidence close	Source title and applicable product record are retained with the decision

NO BORROWED VALUES

Another Honeywell suffix cannot fill a missing K2LCN-4 parameter.

ENGINEERING OUTPUT

Record the exact value used, its purpose and any unresolved item before selection.

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LCN Processor Board / System role and data flow

Application Module service

Restores a four-megaword K2LCN processor position.

Operator Station node

Supports the documented four-megaword node configuration.

Redundant node maintenance

Uses matched processor types across the node pair.

Legacy LCN restoration

Returns node processing and LCN communications after controlled replacement.

Signal and configuration path

Field layer	Sensors, loads, terminal or operator interface assigned by the design
Module layer	K2LCN-4 performs its published lcn processor board role
Control layer	PLC project, address map or runtime application interprets the exchanged data
Acceptance layer	Diagnostics and intended process response are witnessed together

Engineering boundary

Application examples describe system roles only. They do not establish automatic interchangeability, replacement approval or suffix equivalence.

Observed input	Field, network or operator condition presented to K2LCN-4
Published action	The documented lcn processor board function processes that condition
Configured interpretation	PLC logic, HMI project or station mapping assigns system meaning
Expected output	A defined state, value, message or command is produced
Acceptance proof	The complete path is witnessed under controlled conditions

APPLICATION RECORD

Tie each K2LCN-4 use case to the installed drawing or project tag.

EXCEPTION CONTROL

Document and resolve any behavior that differs from the approved response.

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LCN Processor Board / Engineering decisions

SUPPORTED IDENTITY

K2LCN-4

PUBLISHED FUNCTION

LCN Processor Board

SYSTEM CONTEXT

TDC 3000 Five/Ten-Slot Node

UNSUPPORTED INFERENCE

A nearby model, suffix or family resemblance

Model-specific questions

K2LCN-4 51401551-400: what memory size does the -400 suffix identify?

Honeywell's service table identifies 51401551-400 as the four-megaword K2LCN board.

K2LCN-4 51401551-400: which rear-board path accompanies the processor?

Documented five/ten-slot configurations place the K2LCN processor with an LCN I/O board.

K2LCN-4 51401551-400: what rule applies to redundant node pairs?

Use compatible processor types in both partners; do not mix processor generations without the documented engineering procedure.

K2LCN-4 51401551-400: what is checked at first power-up?

Review rail status, processor diagnostics, firmware identity, memory recognition and both LCN communication paths.

K2LCN-4 51401551-400: what processor function does the board perform?

It is the high-performance/density processor board for an approved LCN node configuration.

K2LCN-4 51401551-400: can it directly replace an HMPU?

No. The cited service procedure explicitly excludes HMPU replacement by K2LCN.

K2LCN-4 51401551-400: what must be backed up before replacement?

Retain the node database, firmware revisions, memory inventory, LCN addressing and partner-node status.

K2LCN-4 51401551-400: what completes node acceptance?

Confirm stable LCN communication, correct node personality, application execution and redundant-partner behavior where applicable.

Decision closeout

Close the K2LCN-4 technical review with the exact identifier, applicable published limits and the installed configuration assumptions explicitly recorded.

Question coverage	Identity, function, configuration and field handling addressed
Open item	Any unanswered exact-model point is listed without guessed data
Decision owner	Responsible engineering or maintenance role identified
Supporting record	Drawing, project revision, readings and witnessed result attached

APPROVAL

The K2LCN-4 decision is accepted only within the documented scope.

REVIEW TRIGGER

A different suffix, wiring arrangement, project or application requires a new review.

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LCN Processor Board / Evidence and support

Exact product	Honeywell K2LCN-4
Technical class	LCN Processor Board
Platform	TDC 3000 Five/Ten-Slot Node
Evidence use	Exact-model characteristics and engineering boundary
Record with	Installed drawing, project and acceptance evidence

Retained evidence	Nameplate, source title, configuration baseline and field record
Installation evidence	Isolation, conductor/connector record and post-work inspection
Commissioning evidence	Diagnostics, measurements and intended response
Final record	Responsible witness, date, exceptions and approved disposition

VERIFIED APTER POWER CATALOG REFERENCES

Navigation only; compatibility, substitution and manufacturer endorsement are not implied.

51304542-100 KLCN_CA LCN A Cable Interface Converter	View 51304542-100 KLCN_CA LCN A Cable Interface Converter page
FC-TSDI-16UNI Sixteen-Channel Universal Safe Digital Input FTA	View FC-TSDI-16UNI Sixteen-Channel Universal Safe Digital Input FTA page

MANUFACTURER EVIDENCE RECORD

Honeywell Five/Ten-Slot Module Service Manual

LC15-530, September 1995

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Send the exact model, quantity, destination and requested condition for a quotation.