3008N

Main Processor

PUBLISHED IDENTITY

TRICONEX 3008N is the nuclear-qualified designation of the Enhanced Main Processor III mapped to the standard 3008 platform. It provides 16 MB of DRAM for approved Tricon version 10 nuclear applications.

Technical snapshot

Product role	Enhanced Main Processor III
Qualified designation	3008N
Standard-model mapping	3008
Processor	Motorola MPC860, 32-bit, 50 MHz
DRAM	16 MB without battery backup
SRAM	32 KB with battery backup
Flash PROM	6 MB
TriBus	25 Mbps in the 8110 chassis

Verified related ApterPower product pages

TRICONEX 3502E

TRICONEX 3503E

Engineering data

Official evidence organized for exact-model review

Product role	Enhanced Main Processor III	**Flash PROM**	6 MB
Qualified designation	3008N	**TriBus**	25 Mbps in the 8110 chassis
Standard-model mapping	3008	**Standard system versions**	Tricon 9.6-10.x
Processor	Motorola MPC860, 32-bit, 50 MHz	**Controller arrangement**	Three independent Main Processor channels
DRAM	16 MB without battery backup	**Qualification class**	Tricon v10 nuclear 1E equipment table
SRAM	32 KB with battery backup	**Acceptance requirement**	Verify qualified revision and installed configuration

EVIDENCE BOUNDARY

Confirm the complete 3008N identity, installed system record and approved companion hardware before replacement or commissioning.

Applications and system role

Four distinct documented operating contexts

INSTALLED TRICON SYSTEMS

Controlled service where the approved system records explicitly identify 3008N.

PLANNED MAINTENANCE

Like-for-like inspection or replacement work that preserves the documented main processor arrangement.

LIFECYCLE-MANAGED SPARES

Exact-identity spare control for operating installations that retain the TRICONEX 3008N designation.

COMMISSIONING VERIFICATION

Functional checks that confirm the recorded interface, configuration and diagnostics after 3008N is installed.

Integration control

The equipment table maps 3008N to the standard 3008 processor, whose manufacturer specifications list a 50 MHz MPC860 CPU, 16 MB DRAM, 32 KB SRAM and 6 MB Flash PROM. The standard platform operates on a 25 Mbps TriBus in the 8110 chassis. Nuclear-qualified installation records, revisions and companion modules must all remain controlled.

Verified related ApterPower product pages

TRICONEX 3504E

TRICONEX 3511

Technical questions

Complete model-specific answers from the approved evidence set

What function does TRICONEX 3008N perform?

TRICONEX 3008N performs the documented role of a main processor within the applicable installed-system architecture.

Which qualified designation is specified for 3008N?

The manufacturer specifies the qualified designation for 3008N as 3008N.

How does the manufacturer define the standard-model mapping of 3008N?

The manufacturer specifies the standard-model mapping for 3008N as 3008.

What processor applies to the TRICONEX 3008N?

The manufacturer specifies the processor for 3008N as Motorola MPC860, 32-bit, 50 MHz.

Where does 3008N fit with respect to dram?

The manufacturer specifies the dram for 3008N as 16 MB without battery backup.

Why must the sram be checked on 3008N?

The manufacturer specifies the sram for 3008N as 32 KB with battery backup.

When replacing 3008N, which flash prom must match the record?

The manufacturer specifies the flash prom for 3008N as 6 MB.

What does the manual state about the tribus for 3008N?

The manufacturer specifies the tribus for 3008N as 25 Mbps in the 8110 chassis.

Product precautions

Eight aligned controls before return to service

1. Record the complete TRICONEX 3008N marking and installed position before work.
2. Place the controller and affected process function in the approved safe maintenance state.
3. Verify the documented system version, companion hardware and interface assigned to 3008N.
4. Isolate the applicable controller and field energy in accordance with the approved procedure.
5. Inspect connectors, keys, cable assemblies and mechanical retention before installation.
6. Install 3008N only in the recorded location without changing the approved architecture.
7. Restore power under control and confirm the specified indicators, communications and diagnostics.
8. Document the final identity, configuration and functional acceptance results before release.

Verified related ApterPower product pages

- TRICONEX 3624
- TRICONEX 3664
- TRICONEX 3703E
- TRICONEX 3704E

Evidence and support

Manufacturer references, related products and direct contact

Manufacturer evidence used

- TRICONEX Planning and Installation Guide for Tricon v9-v11 Systems, document 9720100-012.
- TRICONEX Technical Product Guide, document 9791007-013.
- TRICONEX Field Terminations Guide for Tricon v9-v10 Systems, document 9700052-018.

Verified related ApterPower product pages

TRICONEX 4210

TRICONEX 4329

TRICONEX 4119A

TRICONEX 8312

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