# Emerson

# KJ3002X1-BE1 12P0682X052

## 8-Channel 4-20 mA HART Analog Output Card

This technical reference records only manufacturer-supported boundaries for Emerson KJ3002X1-BE1 12P0682X052. Undocumented suffix-specific features are intentionally excluded.

## System position

| Exact identity | System family | Functional role | Release basis |
|---|---|---|---|
| KJ3002X1-BE1 12P0682X052 | DeltaV M-series | DeltaV Analog Output Card | Recorded test and diagnostics |

## Control the identity boundary

At the final-control boundary, KJ3002X1-BE1 converts eight DeltaV output values into individual 4-20 mA loops while passing supported HART communication. Loop loading, field supply, channel mapping and fail-state behavior must be verified in the installed system.

| IDENTITY / RESOURCE | DOCUMENTED VALUE |
|---|---|
| **Complete item marking** | KJ3002X1-BE1 12P0682X052 |
| **Documented base model** | KJ3002X1-BE1 |
| **Output channels** | 8 |
| **Signal range** | 4 to 20 mA |
| **Digital protocol** | HART pass-through |
| **LocalBus power** | 12 V DC at 150 mA maximum |

Technical evidence record: DeltaV M-series Traditional I/O, Emerson product data sheet


### CONTACT APTER POWER

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# Interfaces and integration

## Trace the installed signal path

Trace the installed power, signal, communication and terminal path before changing any hardware or configuration. Keep the complete item marking linked to the

| | |
|---|---|
| **Complete item marking** | KJ3002X1-BE1 12P0682X052 |
| **Documented base model** | KJ3002X1-BE1 |
| **Output channels** | 8 |

## Define the receiving position

The host review links the physical slot, companion hardware and retained engineering revision to the offered item.

| | |
|---|---|
| **Host** | DeltaV M-series |
| **Primary role** | eight-channel HART analog output |
| **Acceptance** | Recorded diagnostic response |

### Interface control matrix

| CONTROL BAND | VERIFICATION RECORD |
|---|---|
| **Identity** | Model, designation and complete label |
| **Mechanical** | Position, seating and retention |
| **Electrical** | Loop supply and field conductors |
| **Processing** | Range, limits and correction |
| **Supervision** | Supply, range and wire-break states |
| **Release** | Controlled test with retained result |

## Configuration ownership

Keep assigned channel values under the approved project record.

## Acceptance ownership

Name the retained result that authorizes handover of the measurement path.


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# Technical resource bands

Only capabilities tied to the exact execution appear here; operating assignments remain configuration-controlled.

| PARAMETER | VALUE |
|---|---|
| Complete item marking | KJ3002X1-BE1 12P0682X052 |
| Documented base model | KJ3002X1-BE1 |
| Output channels | 8 |
| Signal range | 4 to 20 mA |
| Digital protocol | HART pass-through |
| LocalBus power | 12 V DC at 150 mA maximum |
| Field power | 24 V DC at 300 mA maximum |

| PARAMETER | VALUE |
|---|---|
| Field circuit | 24 V DC at 23 mA per channel |
| Conversion resolution | 12 bit |
| Reference accuracy | 0.25% of span |
| Operating temperature | -40 to 70°C |
| Storage temperature | -40 to 85°C |
| Relative humidity | 5 to 95%, non-condensing |
| Airborne contaminants | ISA-S71.04-1985 G3 |

### Label decision

Accept the stated part number only when the complete installed identity agrees.

### Circuit decision

Confirm the recorded field-circuit arrangement before reconnecting any loop.

### Processing decision

Restore channel processing from the approved project rather than a neighboring position.

### Evidence decision

Use diagnostic observations and a controlled response to support final handover.

## Engineering boundary

A similar faceplate or matching channel total does not prove interchangeability. Resolve the host position, connection scheme and controlled revision before use.

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# Controls and service logic

### Final-control output

Drives eight documented 4-20 mA field loops from the DeltaV I/O subsystem.

### HART device communication

Passes supported HART data through each configured analog-output path.

### Loop-load validation

Confirms field supply, load and terminal arrangement before enabling outputs.

### Controlled stroke testing

Checks each connected final element under the approved maintenance procedure.

## Service decision path

| STEP | REQUIRED OUTCOME |
|---|---|
| **1. Identify** | Resolve label, designation and receiving position |
| **2. Compare** | Review circuit type, processing and supervision |
| **3. Restore** | Rebuild the recorded connection and configuration state |
| **4. Demonstrate** | Retain diagnostic and functional acceptance evidence |

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# Engineering decisions

### What is the documented purpose of Emerson KJ3002X1-BE1 12P0682X052?

Emerson KJ3002X1-BE1 12P0682X052 is retained as the complete item marking for an eight-channel DeltaV analog-output position. Manufacturer documentation

### Which product family contains Emerson KJ3002X1-BE1 12P0682X052?

The documented base model belongs to the DeltaV M-series product family.

### Which function is documented for Emerson KJ3002X1-BE1 12P0682X052?

For the documented base model, output channels is 8.

### What channel or interface detail applies to Emerson KJ3002X1-BE1 12P0682X052?

For the documented base model, signal range is 4 to 20 mA.

### Which power boundary applies to Emerson KJ3002X1-BE1 12P0682X052?

For the documented base model, digital protocol is HART pass-through.

### Which environmental limit applies to Emerson KJ3002X1-BE1 12P0682X052?

For the documented base model, localbus power is 12 V DC at 150 mA maximum.

**Related Apter Power pages**

- Emerson KC3011X1-BA1 12P6749X042
- Emerson KC3010X1-BA1 12P6762X062
- Emerson 1X01046H01L

Technical evidence record: DeltaV M-series Traditional I/O, Emerson product data sheet

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