Emerson

KJ3002X1-BC1 12P0681X092

DeltaV 8-Channel 4-20 mA Analog Input Card

This reference defines the engineering boundary of KJ3002X1-BC1 12P0681X092 and separates documented product resources from site-specific assignments. It supports exact-model review, controlled replacement and evidence-based handover.

System position

Exact identity	System family	Functional role	Release basis
KJ3002X1-BC1 12P0681X092	DeltaV M-series Traditional I/O	Analog Input Card	Recorded test and diagnostics

Control the identity boundary

Treat the complete KJ3002X1-BC1 12P0681X092 marking, the DeltaV 8-Channel 4-20 mA Analog Input Card designation and the receiving DeltaV M-series Traditional I/O position as one controlled identity.

IDENTITY / RESOURCE	DOCUMENTED VALUE
Exact model	KJ3002X1-BC1 12P0681X092
I/O function	Analog input
Channels	8
Nominal signal	4 to 20 mA
Field-loop type	Two-wire current transmitters
Conversion	16-bit A/D family architecture

Technical evidence record: Emerson-authored legacy card instruction and current M-series I/O documentation

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Interfaces and integration

Trace the installed signal path

Integration review proceeds from the receiving position through mechanical, electrical and configuration interfaces before any replacement decision is released.

Exact model	KJ3002X1-BC1 12P0681X092
I/O function	Analog input
Channels	8

Define the receiving position

The host review links the physical slot, companion hardware and retained engineering revision to the offered item.

Host	DeltaV M-series Traditional I/O
Primary role	acquisition of eight 4-20 mA field-current channels
Acceptance	Recorded diagnostic response

Interface control matrix

CONTROL BAND	VERIFICATION RECORD
Identity	Model, designation and complete label
Mechanical	Position, seating and retention
Electrical	Loop supply and field conductors
Processing	Range, limits and correction
Supervision	Supply, range and wire-break states
Release	Controlled test with retained result

Configuration ownership

Keep assigned channel values under the approved project record.

Acceptance ownership

Name the retained result that authorizes handover of the measurement path.

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Technical resource bands

Only capabilities tied to the exact execution appear here; operating assignments remain configuration-controlled.

PARAMETER	VALUE
Exact model	KJ3002X1-BC1 12P0681X092
I/O function	Analog input
Channels	8
Nominal signal	4 to 20 mA
Field-loop type	Two-wire current transmitters

PARAMETER	VALUE
Conversion	16-bit A/D family architecture
Calibration	Not required
Terminal relationship	Match installed key and terminal block
Configuration rule	Restore each channel from the approved project
Acceptance	Loop current, diagnostics and process scaling

Label decision

Accept the stated part number only when the complete installed identity agrees.

Circuit decision

Confirm the recorded field-circuit arrangement before reconnecting any loop.

Processing decision

Restore channel processing from the approved project rather than a neighboring position.

Evidence decision

Use diagnostic observations and a controlled response to support final handover.

Engineering boundary

A similar faceplate or matching channel total does not prove interchangeability. Resolve the host position, connection scheme and controlled revision before use.

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Controls and service logic

Identity control

Preserves the complete KJ3002X1-BC1 12P0681X092 execution.

Host integration

Matches the installed DeltaV M-series Traditional I/O position.

Interface control

Retains power, signal, keying and protective connections.

Release control

Uses diagnostics and a functional test to close the work.

Service decision path

STEP	REQUIRED OUTCOME
1. Identify	Resolve label, designation and receiving position
2. Compare	Review circuit type, processing and supervision
3. Restore	Rebuild the recorded connection and configuration state
4. Demonstrate	Retain diagnostic and functional acceptance evidence

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Engineering decisions

What is the documented role of KJ3002X1-BC1 12P0681X092?

It is identified as a deltav 8-channel 4-20 ma analog input card used for acquisition of eight 4-20 mA field-current channels.

Why must the complete KJ3002X1-BC1 12P0681X092 identity be retained?

The full model and suffix distinguish the installed execution from similar-looking revisions and related hardware.

Which value is documented for KJ3002X1-BC1 12P0681X092 i/o function?

The verified product record gives i/o function as Analog input.

How is channels specified for KJ3002X1-BC1 12P0681X092?

The verified product record gives channels as 8.

What does KJ3002X1-BC1 12P0681X092 nominal signal?

The verified product record gives nominal signal as 4 to 20 mA.

Which setting applies to KJ3002X1-BC1 12P0681X092 field-loop type?

The verified product record gives field-loop type as Two-wire current transmitters.

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