GE IS200VCRCH1B

Discrete I/O Board

GE IS200VCRCH1B is a Mark VI VME discrete I/O processor board for time-stamped contact inputs and relay-output processing through TBCI and TRLY terminal boards.

01	02	03	04
Mark VI single-slot VME discrete I/O processor	1 ms contact-input sequence-of-events time stamp	TBCI contact-input and TRLY relay-output interfaces	Hardware and firmware identity packet to VCMI

System position

Field contacts and relay loads	TBCI / TRLY terminal boards	IS200VCRCH1B I/O processing	VCMI and Mark VI controller data path

Evidence boundary

Only verified exact-model facts and explicitly identified family context are included. Similar codes are not treated as substitutes.

Model-specific release focus

- Mark VI single-slot VME discrete I/O processor
- 1 ms contact-input sequence-of-events time stamp
- TBCI contact-input and TRLY relay-output interfaces
- Hardware and firmware identity packet to VCMI

02 / CAPABILITY MAP

Engineering domains and architecture

Control platform	GE Mark VI
Board acronym	VCRC
Bus format	Single-width VME I/O board
Contact-input capacity	48 points with two TBCI terminal boards

Relay-output capacity	24 points with two TRLY terminal boards
Event time stamp	1 ms for VCRC contact inputs
I/O arrangement	TMR or simplex
Terminal-board identification	ID wire in the 37-pin cable

Interface architecture

TBCI

Contact-input termination; two boards provide the documented 48-point arrangement

TRLY

Relay-output termination; two boards provide the documented 24-output arrangement

VCMI

Receives board catalog, revision, serial, firmware and terminal-board identity data

Toolbox configuration

Downloads I/O configuration and tune-up data over the control network

Interface evidence for acceptance

- TBCI: Contact-input termination; two boards provide the documented 48-point arrangement
- TRLY: Relay-output termination; two boards provide the documented 24-output arrangement
- VCMI: Receives board catalog, revision, serial, firmware and terminal-board identity data
- Toolbox configuration: Downloads I/O configuration and tune-up data over the control network

03 / INTEGRATION AND APPLICATIONS

Use cases with controlled scope

Sequence-of-events monitoring

Time-stamps discrete contact transitions to 1 ms resolution.

Turbine auxiliary I/O

Acquires permissives, alarms and equipment status for Mark VI control logic.

Relay command distribution

Processes output commands routed to TRLY relay terminal boards.

Simplex or TMR channels

Fits the documented redundant or nonredundant Mark VI discrete-I/O architecture.

INTEGRATION CHECKPOINTS

Identity

GE IS200VCRCH1B is preserved in full.

Host system

Rack, slot and surrounding hardware are verified.

Configuration

Project, firmware and settings are recorded.

Acceptance

Functional evidence is captured before release.

System relationship

Field contacts and relay loads	TBCI / TRLY terminal boards	IS200VCRCH1B I/O processing	VCMI and Mark VI controller data path

Configuration-control record

- Relay-output capacity: 24 points with two TRLY terminal boards
- Event time stamp: 1 ms for VCRC contact inputs
- I/O arrangement: TMR or simplex
- Terminal-board identification: ID wire in the 37-pin cable

Decision matrix

Question	Answer
What is the Mark VI role of GE IS200VCRCH1B?	It is the VCRC discrete I/O processor for contact inputs and relay outputs.
How many contact inputs can GE IS200VCRCH1B process?	The documented arrangement uses two TBCI boards for 48 contact-input points.
How many relay outputs are associated with GE IS200VCRCH1B?	Two TRLY terminal boards provide a 24-relay-output arrangement.
What is the event resolution of GE IS200VCRCH1B contact inputs?	VCRC contact transitions are time-stamped to 1 ms for sequence-of-events monitoring.

Eight release precautions

1

Match GE IS200VCRCH1B to the controlled rack position and terminal-board combination.

2

Remove field and rack power and verify absence of voltage before disconnecting VME or 37-pin cables.

3

Use grounded ESD controls when handling the board outside the Mark VI cabinet.

4

Record TBCI and TRLY cable destinations before removal so input and relay banks are not crossed.

5

Inspect the terminal-board ID-wire path and cable pins before energization.

6

Restore the approved Toolbox I/O configuration instead of copying settings from another rack.

7

Check contact wetting sources and relay-load isolation before forcing any point.

8

After replacement, test point mapping, SOE time stamps, relay outputs and all associated permissives.

Commissioning decision points

- Inspect the terminal-board ID-wire path and cable pins before energization.
- Restore the approved Toolbox I/O configuration instead of copying settings from another rack.
- Check contact wetting sources and relay-load isolation before forcing any point.
- After replacement, test point mapping, SOE time stamps, relay outputs and all associated permissives.

Product verification and support

Apter Power verification record

Model identity	Keep the complete GE model code and every suffix in purchasing, installation and acceptance records.
Verification boundary	Confirm the physical nameplate, installed position and revision-controlled project documents before field work.
Apter Power support	Use the contact details and Apter Power website links in this document for product and sourcing support.

Related Apter Power references	Scope note
GE IS200TBCIH1BBC	Portfolio reference only; verify compatibility independently.
GE IS200TREAH1A	Portfolio reference only; verify compatibility independently.
GE IS200TRTDH1CCC	Portfolio reference only; verify compatibility independently.

	Apter Power technical and sourcing support sales13@apterpower.com +86 180 3023 5313 www.apterpower.com Scan for the Apter Power contact page.	Model record GE IS200VCRCH1B Discrete I/O Board Verify the complete nameplate before installation.

Handover evidence to retain

- Exact identity retained: GE IS200VCRCH1B
- TBCI / TRLY terminal boards
- IS200VCRCH1B I/O processing
- Archive as-left checks and measured results before release.