GE VMIPCI-5565-110000

Reflective Memory Board

GE Fanuc VMIPCI-5565-110000 is a 64-bit PCI reflective-memory board with 128 MB SDRAM, 4K FIFOs and multimode fiber for deterministic shared-data exchange across as many as 256 nodes.

Job objective

Replace, install or verify the exact module without changing unrelated wiring, configuration, dimensions, weight or other stored product data.

Safe-work boundary

Identify Match the complete model and every suffix or secondary code.	**Isolate** Apply the plant-approved electrical and process isolation procedure.
Preserve Back up configuration and photograph all connections.	**Control** Use ESD protection and revision-controlled documentation.

Required evidence before work

Nameplate photo; slot/rack record; connector map; project backup where applicable; fault history; approved acceptance procedure.

Model-specific release focus

- 128 MB local SDRAM reflective memory
- 2.12 Gbaud multimode fiber ring
- Two independent PCI DMA engines
- Up to 256 network nodes with remote interrupts

Readiness checklist

Exact identity	GE VMIPCI-5565-110000 matches the work order and physical unit
System position	Rack, slot and functional assignment are recorded
Configuration	Parameters, firmware or project data are backed up when applicable
Connections	Every cable, terminal, jumper and shield is labeled
Condition	Connector, board and packaging condition are inspected
Test plan	Expected states and acceptance evidence are agreed

Interface preparation

PCI bus	64-bit host interface with 33/66 MHz transfer capability
Fiber ring	Multimode SFF 850 nm link for node-to-node retransmission
Reflective memory	128 MB local SDRAM mapped to matching addresses across nodes
Interrupt and DMA	Network interrupt FIFOs and two independent read/write DMA engines

Interface evidence for acceptance

- PCI bus: 64-bit host interface with 33/66 MHz transfer capability
- Fiber ring: Multimode SFF 850 nm link for node-to-node retransmission
- Reflective memory: 128 MB local SDRAM mapped to matching addresses across nodes
- Interrupt and DMA: Network interrupt FIFOs and two independent read/write DMA engines

03 / INSTALLATION SEQUENCE

Controlled replacement workflow

1	Confirm isolation and absence of hazardous energy
2	Compare old and replacement identities and revisions
3	Photograph and label all connections
4	Remove the existing module using ESD-safe handling
5	Inspect slot, connector, grounding and cooling path
6	Install the exact replacement without forcing connectors
7	Restore cables, jumpers and settings from the controlled record
8	Perform a final visual and continuity check before energization

STOP POINT

Do not energize if the model, slot, connector orientation, grounding, jumper state or documented system assignment is unresolved.

Configuration-control record

- Serial link rate: 2.12 Gbaud
- Network nodes: Up to 256
- Nonredundant transfer rate: 47.1 MB/s to 174 MB/s
- PCI interface: 64-bit, 66 MHz, PCI revision 2.2

Staged return to service

Stage	Action	Acceptance
Stage 1	Visual inspection and isolation release	Expected controlled result
Stage 2	Apply control power under observation	Expected controlled result
Stage 3	Confirm module recognition or status	Expected controlled result
Stage 4	Verify communication and affected signal paths	Expected controlled result
Stage 5	Exercise the documented function	Expected controlled result
Stage 6	Archive measured results and as-left configuration	Expected controlled result

Product-specific acceptance focus

1

Use multimode fiber and compatible LC/SFF optics for the 110000 transmission option.

2

Maintain the daisy-chain ring direction and protect connectors from dust and excessive bend radius.

3

Coordinate redundant-transfer mode and packet-size settings across every participating node.

4

After installation, verify ring continuity, memory replication, DMA, interrupts and error-status handling.

Commissioning decision points

- Use multimode fiber and compatible LC/SFF optics for the 110000 transmission option.
- Maintain the daisy-chain ring direction and protect connectors from dust and excessive bend radius.
- Coordinate redundant-transfer mode and packet-size settings across every participating node.
- After installation, verify ring continuity, memory replication, DMA, interrupts and error-status handling.

Fault isolation and final record

1	Unexpected state: stop and capture evidence
2	Recheck identity, power, slot and connectors
3	Compare configuration and revision records
4	Test surrounding system before condemning the module
5	Restore a safe state and document the outcome

Apter Power handover record

Model identity	Keep the complete GE model code and every suffix in purchasing, installation and acceptance records.
Verification boundary	Confirm the physical nameplate, installed position and revision-controlled project documents before field work.
Apter Power support	Use the contact details and Apter Power website links in this document for product and sourcing support.

Related Apter Power references	Scope note
GE IC698ETM001	Portfolio reference only; verify compatibility independently.
GE IS200VRTDH1DAC	Portfolio reference only; verify compatibility independently.
GE product catalog	Portfolio reference only; verify compatibility independently.

Commissioning support sales13@apterpower.com +86 180 3023 5313 www.apterpower.com

Handover evidence to retain

- Exact identity retained: GE VMIPCI-5565-110000
- VMIPCI-5565-110000 local SDRAM and FIFOs
- Fiber-optic daisy-chain ring
- Archive as-left checks and measured results before release.