

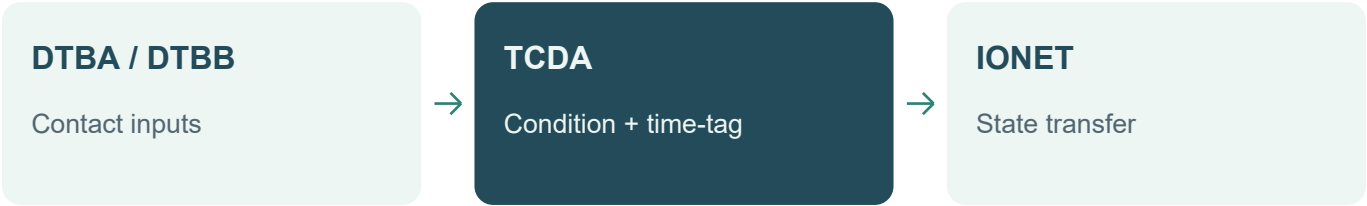
1

GE DS200TCDAH1BGD

Technical Reference / Digital I/O Board

GE DS200TCDAH1BGD is a TCDA digital I/O board for the SPEEDTRONIC Mark V control platform. The TCDA function processes contact input and relay-output signals within the digital I/O architecture.

SYSTEM RELATIONSHIP



Functional signal relationship; terminal and core usage depends on the installed system.

INTERFACE DOSSIER

Interface	Documented role
JQ / JR	DTBA / DTBB contact-input path
JO1 / JO2	TCRA contact-output signals; core-dependent use
JX1 / JX2	IONET, shielded twisted-pair connections
JP	TCPS board-power interface

Evidence boundary

TCDA functional-board information is documented. The complete H1BGD suffix is retained; suffix-specific ratings and interchangeability remain unverified.

PRODUCT SUPPORT

+86 180 3023 5313

sales13@apterpower.com

www.apterpower.com



DS200TCDAH1BGD

2

Configuration & application

Technical Reference / Engineering decisions

VERIFIED TECHNICAL FIELDS

Field	Documented value
Functional acronym	TCDA
System platform	SPEEDTRONIC Mark V
Board function	Digital contact I/O
Input terminal boards	DTBA / DTBB
Relay output boards	TCRA
Communication network	IONET
Power interface	JP
Input connectors	JQ / JR
Output connectors	JO1 / JO2
IONET connectors	JX1 / JX2

Source record

GEH-6153, SPEEDTRONIC Mark V LM Turbine Control Function Manual, WINSYS edition, Chapter 4, page 4-14.

PRODUCT SUPPORT

+86 180 3023 5313
sales13@apterpower.com
www.apterpower.com



DS200TCDAH1BGD

3

Engineering decision map

Technical Reference / Configuration and uses

Configuration	Purpose
J1 / J8	Factory-test positions
J2 / J3	IONET termination resistors
J4 / J5 / J6	IONET identity
J7	Stall timer
HMI I/O constants	Contact-input inversion

APPLICATION CONTEXTS

Turbine contact monitoring

Acquires discrete contact states through DTBA and DTBB in the Mark V digital I/O architecture.

Relay signal routing

Carries contact-output signals to the configured TCRA relay-board positions.

Event-state tracking

Conditions and time-tags changing contacts before transferring the information through IONET.

Mark V I/O maintenance

Supports restoration of the installed digital I/O path with matching wiring, address and jumper records.

Match the TCDA board to its installed digital I/O core, terminal-board wiring and IONET address. Preserve the application-specific jumper configuration when replacing the board.

PRODUCT SUPPORT

+86 180 3023 5313
sales13@apterpower.com
www.apterpower.com



DS200TCDAH1BGD