

INDUSTRIAL AUTOMATION PRODUCT

KJ4006X1-BP1

DeltaV S-series 40-Pin Mass Terminal Block

Emerson KJ4006X1-BP1 is a DeltaV S-series 40-pin mass terminal block for the assigned I/O card. It must be keyed and wired to match the documented channel configuration.

CONTROLLED ENGINEERING COPY

IDENTITY	VERIFIED VALUE
Manufacturer	Emerson
System family	DeltaV S-series I/O
Part number	KJ4006X1-BP1
Document status	Controlled product and system reference

DOCUMENT SCOPE

Exact article identity, official technical data, host matching, installation checks, applications, precautions and controlled acceptance evidence.

MODEL-SPECIFIC ENGINEERING NOTE

Install KJ4006X1-BP1 only in the documented DeltaV S-series I/O arrangement. Preserve identity, keying, wiring and configuration, then verify diagnostics and mass termination of documented S-series I/O field conductors through a keyed 40-pin interface under controlled conditions.

System Role and Architecture

Emerson KJ4006X1-BP1 is a DeltaV S-series 40-pin mass terminal block for the assigned I/O card. It must be keyed and wired to match the documented channel configuration.

FUNCTION 1
40-pin mass terminal block

FUNCTION 2
30 V DC field-power rating

FUNCTION 3
I/O-card keying requirement

FUNCTION 4
Industrial environmental ratings

RELATED APTER POWER PAGES

1 IS200DSPXH1DBD

2 IS2020JPDCG1ACB

3 IS200EISBH1AAA

4 WESDAC-D20ME-GE-HARRIS-820-0468-01

5 IS200BPIBG1A-C2-A0IS200BPIBG1AEB

6 IS200ECGIH1A-C2-A0IS200ECGIH1APR2

7 IC693CPU374-158503

8 IC695ALG312

9 IC695ALG508

MATCHED SYSTEM COMPONENTS

NO.	DOCUMENTED COMPONENT / ROLE
01	DeltaV S-series I/O
02	Matched DeltaV S-series I/O carrier, host or field hardware
03	Documented power, signal, keyed termination and protective interfaces
04	Approved DeltaV configuration and acceptance record

SYSTEM INTEGRATION RECORD

INTEGRATION ITEM	DOCUMENTED ROLE
Host identity	DeltaV S-series I/O
Interface role	mass termination of documented S-series I/O field conductors through a keyed 40-pin interface
Field / power context	documented DeltaV S-series I/O installation
Commissioning evidence	Identity, keying, wiring, diagnostics and controlled response recorded

INSTALLATION AND CONNECTION CHECKS

- 1

Verify the complete KJ4006X1-BP1 identity and the documented DeltaV S-series 40-Pin Mass Terminal Block execution before work.
- 2
- Place the connected process and DeltaV hardware in the approved maintenance state.

3

4

Technical Specifications

Technical values below are limited to the named product record and directly applicable manufacturer documentation.

PARAMETER	VALUE	PARAMETER	VALUE
Product designation	S-series 40-pin mass terminal block	Relative humidity	5 to 95%, non-condensing
Field power	30 V DC at 3.2 A	Airborne contaminants	ISA-S71.04 Class G3
Ambient temperature	-40 to +70 deg C	Keying requirement	Set keys for the installed I/O card
Shock	10 g half-sine for 11 ms	Addition/removal with system power	Only with no I/O card and no field wiring
Vibration, 2-13.2 Hz	1 mm peak-to-peak	Maintenance	No user-serviceable parts; calibration not required
Vibration, 13.2-150 Hz	0.7 g	Hazardous-area marking	ATEX / IECEx Zone 2 component

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5 IS200BPIBG1A-C2-A0IS200BPIBG1AEB

6 IS200ECGIH1A-C2-A0IS200ECGIH1APR2

7 IC693CPU374-158503

8 IC695ALG312

9 IC695ALG508

ENGINEERING BOUNDARY

Confirm KJ4006X1-BP1, its exact DeltaV S-series I/O host, installed position, keying, power, wiring, configuration and controlled acceptance evidence before release.

PRE-INSTALLATION CHECKS

- 1

Verify the complete KJ4006X1-BP1 identity and the documented DeltaV S-series 40-Pin Mass Terminal Block execution before work.
- 2

Place the connected process and DeltaV hardware in the approved maintenance state.
- 3

Isolate every documented power, signal and stored-energy source before disconnecting hardware.
- 4

Record the installed carrier position, keyed interfaces, terminals, conductors and active configuration.

Applications and Engineering Context

Mass-terminated field wiring

Connects the documented multi-conductor field interface to S-series I/O.

I/O cabinet replacement

Preserves the exact keying, pin map and conductor identification.

Prewired terminal installation

Allows controlled terminal preparation before the matched I/O card is installed.

Channel acceptance testing

Verifies every mapped conductor and assigned channel before release.

TECHNICAL NOTE

Install KJ4006X1-BP1 only in the documented DeltaV S-series I/O arrangement. Preserve identity, keying, wiring and configuration, then verify diagnostics and mass termination of documented S-series I/O field conductors through a keyed 40-pin interface under controlled conditions.

MODEL-SPECIFIC ACCEPTANCE EVIDENCE

CHECK	ACCEPTANCE CONDITION
Identity	KJ4006X1-BP1 recorded
Host / carrier	DeltaV S-series I/O
Physical interface	Installed terminals, conductors and signal paths matched to the as-found record
Configuration	Approved hardware and DeltaV configuration restored
Functional proof	Controlled test confirms mass termination of documented S-series I/O field conductors through a keyed 40-pin interface

IMPORTANT NOTICE

Confirm KJ4006X1-BP1, its exact DeltaV S-series I/O host, installed position, keying, power, wiring, configuration and controlled acceptance evidence before release.

Related Products and Source Record

Brand-qualified Apter Power navigation links are provided for system planning only; manufacturer evidence remains the named source record.

RELATED PRODUCTS ON APTER POWER

1

IS200DSPXH1DBD

View IS200DSPXH1DBD page

2

IS2020JPDCG1ACB

View IS2020JPDCG1ACB page

3

IS200EISBH1AAA

View IS200EISBH1AAA page

4

WESDAC-D20ME-GE-HARRIS-820-0468-00

View WESDAC-D20ME-GE-HARRIS-820-0468-00 page

5

IS200BPIBG1A-C2-A0IS200BPIBG1AEB

View IS200BPIBG1A-C2-A0IS200BPIBG1AEB page

6

IS200ECGIH1A-C2-A0IS200ECGIH1APR2

View IS200ECGIH1A-C2-A0IS200ECGIH1APR2 page

7

IC693CPU374-158503

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IC695ALG312

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IC695ALG508

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MANUFACTURER SOURCE RECORD

Emerson ATEX74 instruction sheet for KJ4006X1-BP1 | Manufacturer evidence verified 29 September 2026

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RECOMMENDATION SCOPE

These links support product navigation and comparison. Technical values in this document are sourced only from the manufacturer record above.