

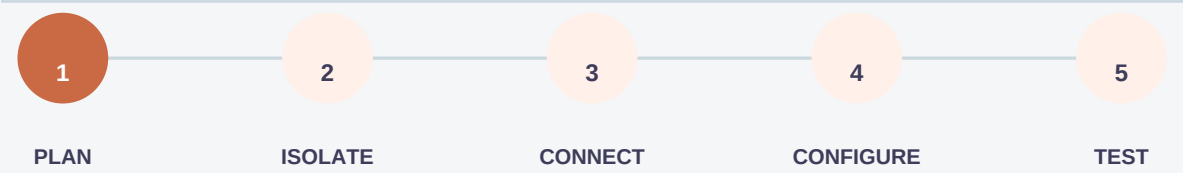
KJ4006X1-BP1

DeltaV S-series 40-Pin Mass Terminal Block

CONTROLLED ENGINEERING COPY

Emerson KJ4006X1-BP1 is a DeltaV S-series 40-pin mass terminal block for the assigned I/O card. It must be keyed and wired to match the documented channel configuration.

INSTALLATION IDENTITY	REQUIRED VALUE
Complete model	KJ4006X1-BP1
Manufacturer	Emerson
System family	DeltaV S-series I/O
Record scope	Installation, application and verification



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01 PRE-INSTALLATION PLAN

- 1

Verify the complete KJ4006X1-BP1 identity and the documented DeltaV S-series 40-Pin Mass Terminal Block execution before work.
- 2

Place the connected process and DeltaV hardware in the approved maintenance state.
- 3

Isolate every documented power, signal and stored-energy source before disconnecting hardware.
- 4

Record the installed carrier position, keyed interfaces, terminals, conductors and active configuration.
- 5

Use ESD controls and protect keyed connectors, electronic contacts and field wiring during handling.

MATCHED HARDWARE

- Emerson KJ4006X1-BP1 with complete article identity
- Matched DeltaV S-series I/O host or companion hardware
- Documented power, signal, keyed and protective interfaces
- Approved configuration, diagnostics and acceptance record

INSTALLATION CONTROL RECORD

CONTROL ITEM	REQUIRED RECORD
Identity	KJ4006X1-BP1 recorded
Physical interface	Installed terminals, conductors and signal paths matched to the as-found record
Configuration	Approved hardware and DeltaV configuration restored
Functional proof	Controlled test confirms mass termination of documented S-series I/O field conductors through a keyed 40-pin interface

02 WIRING & INTERFACE CHECKS

CHECK ITEM	REQUIRED VALUE
Product designation	S-series 40-pin mass terminal block
Field power	30 V DC at 3.2 A
Ambient temperature	-40 to +70 deg C
Shock	10 g half-sine for 11 ms
Vibration, 2-13.2 Hz	1 mm peak-to-peak
Vibration, 13.2-150 Hz	0.7 g
Relative humidity	5 to 95%, non-condensing
Airborne contaminants	ISA-S71.04 Class G3
Keying requirement	Set keys for the installed I/O card
Addition/removal with system power	Only with no I/O card and no field wiring
Maintenance	No user-serviceable parts; calibration not required
Hazardous-area marking	ATEX / IECEx Zone 2 component

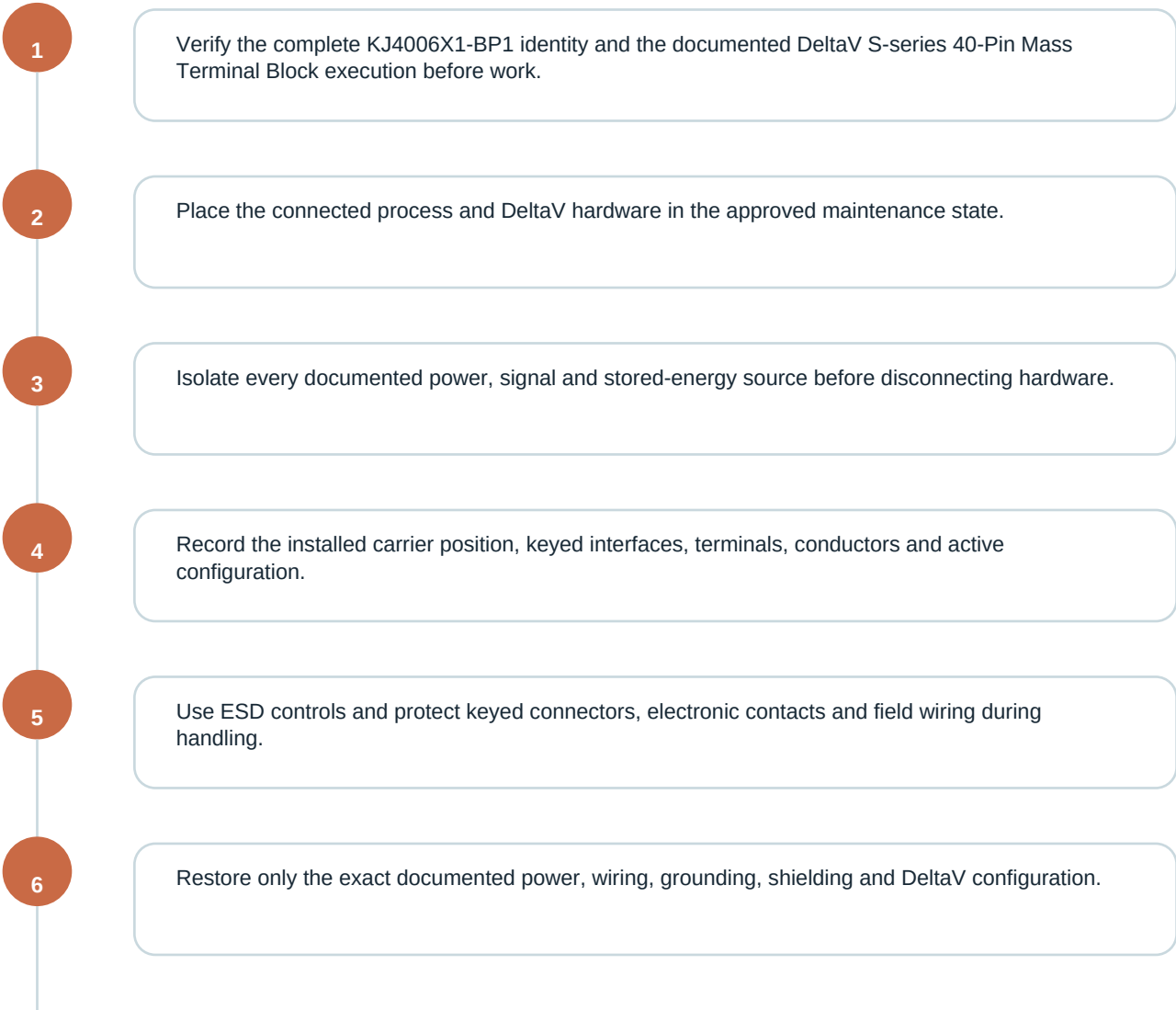
RELATED PRODUCT PAGES

1 IC693PTM101A	2 IC695CMX128	3 IC693CPU363-DL
4 IC200MDL940F	5 IC660ERD024C1	6 IC693ALG442C
7 IC693DNM200-BC	8 DS3800NERA1P1D	9 IS200SRDH2ACB

FIELD NOTE

Confirm KJ4006X1-BP1, its exact DeltaV S-series I/O host, installed position, keying, power, wiring, configuration and controlled acceptance evidence before release.

03 COMMISSIONING WORKFLOW



COMMISSIONING ACCEPTANCE EVIDENCE

EVIDENCE ITEM	ACCEPTANCE CONDITION
Identity	KJ4006X1-BP1
Physical interface	Installed terminals, conductors and signal paths matched to the as-found record
Configuration	Approved hardware and DeltaV configuration restored
Functional proof	Controlled test confirms mass termination of documented S-series I/O field conductors through a keyed 40-pin interface

04 RETURN-TO-SERVICE RECORD

- ☐ Use ESD controls and protect keyed connectors, electronic contacts and field wiring during handling.
- ☐ Restore only the exact documented power, wiring, grounding, shielding and DeltaV configuration.
- ☐ Energize in a controlled sequence and review device, carrier, controller and process diagnostics.
- ☐ Function-test mass termination of documented S-series I/O field conductors through a keyed 40-pin interface and retain the acceptance record before release.

VERIFICATION RECORD

PRODUCT IDENTITY	KJ4006X1-BP1
INSTALLED POSITION	Documented DeltaV S-series I/O position
VERIFICATION METHOD	Controlled inspection and functional test
RELEASE RECORD	Final diagnostic state retained before service

FUNCTIONAL ACCEPTANCE CHECKLIST

- ☐ Use ESD controls and protect keyed connectors, electronic contacts and field wiring during handling.
- ☐ Restore only the exact documented power, wiring, grounding, shielding and DeltaV configuration.
- ☐ Energize in a controlled sequence and review device, carrier, controller and process diagnostics.
- ☐ Function-test mass termination of documented S-series I/O field conductors through a keyed 40-pin interface and retain the acceptance record before release.

SOURCE IDENTIFIERS

Emerson ATEX74 instruction sheet for KJ4006X1-BP1 | Manufacturer evidence verified 29 September 2026