

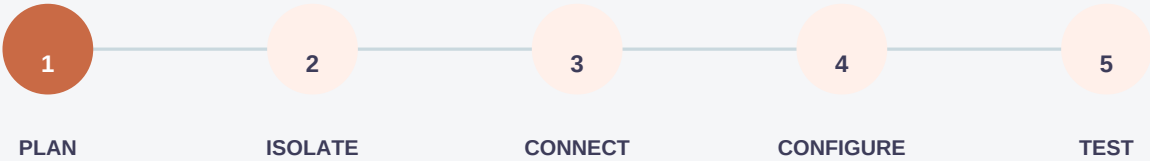
IC695CPU315

PACSystems RX3i CPU315 Controller

CONTROLLED ENGINEERING COPY

Emerson IC695CPU315 is a 1 GHz PACSystems RX3i CPU with 20 MB battery-backed and non-volatile user memory. It supports dual backplane buses and embedded RS-232/RS-485 communications.

INSTALLATION IDENTITY	REQUIRED VALUE
Complete model	IC695CPU315
Manufacturer	Emerson
System family	PACSystems RX3i
Record scope	Installation, application and verification



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01 PRE-INSTALLATION PLAN

- 1

Confirm the complete IC695CPU315 marking, module designation and documented RX3i installation position.
- 2

Place the controller, machine and connected process in the approved maintenance state.
- 3

Isolate controller and field power, prove the safe state and apply ESD controls before handling.
- 4

Record the slot, terminal block, every point assignment, common, shield and connected circuit before removal.
- 5

Verify the official electrical and configuration values documented for IC695CPU315; do not infer adjacent catalog executions.

MATCHED HARDWARE

- IC695CPU315 with complete catalog identity
- Matched RX3i backplane or carrier
- Documented terminal block and field interfaces
- Approved project, diagnostics and acceptance record

INSTALLATION CONTROL RECORD

CONTROL ITEM	REQUIRED RECORD
Identity	IC695CPU315 recorded
Physical interface	Installed terminal assembly, field power and point assignments matched to the as-found record
Configuration	Approved hardware, firmware and engineering configuration restored
Functional proof	Controlled test confirms real-time CPU execution and dual-bus RX3i backplane control

02 WIRING & INTERFACE CHECKS

CHECK ITEM	REQUIRED VALUE
Processor	1 GHz Celeron M
Battery-backed user memory	20 MB
Non-volatile flash user memory	20 MB
Discrete memory	32K bits each for %I and %Q
Analog memory	Up to 32K words each for %AI and %AQ
Program blocks	Up to 512; 128 KB maximum per block
Power requirement	+3.3 VDC at 1.0 A nominal; +5 VDC at 1.2 A nominal
Operating temperature	0 to 60 °C
Embedded communications	RS-232 and RS-485
Serial protocols	Modbus RTU Slave, SNP and Serial I/O
Backplane	RX3i PCI and high-speed serial bus
Time-of-day clock drift	2 seconds per day maximum

RELATED PRODUCT PAGES

1

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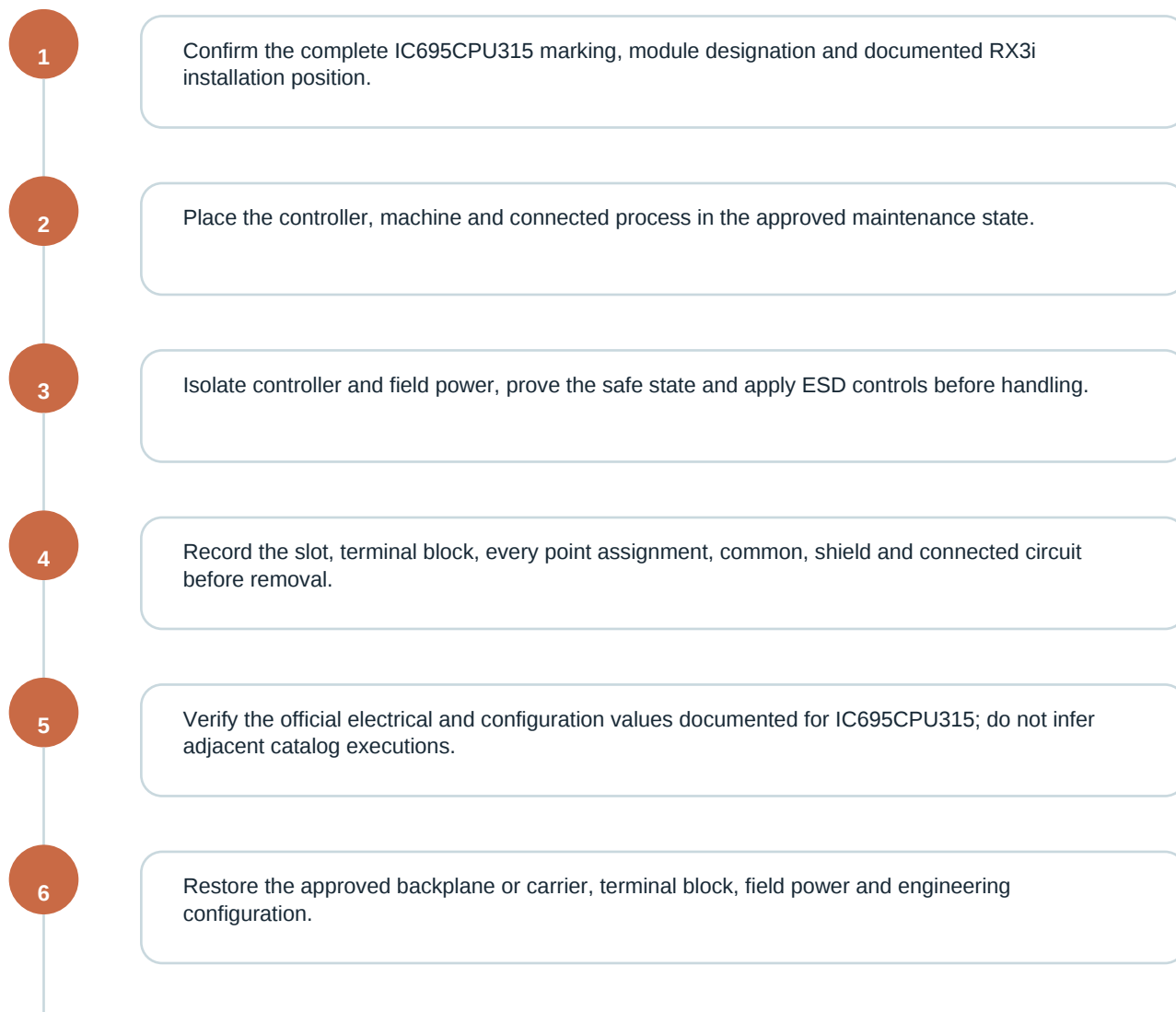
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[General Electric IS200STAOH2AAA](#)

FIELD NOTE

Confirm IC695CPU315, the complete RX3i host, hardware revision, slot, terminal block, field power, wiring, grounding, shielding, configuration, diagnostics and controlled acceptance evidence before release.

03 COMMISSIONING WORKFLOW



COMMISSIONING ACCEPTANCE EVIDENCE

EVIDENCE ITEM	ACCEPTANCE CONDITION
Identity	IC695CPU315
Physical interface	Installed terminal assembly, field power and point assignments matched to the as-found record
Configuration	Approved hardware, firmware and engineering configuration restored
Functional proof	Controlled test confirms real-time CPU execution and dual-bus RX3i backplane control

04 RETURN-TO-SERVICE RECORD

- ☐ Verify the official electrical and configuration values documented for IC695CPU315; do not infer adjacent catalog executions.
- ☐ Restore the approved backplane or carrier, terminal block, field power and engineering configuration.
- ☐ Energize in the manufacturer sequence and review CPU, bus, module and channel diagnostics.
- ☐ Complete a controlled signal, output or controller-response test before service release.

VERIFICATION RECORD

PRODUCT IDENTITY	IC695CPU315
INSTALLED POSITION	Documented PACSystems RX3i slot or distributed-I/O carrier position
VERIFICATION METHOD	Controlled inspection and functional test
RELEASE RECORD	Final diagnostic state retained before service

FUNCTIONAL ACCEPTANCE CHECKLIST

- ☐ Verify the official electrical and configuration values documented for IC695CPU315; do not infer adjacent catalog executions.
- ☐ Restore the approved backplane or carrier, terminal block, field power and engineering configuration.
- ☐ Energize in the manufacturer sequence and review CPU, bus, module and channel diagnostics.
- ☐ Complete a controlled signal, output or controller-response test before service release.

SOURCE IDENTIFIERS

Emerson Important Product Information GFK-2586U | Emerson official product and PACSystems documentation, accessed 24 September 2026