

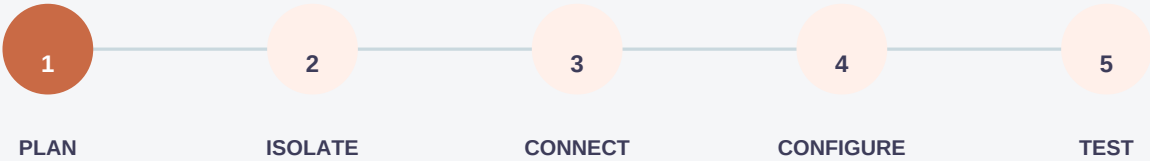
IC695CPU320

PACSystems RX3i CPU320 Controller

CONTROLLED ENGINEERING COPY

Emerson IC695CPU320 is a 1 GHz PACSystems RX3i CPU with 64 MB user memory. It operates on the RX3i dual backplane bus and supports legacy serial communications for controlled-system integration.

INSTALLATION IDENTITY	REQUIRED VALUE
Complete model	IC695CPU320
Manufacturer	Emerson
System family	PACSystems RX3i
Record scope	Installation, application and verification



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01 PRE-INSTALLATION PLAN

- 1

Confirm the complete IC695CPU320 marking, module designation and documented RX3i installation position.
- 2

Place the controller, machine and connected process in the approved maintenance state.
- 3

Isolate controller and field power, prove the safe state and apply ESD controls before handling.
- 4

Record the slot, terminal block, every point assignment, common, shield and connected circuit before removal.
- 5

Verify the official electrical and configuration values documented for IC695CPU320; do not infer adjacent catalog executions.

MATCHED HARDWARE

- IC695CPU320 with complete catalog identity
- Matched RX3i backplane or carrier
- Documented terminal block and field interfaces
- Approved project, diagnostics and acceptance record

INSTALLATION CONTROL RECORD

CONTROL ITEM	REQUIRED RECORD
Identity	IC695CPU320 recorded
Physical interface	Installed terminal assembly, field power and point assignments matched to the as-found record
Configuration	Approved hardware, firmware and engineering configuration restored
Functional proof	Controlled test confirms high-capacity CPU execution and dual-bus RX3i backplane control

02 WIRING & INTERFACE CHECKS

CHECK ITEM	REQUIRED VALUE
Processor	1 GHz
User memory	64 MB
Power requirement	+3.3 VDC at 1.0 A nominal; +5 VDC at 1.2 A nominal
Embedded communications	RS-232 and RS-485
Backplane	RX3i PCI and high-speed serial bus
Compatible Genius gateway	IC695GCG001 with firmware 8.15 or later
Program environment	PACSystems RX3i
Battery retention	Supported with approved RX3i battery packs

RELATED PRODUCT PAGES

1 Emerson KJ3221X1-BA1-12P2531X122-VE4003S2B

2 Emerson KJ4001X1-BE1-12P0818X072-VE4050S2K

3 Emerson KJ2005X1-BK1-C2-A0SE3007

4 General Electric IC698CPE010

5 General Electric IC697MEM715

6 General Electric IC752SPL013

7 Emerson KC4011X1-BZ1

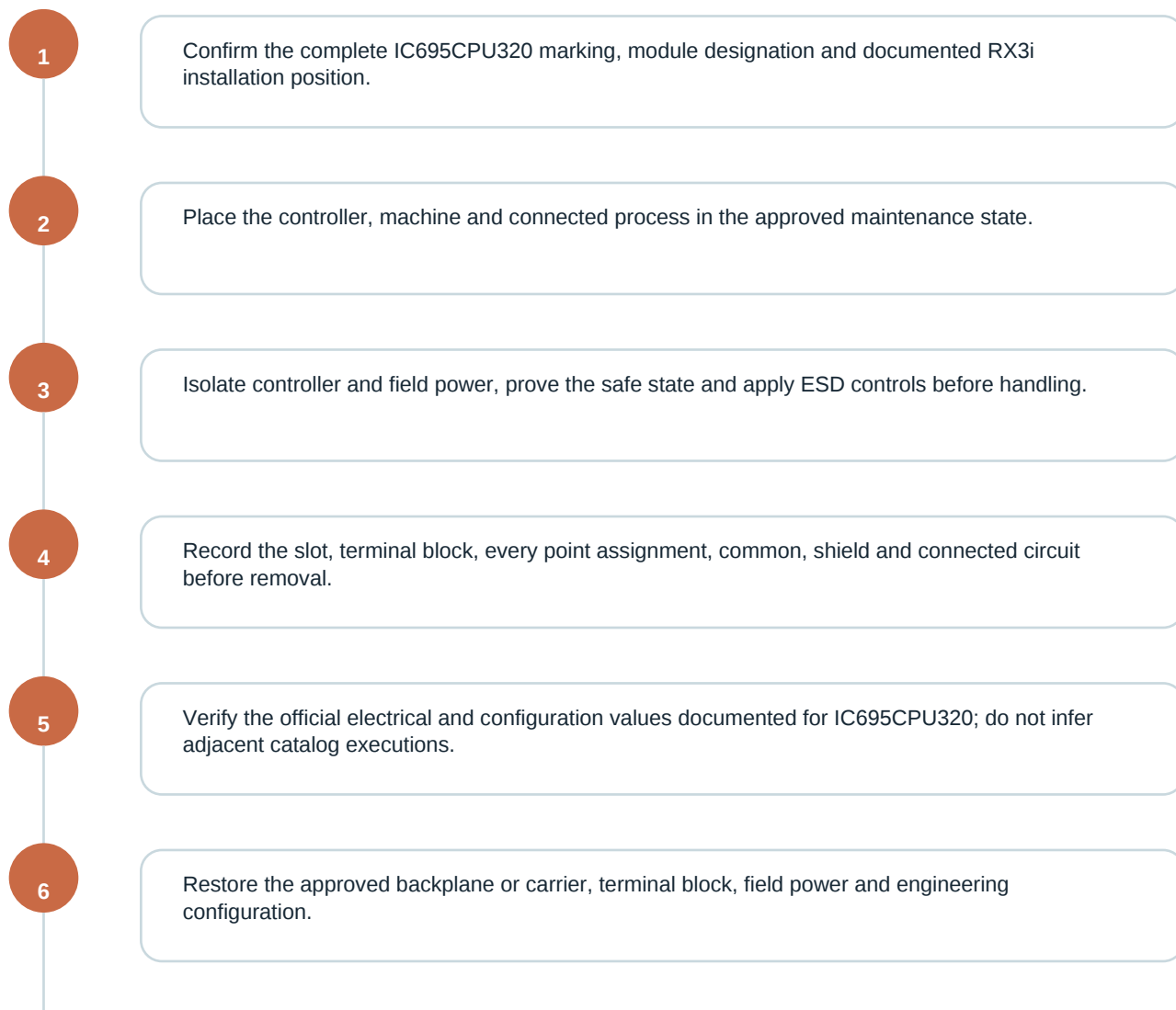
8 Emerson KJ2201X1-HA1

9 Emerson KJ2201X1-BA1

FIELD NOTE

Confirm IC695CPU320, the complete RX3i host, hardware revision, slot, terminal block, field power, wiring, grounding, shielding, configuration, diagnostics and controlled acceptance evidence before release.

03 COMMISSIONING WORKFLOW



COMMISSIONING ACCEPTANCE EVIDENCE

EVIDENCE ITEM	ACCEPTANCE CONDITION
Identity	IC695CPU320
Physical interface	Installed terminal assembly, field power and point assignments matched to the as-found record
Configuration	Approved hardware, firmware and engineering configuration restored
Functional proof	Controlled test confirms high-capacity CPU execution and dual-bus RX3i backplane control

04 RETURN-TO-SERVICE RECORD

- ☐ Verify the official electrical and configuration values documented for IC695CPU320; do not infer adjacent catalog executions.
- ☐ Restore the approved backplane or carrier, terminal block, field power and engineering configuration.
- ☐ Energize in the manufacturer sequence and review CPU, bus, module and channel diagnostics.
- ☐ Complete a controlled signal, output or controller-response test before service release.

VERIFICATION RECORD

PRODUCT IDENTITY	IC695CPU320
INSTALLED POSITION	Documented PACSystems RX3i slot or distributed-I/O carrier position
VERIFICATION METHOD	Controlled inspection and functional test
RELEASE RECORD	Final diagnostic state retained before service

FUNCTIONAL ACCEPTANCE CHECKLIST

- ☐ Verify the official electrical and configuration values documented for IC695CPU320; do not infer adjacent catalog executions.
- ☐ Restore the approved backplane or carrier, terminal block, field power and engineering configuration.
- ☐ Energize in the manufacturer sequence and review CPU, bus, module and channel diagnostics.
- ☐ Complete a controlled signal, output or controller-response test before service release.

SOURCE IDENTIFIERS

Emerson PACSystems RX3i System Manual GFK-2314AB | Emerson official product and PACSystems documentation, accessed 24 September 2026