APTER POWER

COMPLETE PRODUCT INFORMATION

IC695CPU315

PACSystems RX3i CPU315 Controller

Emerson IC695CPU315 is a 1 GHz PACSystems RX3i CPU with 20 MB battery-backed and non-volatile user memory. It supports dual backplane buses and embedded RS-232/RS-485

PRODUCT SNAPSHOT

BRAND Emerson	FAMILY PACSystems RX3i
PART IC695CPU315	DOCUMENT STATUS Controlled engineering copy

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WHATSAPP / PHONE

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PRODUCT OVERVIEW

IC695CPU315 provides real-time CPU execution and dual-bus RX3i backplane control for a matched PACSystems RX3i installation.

- Exact catalog identity IC695CPU315
- PACSystems RX3i CPU315 Controller
- Documented role: real-time CPU execution and dual-bus RX3i backplane control
- Controlled installation, diagnostics and functional verification

RELATED APTER POWER PAGES

1. Emerson 1C31166G02
2. Emerson KJ3003X1-EA1
3. Emerson KJ4006X1-BD1
4. General Electric IS200ECTBG1A
5. General Electric IS200EMIOH1ACA
6. General Electric IS200STAOH2AAA
7. General Electric IS200TBTCH1BBB
8. General Electric IS200TRTDH1CCC-594440
9. General Electric IS200ACLEH1BCB

SYSTEM COMPONENTS

DOCUMENTED COMPONENT	ROLE IN SYSTEM
Proximity probe	Matched extension cable
Proximitor Sensor	AISI 4140 target

ENGINEERING CONTEXT

The component remains matched to the documented RX3i slot or carrier, terminal assembly, field power and engineering configuration. Commissioning preserves the installed architecture and verifies controlled response.

TECHNICAL DATA

PARAMETER	VALUE
Processor	1 GHz Celeron M
Battery-backed user memory	20 MB
Non-volatile flash user memory	20 MB
Discrete memory	32K bits each for %I and %Q
Analog memory	Up to 32K words each for %AI and %AQ
Program blocks	Up to 512; 128 KB maximum per block

PARAMETER	VALUE
Power requirement	+3.3 VDC at 1.0 A nominal; +5 VDC at 1.2 A nominal
Operating temperature	0 to 60 °C
Embedded communications	RS-232 and RS-485
Serial protocols	Modbus RTU Slave, SNP and Serial I/O
Backplane	RX3i PCI and high-speed serial bus
Time-of-day clock drift	2 seconds per day maximum

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TECHNICAL NOTE

Install IC695CPU315 only in the documented PACSystems RX3i position. Preserve the as-found terminal, point and configuration records; then verify diagnostics and real-time CPU execution and dual-bus RX3i backplane control under controlled conditions.

APPLICATIONS & PRECAUTIONS

COMMON APPLICATIONS

PACSystems RX3i planned maintenance

Restores real-time CPU execution and dual-bus RX3i backplane control in the documented control

PACSystems RX3i CPU315 Controller replacement

Uses exact IC695CPU315 identity instead of an adjacent I/O or CPU execution.

Control-system refurbishment

Preserves the installed slot, terminal block, point map, power and engineering configuration.

Controlled recommissioning

Confirms diagnostics and the affected signal or control function before automatic operation.

PRODUCT PRECAUTIONS

1. Confirm the complete IC695CPU315 marking, module designation and documented RX3i installation position.
2. Place the controller, machine and connected process in the approved maintenance state.
3. Isolate controller and field power, prove the safe state and apply ESD controls before handling.
4. Record the slot, terminal block, every point assignment, common, shield and connected circuit before removal.
5. Verify the official electrical and configuration values documented for IC695CPU315; do not infer adjacent
6. Restore the approved backplane or carrier, terminal block, field power and engineering configuration.
7. Energize in the manufacturer sequence and review CPU, bus, module and channel diagnostics.
8. Complete a controlled signal, output or controller-response test before service release.

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ENGINEER FAQ

Q1 How is IC695CPU315 distinguished from adjacent RX3i catalog numbers?

Match the complete IC695CPU315 marking, the PACSystems RX3i CPU315 Controller designation and its recorded slot or carrier position.

Q2 What function is assigned to IC695CPU315 in the documented system?

The module provides real-time CPU execution and dual-bus RX3i backplane control in the matched PACSystems RX3i installation.

Q3 What is the official processor for IC695CPU315?

Emerson documents processor as 1 GHz Celeron M; verify that value against the installed execution before work.

Q4 Which battery-backed user memory applies to IC695CPU315?

The official value is 20 MB. Preserve the corresponding power, signal or controller configuration.

Q5 How should the documented non-volatile flash user memory be checked on IC695CPU315?

Confirm non-volatile flash user memory is 20 MB and compare the as-found wiring or configuration with the approved record.

Q6 What evidence is needed for the discrete memory of IC695CPU315?

Record the installed condition and verify the official value 32K bits each for %I and %Q during controlled commissioning.

Q7 What must be preserved before replacing IC695CPU315?

Record the slot or carrier, terminal assembly, point assignments, wiring, commons, shields, firmware or configuration state and active diagnostics.

Q8 How is IC695CPU315 accepted after installation?

Restore the documented hardware and point map, review module and controller diagnostics, and perform a controlled test of real-time CPU execution and dual-bus RX3i backplane control.

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SOURCE Emerson Important Product Information GFK-2586U | Emerson official product and PACSystems documentation, accessed 24 September 2026

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