

INDUSTRIAL AUTOMATION PRODUCT

IC695CPU315

PACSystems RX3i CPU315 Controller

Emerson IC695CPU315 is a 1 GHz PACSystems RX3i CPU with 20 MB battery-backed and non-volatile user memory. It supports dual backplane buses and embedded RS-232/RS-485 communications.

CONTROLLED ENGINEERING COPY

IDENTITY	VERIFIED VALUE
Manufacturer	Emerson
System family	PACSystems RX3i
Part number	IC695CPU315
Document status	Controlled product and system reference

DOCUMENT SCOPE

Exact article identity, official technical data, host matching, installation checks, applications, precautions and controlled acceptance evidence.

MODEL-SPECIFIC ENGINEERING NOTE

Install IC695CPU315 only in the documented PACSystems RX3i position. Preserve the as-found terminal, point and configuration records; then verify diagnostics and real-time CPU execution and dual-bus RX3i backplane control under controlled conditions.

System Role and Architecture

Emerson IC695CPU315 is a 1 GHz PACSystems RX3i CPU with 20 MB battery-backed and non-volatile user memory. It supports dual backplane buses and embedded RS-232/RS-485 communications.

FUNCTION 1
Exact catalog identity IC695CPU315

FUNCTION 2
PACSystems RX3i CPU315 Controller

FUNCTION 3
Documented role: real-time CPU execution and dual-bus RX3i backplane control

FUNCTION 4
Controlled installation, diagnostics and functional verification

RELATED APTER POWER PAGES		
1 General Electric IS200ECTBG1A	2 General Electric IS200EMIOH1ACA	3 General Electric IS200STAOH2AAA
4 General Electric IS200BTCH1BBB	5 General Electric IS200TRTDH1CCC-594440	6 General Electric IS200ACLEH1BCB
7 Emerson 1C31166G02	8 Emerson KJ3003X1-EA1	9 Emerson KJ4006X1-BD1

MATCHED SYSTEM COMPONENTS

NO.	DOCUMENTED COMPONENT / ROLE
01	PACSystems RX3i
02	Matched RX3i backplane, carrier or terminal block
03	Documented field wiring, commons, shields and point map
04	Approved controller project and acceptance record

SYSTEM INTEGRATION RECORD

INTEGRATION ITEM	DOCUMENTED ROLE
Host identity	PACSystems RX3i
Interface role	real-time CPU execution and dual-bus RX3i backplane control
Field / power context	documented PACSystems RX3i backplane or distributed-I/O position
Commissioning evidence	Identity, electrical interfaces, configuration, diagnostics and controlled functional response recorded

INSTALLATION AND CONNECTION CHECKS

- 1

Confirm the complete IC695CPU315 marking, module designation and documented RX3i installation position.
- 2
- Place the controller, machine and connected process in the approved maintenance state.

3

4

Technical Specifications

Technical values below are limited to the named product record and directly applicable manufacturer documentation.

PARAMETER	VALUE	PARAMETER	VALUE
Processor	1 GHz Celeron M	Power requirement	+3.3 VDC at 1.0 A nominal; +5 VDC at 1.2 A nominal
Battery-backed user memory	20 MB	Operating temperature	0 to 60 °C
Non-volatile flash user memory	20 MB	Embedded communications	RS-232 and RS-485
Discrete memory	32K bits each for %I and %Q	Serial protocols	Modbus RTU Slave, SNP and Serial I/O
Analog memory	Up to 32K words each for %AI and %AQ	Backplane	RX3i PCI and high-speed serial bus
Program blocks	Up to 512; 128 KB maximum per block	Time-of-day clock drift	2 seconds per day maximum

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9Emerson KJ4006X1-BD1

ENGINEERING BOUNDARY

Confirm IC695CPU315, the complete RX3i host, hardware revision, slot, terminal block, field power, wiring, grounding, shielding, configuration, diagnostics and controlled acceptance evidence before release.

PRE-INSTALLATION CHECKS

- 1

Confirm the complete IC695CPU315 marking, module designation and documented RX3i installation position.
- 2

Place the controller, machine and connected process in the approved maintenance state.
- 3

Isolate controller and field power, prove the safe state and apply ESD controls before handling.
- 4

Record the slot, terminal block, every point assignment, common, shield and connected circuit before removal.

Applications and Engineering Context

PACSystems RX3i planned maintenance

Restores real-time CPU execution and dual-bus RX3i backplane control in the documented control architecture.

PACSystems RX3i CPU315 Controller replacement

Uses exact IC695CPU315 identity instead of an adjacent I/O or CPU execution.

Control-system refurbishment

Preserves the installed slot, terminal block, point map, power and engineering configuration.

Controlled recommissioning

Confirms diagnostics and the affected signal or control function before automatic operation.

TECHNICAL NOTE

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MODEL-SPECIFIC ACCEPTANCE EVIDENCE

CHECK	ACCEPTANCE CONDITION
Identity	IC695CPU315 recorded
Host / carrier	PACSystems RX3i
Physical interface	Installed terminal assembly, field power and point assignments matched to the as-found record
Configuration	Approved hardware, firmware and engineering configuration restored
Functional proof	Controlled test confirms real-time CPU execution and dual-bus RX3i backplane control

IMPORTANT NOTICE

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Related Products and Source Record

Brand-qualified Apter Power navigation links are provided for system planning only; manufacturer evidence remains the named source record.

RELATED PRODUCTS ON APTER POWER

1

General Electric IS200ECTBG1A

View General Electric IS200ECTBG1A page

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Emerson KJ4006X1-BD1

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MANUFACTURER SOURCE RECORD

Emerson Important Product Information GFK-2586U | Emerson official product and PACSystems documentation, accessed 24 September 2026

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RECOMMENDATION SCOPE

These links support product navigation and comparison. Technical values in this document are sourced only from the manufacturer record above.