

INDUSTRIAL AUTOMATION PRODUCT

B&R 3CP360.60-1

System 2005 CPU

B&R; 3CP360.60-1 is a System 2005 CPU built around a Pentium 266 processor with DRAM, SRAM and removable CompactFlash application memory. Its integrated RS232, Ethernet and USB ports coordinate controller logic, communication and supported aPCI expansion.

IDENTITY	VERIFIED VALUE
Manufacturer	B&R;
Complete model	B&R; 3CP360.60-1
Product role	System 2005 CPU
System family	System 2005
Country of origin	Austria

DOCUMENT SCOPE

Exact identity, manufacturer-supported technical data, installation checks, applications, precautions and controlled verification records.

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B&R 3CP360.60-1

Technical Specifications

PARAMETER	VERIFIED VALUE
Processor	Pentium 266
DRAM	32 MB
SRAM	496 kB user RAM; 512 kB stated product memory
Remanent variables	256 kB
Application memory	1 removable CompactFlash slot
Interface IF1	RS232, 9-pin male DSUB, max. 115.2 kbit/s
Interface IF2	10/100 Mbit/s Ethernet, shielded RJ45
Interface IF3	USB 1.1 Type A
aPCI slots	1 for IF7xx interface modules
Shortest task class	400 us
Total power	14 W without memory card or interface module
Protection rating	IP20

Model-Specific Engineering Note

ENGINEERING BOUNDARY

Back up the application, CompactFlash contents, interface settings and retained variables before replacing B&R; 3CP360.60-1. Refit the CPU in base-unit slots 3 and 4, restore the approved aPCI module and prove Ethernet, serial, USB and I/O operation.

Pre-Installation Checks

NO.	REQUIRED CHECK
1	Record the B&R; 3CP360.60-1 hardware revision, firmware and base-unit slot assignment.
2	Back up the CompactFlash application before removing power or the CPU.
3	Preserve the aPCI interface model, settings and cable assignments installed in the single expansion slot.
4	Place the controlled process in its approved safe state before stopping System 2005 execution.
5	Check the lithium backup-battery status before relying on retained SRAM data.

System Role and Architecture

B&R; 3CP360.60-1 is a System 2005 CPU built around a Pentium 266 processor with DRAM, SRAM and removable CompactFlash application memory. Its integrated RS232, Ethernet and USB ports coordinate controller logic, communication and supported aPCI expansion.

FUNCTION 1
Executes System 2005 control tasks while an integrated I/O processor handles background data

FUNCTION 2
Stores the application on removable CompactFlash media

FUNCTION 3
Combines serial, Ethernet and USB service interfaces on the CPU

FUNCTION 4
Accepts one IF7xx aPCI communication or interface module

Common Applications

APPLICATION	ENGINEERING CONTEXT
System 2005 machine controller	B&R; 3CP360.60-1 executes deterministic application logic and coordinates installed I/O.
Ethernet-connected production cell	The shielded 10/100BASE-T port exchanges supervisory and controller data over the documented segment.
Serial equipment integration	The RS232 DSUB interface connects approved serial devices at rates up to 115.2 kbit/s.
aPCI interface expansion	One IF7xx slot adds the project-specific fieldbus or communication interface retained by the system record.

CONTROLLED ACCEPTANCE DUTY
Confirm identity, physical interface, companion hardware, configured values and functional response against the installed-system record before release.

Engineering Precautions

NO.	PRECAUTION
1	Record the B&R; 3CP360.60-1 hardware revision, firmware and base-unit slot assignment.
2	Back up the CompactFlash application before removing power or the CPU.
3	Preserve the aPCI interface model, settings and cable assignments installed in the single expansion slot.
4	Place the controlled process in its approved safe state before stopping System 2005 execution.
5	Check the lithium backup-battery status before relying on retained SRAM data.
6	Label the RS232, Ethernet and USB connections and preserve network parameters.
7	Allow for the documented 14 W CPU load plus the separate memory and interface-module demand.
8	After restoration, prove the application checksum, retained data, I/O scan and every active communication link.

Model-Specific FAQ - Part 1

QUESTION	ANSWER
What memory must be backed up before exchanging B&R; 3CP360.60-1?	The application resides on removable CompactFlash, while user and remanent data use SRAM; preserve both the CompactFlash image and retained values.
Which communication ports are built into B&R; 3CP360.60-1?	The CPU provides RS232 on a 9-pin male DSUB, switched 10/100 Mbit/s Ethernet on RJ45 and one USB 1.1 Type-A port.
How is an additional fieldbus interface added to B&R; 3CP360.60-1?	Use the single insertion slot intended for IF7xx aPCI interface modules and restore the exact documented module and configuration.
Where is B&R; 3CP360.60-1 installed in a System 2005 base unit?	The official data sheet assigns the double-width CPU to base-unit slots 3 and 4 and does not permit it in an expansion unit.

Model-Specific FAQ - Part 2

QUESTION	ANSWER
What task timing is documented for B&R; 3CP360.60-1?	The shortest task class cycle time is 400 microseconds, with a typical instruction cycle time of 0.02 microseconds.
Why should the backup battery be checked on B&R; 3CP360.60-1?	Battery monitoring protects retained SRAM and the nonvolatile real-time clock; B&R; states a lithium-battery life of at least three years.
Which interfaces of B&R; 3CP360.60-1 are electrically isolated?	The data sheet shows IF2 Ethernet isolated from the PLC and from IF1 and IF3, while the RS232 and USB paths are not isolated from the PLC.
What proves successful B&R; 3CP360.60-1 recommissioning?	Verify boot from the approved CompactFlash, compare the application checksum, confirm retained variables and exercise Ethernet, RS232, USB, aPCI and controlled I/O functions.

Related Products on Apter Power

MODEL	APTER POWER PAGE
GE 0552N1QLG132A-06-V1-1	View GE 0552N1QLG132A-06-V1-1 page
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GE 151X1233DB02SA02	View GE 151X1233DB02SA02 page
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GE 269PLUS-100P-120	View GE 269PLUS-100P-120 page
GE 531X111PSHAPG3	View GE 531X111PSHAPG3 page
GE 531X111PSHARG1	View GE 531X111PSHARG1 page

MANUFACTURER SOURCE RECORD

Datasheet 3CP360.60-1
B&R; version 1.0, 2004-06-08

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