

X20CP3585

Redundancy-capable X20 controller

B&R X20CP3585 is a Redundancy-capable X20 controller. It performs the documented X20 System system role in the approved industrial installation.

BRAND	B&R	FAMILY	X20 System
PART	X20CP3585	SYSTEM ROLE	Provides the documented redundancy-capable x20 controller function

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X20CP3585

MANUFACTURER SOURCE RECORD

B&R X20CP3585 product data
B&R product record X20CP3585

Product Overview

SYSTEM FUNCTION

B&R X20CP3585 is a Redundancy-capable X20 controller. It performs the documented X20 System system role in the approved industrial installation.

FUNCTION 1

Provides the documented redundancy-capable x20 controller function

FUNCTION 2

Uses exact B&R article identity X20CP3585

FUNCTION 3

Integrates with the matched X20 System hardware

FUNCTION 4

Supports controlled diagnostics and functional verification

System Components

COMPONENT	DOCUMENTED ROLE
Host / carrier	X20 System
Primary interface	Provides the documented redundancy-capable x20 controller function
Engineering context	The component is used with the exact carrier, terminal, power, signal and network arrangement approved for B&R X20CP3585. Commissioning preserves the installed configuration and verifies controlled system response.
Commissioning duty	Complete a controlled functional test and retain the acceptance result before production release.

TECHNICAL NOTE

Confirm active/standby roles, synchronization state, interface modules, CompactFlash image and project revision before controlled switchover testing.

Technical Data

PARAMETER	VALUE	PARAMETER	VALUE
Processor	Intel Atom; 1 GHz	Application storage	CompactFlash
Main memory	256 MB DDR2 SDRAM	Redundancy	Supported
Modular interface slots	3	Cooling	Fanless
Interfaces	Ethernet; POWERLINK; USB		

Related Products on Apter Power

MODEL	SYSTEM ROLE	APTER POWER PAGE
B&R X20AT6402	Related industrial automation product	View B&R X20AT6402 page
B&R 7CP476.60-1	Related industrial automation product	View B&R 7CP476.60-1 page
B&R 7CP474.60-1	Related industrial automation product	View B&R 7CP474.60-1 page
B&R 8I0IF109.400-2	Related industrial automation product	View B&R 8I0IF109.400-2 page
B&R 8I0IF109.400-1	Related industrial automation product	View B&R 8I0IF109.400-1 page
B&R 5ACPCI.ETH3-01	Related industrial automation product	View B&R 5ACPCI.ETH3-01 page
B&R 5AP1130.185C-000	Related industrial automation product	View B&R 5AP1130.185C-000 page
Bihl+Wiedemann BWU3274	Related industrial automation product	View Bihl+Wiedemann BWU3274 page

ENGINEERING BOUNDARY

Confirm B&R X20CP3585, X20 System host, hardware revision, terminals and connectors, wiring, grounding, configuration, companion accessories and controlled commissioning results before service release.

Applications and Precautions

Common Applications

X20 System system maintenance

Restores the documented redundancy-capable x20 controller function in an engineered installation.

Installed-interface verification

Confirms the documented processor and main memory before process release.

Control-panel modernization

Preserves the approved B&R hardware identity during a controlled system upgrade.

Controlled recommissioning

Verifies configuration, diagnostics and field response after maintenance or replacement.

Product Precautions

- 1

Confirm the complete B&R X20CP3585 marking and installed X20 System position before work.
- 2

Place the machine or process in the approved maintenance state and isolate every applicable energy source.
- 3

Record power, signal, network, terminal and protective-ground connections before removal.
- 4

Use ESD controls and protect keyed connectors, terminals and exposed electronics from contamination.
- 5

Verify processor (Intel Atom; 1 GHz) against the installed project and connected hardware.
- 6

Verify main memory (256 MB DDR2 SDRAM) and restore only the approved configuration.
- 7

Energize in the prescribed sequence and review module, host and network diagnostics.
- 8

Complete a controlled functional test and retain the acceptance result before production release.

IMPORTANT NOTICE

Confirm B&R X20CP3585, X20 System host, hardware revision, terminals and connectors, wiring, grounding, configuration, companion accessories and controlled commissioning results before service release.

ACCEPTANCE CHECK	REQUIRED EVIDENCE
Identity	X20CP3585
Configuration	Energize in the prescribed sequence and review module, host and network diagnostics.
Functional release	Complete a controlled functional test and retain the acceptance result before production release.

Engineer FAQ

Q1 Which X20 System position accepts X20CP3585?

Use the approved X20 System position and confirm the complete B&R X20CP3585 article identity.

Q3 Which value is checked first for X20CP3585?

Check processor: Intel Atom; 1 GHz.

Q5 What should be recorded before removing X20CP3585?

Record every power, signal, network, terminal and configuration assignment.

Q7 How is X20CP3585 recommissioned?

Restore the approved hardware and project, then review diagnostics in the prescribed startup sequence.

Q2 Which documented function does X20CP3585 provide?

It provides the documented redundancy-capable x20 controller role in the configured system.

Q4 Which second interface value governs X20CP3585?

Verify main memory: 256 MB DDR2 SDRAM.

Q6 How is X20CP3585 handled safely?

Isolate applicable energy, apply ESD controls and protect all connectors and terminals.

Q8 What releases X20CP3585 to service?

A controlled functional test with the expected response and no unresolved diagnostics.

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