

X20CP3585

Redundancy-capable X20 controller

B&R X20CP3585 is a Redundancy-capable X20 controller. It performs the documented X20 System system role in the approved industrial installation.

IDENTITY	VERIFIED VALUE
Manufacturer	B&R
System family	X20 System
Part number	X20CP3585
Product role	Provides the documented redundancy-capable x20 controller function
Lifecycle	Installed product record

DOCUMENT SCOPE

Exact-article identity, technical data, host role, installation checks, applications, precautions and engineering verification.

MODEL-SPECIFIC ENGINEERING NOTE

Confirm active/standby roles, synchronization state, interface modules, CompactFlash image and project revision before controlled switchover testing.

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X20CP3585

Technical Specifications

Technical values below are limited to the named B&R product record and directly applicable manufacturer documentation.

PARAMETER	VALUE	PARAMETER	VALUE
Processor	Intel Atom; 1 GHz	Application storage	CompactFlash
Main memory	256 MB DDR2 SDRAM	Redundancy	Supported
Modular interface slots	3	Cooling	Fanless
Interfaces	Ethernet; POWERLINK; USB		

Verified Product Record

DATA GROUP	VERIFIED VALUE
Installed host / carrier	X20 System
Primary system role	Provides the documented redundancy-capable x20 controller function
Connection boundary	Confirm active/standby roles, synchronization state, interface modules, CompactFlash image and project revision before controlled switchover testing.
Controlled acceptance duty	X20 System system maintenance

ENGINEERING BOUNDARY

Confirm B&R X20CP3585, X20 System host, hardware revision, terminals and connectors, wiring, grounding, configuration, companion accessories and controlled commissioning results before service release.

Pre-Installation Checks

1	Confirm the complete B&R X20CP3585 marking and installed X20 System position before work.	2	Place the machine or process in the approved maintenance state and isolate every applicable energy source.
3	Record power, signal, network, terminal and protective-ground connections before removal.	4	Use ESD controls and protect keyed connectors, terminals and exposed electronics from contamination.

Documented Service Record

RECORD ITEM	REQUIRED ENTRY
As-found article	X20CP3585
Installed host	X20 System
Connection record	Capture before removal
Controlled release	Diagnostics and functional proof

System Role and Architecture

B&R X20CP3585 is a Redundancy-capable X20 controller. It performs the documented X20 System system role in the approved industrial installation.

FUNCTION 1
Provides the documented redundancy-capable x20 controller function

FUNCTION 2
Uses exact B&R article identity X20CP3585

FUNCTION 3
Integrates with the matched X20 System hardware

FUNCTION 4
Supports controlled diagnostics and functional verification

Matched System Components

NO.	DOCUMENTED COMPONENT / ROLE
1	X20 System
2	256 MB DDR2 SDRAM
3	X20 System system maintenance
4	Confirm active/standby roles, synchronization state, interface modules, CompactFlash image and project revision before controlled switchover testing.

System Integration Record

INTEGRATION ITEM	DOCUMENTED ROLE
Host identity	X20 System
Interface role	Provides the documented redundancy-capable x20 controller function
Field / power context	The component is used with the exact carrier, terminal, power, signal and network arrangement approved for B&R X20CP3585. Commissioning preserves the installed configuration and verifies controlled system response.
Commissioning evidence	Complete a controlled functional test and retain the acceptance result before production release.

Installation and Connection Checks

1Verify processor (Intel Atom; 1 GHz) against the installed project and connected hardware.

2Verify main memory (256 MB DDR2 SDRAM) and restore only the approved configuration.

3Energize in the prescribed sequence and review module, host and network diagnostics.

4Complete a controlled functional test and retain the acceptance result before production release.

MANUFACTURER SOURCE RECORD

B&R X20CP3585 product data
B&R product record X20CP3585

Applications and Engineering Context

X20 System system maintenance

Restores the documented redundancy-capable x20 controller function in an engineered installation.

Control-panel modernization

Preserves the approved B&R hardware identity during a controlled system upgrade.

Installed-interface verification

Confirms the documented processor and main memory before process release.

Controlled recommissioning

Verifies configuration, diagnostics and field response after maintenance or replacement.

TECHNICAL NOTE

Confirm active/standby roles, synchronization state, interface modules, CompactFlash image and project revision before controlled switchover testing.

Model-Specific Acceptance Evidence

CHECK	ACCEPTANCE CONDITION
Identity	X20CP3585 recorded
Host / carrier	X20 System
Physical interface	Provides the documented redundancy-capable x20 controller function
Configuration	Energize in the prescribed sequence and review module, host and network diagnostics.
Functional proof	Complete a controlled functional test and retain the acceptance result before production release.

IMPORTANT NOTICE

Confirm B&R X20CP3585, X20 System host, hardware revision, terminals and connectors, wiring, grounding, configuration, companion accessories and controlled commissioning results before service release.

Controlled Release Summary

RELEASE ITEM	MODEL-SPECIFIC CONDITION
Article identity	X20CP3585
Approved role	X20 System system maintenance
Final validation	Complete a controlled functional test and retain the acceptance result before production release.

Engineering Precautions

- 1

Confirm the complete B&R X20CP3585 marking and installed X20 System position before work.
- 2

Place the machine or process in the approved maintenance state and isolate every applicable energy source.
- 3

Record power, signal, network, terminal and protective-ground connections before removal.
- 4

Use ESD controls and protect keyed connectors, terminals and exposed electronics from contamination.
- 5

Verify processor (Intel Atom; 1 GHz) against the installed project and connected hardware.
- 6

Verify main memory (256 MB DDR2 SDRAM) and restore only the approved configuration.
- 7

Energize in the prescribed sequence and review module, host and network diagnostics.
- 8

Complete a controlled functional test and retain the acceptance result before production release.

Related Products on Apter Power

MODEL	SYSTEM ROLE	APTER POWER PAGE
B&R 3CP340.60-2	Related industrial automation product	View B&R 3CP340.60-2 page
B&R X67SM2436	Related industrial automation product	View B&R X67SM2436 page
B&R 5MP050.0653-03	Related industrial automation product	View B&R 5MP050.0653-03 page
B&R 80SD100XD.C0XX-21	Related industrial automation product	View B&R 80SD100XD.C0XX-21 page
B&R 7EX290.50-1	Related industrial automation product	View B&R 7EX290.50-1 page
B&R X20AI2237	Related industrial automation product	View B&R X20AI2237 page
B&R X20AT6402	Related industrial automation product	View B&R X20AT6402 page
B&R 7CP476.60-1	Related industrial automation product	View B&R 7CP476.60-1 page

MANUFACTURER SOURCE RECORD

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B&R product record X20CP3585

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