

X20CP1584

X20 controller with integrated POWERLINK

CONTROLLED ENGINEERING COPY

Back up the Automation Studio project and CompactFlash image, then verify interface addresses, POWERLINK node assignments and cycle timing.

INSTALLATION IDENTITY	REQUIRED VALUE
Complete model	X20CP1584
Manufacturer part	X20CP1584
Hardware type	Intel Atom E620T; 0.6 GHz
System family	B&R X20 PLC



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01 PRE-INSTALLATION PLAN

- 1

Verify the full X20CP1584 marking and its assigned B&R X20 PLC position before work.
- 2

Place the connected machine or process in the approved maintenance state.
- 3

Isolate the applicable supply and plc logic, ethernet/powerlink data and x20 i/o field energy before disconnecting conductors.
- 4

Record connector, conductor, address and software identities before removal.
- 5

Use ESD controls and protect keyed interfaces, exposed contacts and electronic assemblies.

MATCHED HARDWARE

- X20 bus base and I/O slices
- 24 V DC controller supply
- CompactFlash application media
- Automation Studio project

INSTALLATION CONTROL RECORD

CONTROL ITEM	REQUIRED RECORD
Identity	X20CP1584 / X20CP1584 recorded
Physical interface	X20 bus base and I/O slices
Companion check	24 V DC controller supply
Functional proof	Uses an Intel Atom E620T processor
Application test	Packaging-machine control

02 WIRING & INTERFACE CHECKS

CHECK ITEM	REQUIRED VALUE
Processor	Intel Atom E620T; 0.6 GHz
Main memory	256 MB DDR2 SDRAM
Nonvolatile memory	1 MB SRAM
Application storage	CompactFlash slot 1
Shortest task class	400 us
Interfaces	RS232; Ethernet; POWERLINK; 2 USB; X2X Link

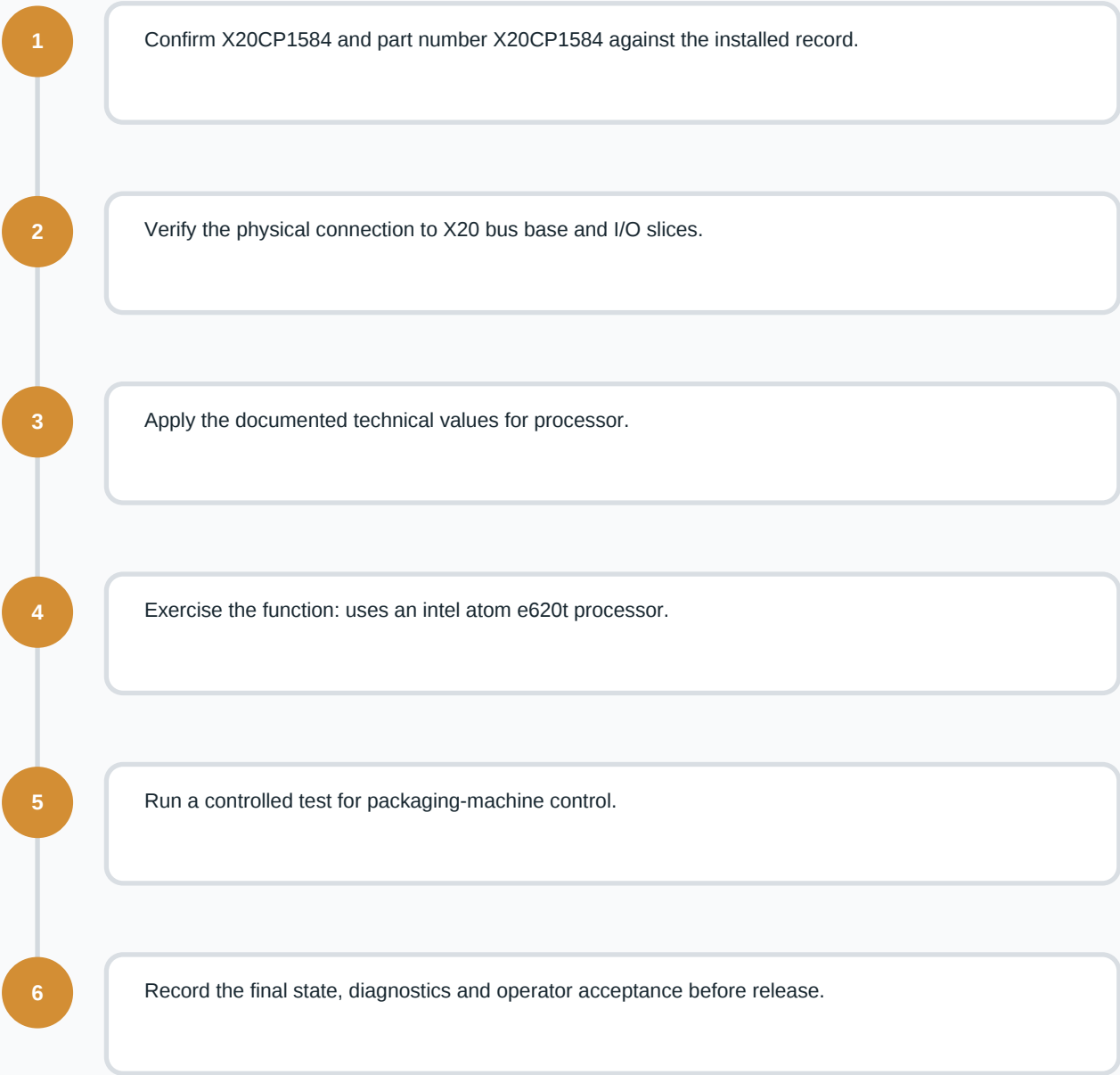
SYSTEM CONNECTION RECORD

CONNECTION ITEM	DOCUMENTED ASSIGNMENT
Installed host / carrier	X20 bus base and I/O slices
Companion hardware	24 V DC controller supply
Field connection	CompactFlash application media
Primary system function	Uses an Intel Atom E620T processor
Controlled acceptance duty	Packaging-machine control

FIELD NOTE

Back up the Automation Studio project and CompactFlash image, then verify interface addresses, POWERLINK node assignments and cycle timing.

03 COMMISSIONING WORKFLOW



COMMISSIONING ACCEPTANCE EVIDENCE

EVIDENCE ITEM	ACCEPTANCE CONDITION
Identity	X20CP1584 / X20CP1584 recorded
Physical interface	X20 bus base and I/O slices
Companion check	24 V DC controller supply
Functional proof	Uses an Intel Atom E620T processor
Application test	Packaging-machine control

04 RETURN-TO-SERVICE RECORD

- ☐
- Confirm the installed X20 bus base and I/O slices and 24 V DC controller supply against the approved project.
- ☐
- Restore power in a controlled sequence and review device, network and host diagnostics.
- ☐
- Prove the intended packaging-machine control function and retain the final test result.
- ☐
- Confirm the intended process response before normal operation.

VERIFICATION RECORD

PRODUCT IDENTITY	X20CP1584
INSTALLED POSITION	Documented B&R X20 PLC position
VERIFICATION METHOD	Controlled inspection and functional test
RELEASE RECORD	Final diagnostic state retained before service

RELATED PRODUCTS ON APTER POWER

MODEL	SYSTEM ROLE	APTER POWER PAGE
6SE7027-2TD84-6HF3	Related industrial automation product	View 6SE7027-2TD84-6HF3 page
6RY1803-0AA25-0AA1	Related industrial automation product	View 6RY1803-0AA25-0AA1 page
6SE7035-1EJ84-1JC1	Related industrial automation product	View 6SE7035-1EJ84-1JC1 page
6SE7033-7EG84-1JF1	Related industrial automation product	View 6SE7033-7EG84-1JF1 page
6SL3352-6TG38-8AA3	Related industrial automation product	View 6SL3352-6TG38-8AA3 page
6SE7041-2WL84-1JC1	Related industrial automation product	View 6SE7041-2WL84-1JC1 page
6SL3120-2TE21-8AC0	Related industrial automation product	View 6SL3120-2TE21-8AC0 page
6RY1803-1DA04	Related industrial automation product	View 6RY1803-1DA04 page

SOURCE IDENTIFIERS

B&R official product data sheet

B&R product data

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