

INDUSTRIAL AUTOMATION PRODUCT

X20CP1584

X20 controller with integrated POWERLINK

PLC logic, Ethernet/POWERLINK data and X20 I/O

IDENTITY	VERIFIED VALUE
Manufacturer	B&R
System family	B&R X20 PLC
Part number	X20CP1584
Country	Austria
HS code	8537101190

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Technical Specifications

Technical values below are limited to the named B&R product record.

PARAMETER	TECHNICAL VALUE
Processor	Intel Atom E620T; 0.6 GHz
Main memory	256 MB DDR2 SDRAM
Nonvolatile memory	1 MB SRAM
Application storage	CompactFlash slot 1
Shortest task class	400 us
Interfaces	RS232; Ethernet; POWERLINK; 2 USB; X2X Link

ENGINEERING DATA SUMMARY

DATA GROUP	VERIFIED ASSIGNMENT
Installed host / carrier	X20 bus base and I/O slices
Companion hardware	24 V DC controller supply
Field connection	CompactFlash application media
Primary system function	Uses an Intel Atom E620T processor
Controlled acceptance duty	Packaging-machine control

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System Role and Architecture

PLC logic, Ethernet/POWERLINK data and X20 I/O

FUNCTION 1

Uses an Intel Atom E620T processor

FUNCTION 2

Provides 256 MB DDR2 application memory

FUNCTION 3

Integrates POWERLINK and X2X Link

FUNCTION 4

Supports CompactFlash application storage

Matched system components

- X20 bus base and I/O slices
- 24 V DC controller supply
- CompactFlash application media
- Automation Studio project

SYSTEM INTEGRATION RECORD

INTEGRATION ITEM	DOCUMENTED ROLE
Installed host / carrier	X20 bus base and I/O slices
Companion hardware	24 V DC controller supply
Field connection	CompactFlash application media
Primary system function	Uses an Intel Atom E620T processor
Controlled acceptance duty	Packaging-machine control

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Applications and Engineering Context

Packaging-machine control

Uses an Intel Atom E620T processor

POWERLINK motion coordination

Provides 256 MB DDR2 application memory

Distributed X20 I/O control

Integrates POWERLINK and X2X Link

Controller replacement

Supports CompactFlash application storage

TECHNICAL NOTE

Back up the Automation Studio project and CompactFlash image, then verify interface addresses, POWERLINK node assignments and cycle timing.

MODEL-SPECIFIC ACCEPTANCE EVIDENCE

CHECK	ACCEPTANCE CONDITION
Identity	X20CP1584 / X20CP1584 recorded
Physical interface	X20 bus base and I/O slices
Companion check	24 V DC controller supply
Functional proof	Uses an Intel Atom E620T processor
Application test	Packaging-machine control

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Engineering Precautions

1

Verify the full X20CP1584 marking and its assigned B&R X20 PLC position before work.

2

Place the connected machine or process in the approved maintenance state.

3

Isolate the applicable supply and plc logic, ethernet/powerlink data and x20 i/o field energy before disconnecting conductors.

4

Record connector, conductor, address and software identities before removal.

5

Use ESD controls and protect keyed interfaces, exposed contacts and electronic assemblies.

RELATED PRODUCTS ON APTER POWER

MODEL	SYSTEM ROLE	APTER POWER PAGE
21000-34-10-20-062-04-02	Related industrial automation product	View 21000-34-10-20-062-04-02 page
21000-34-10-20-058-04-02	Related industrial automation product	View 21000-34-10-20-058-04-02 page
330881-16-00-307-06-02	Related industrial automation product	View 330881-16-00-307-06-02 page
330881-16-00-200-06-02	Related industrial automation product	View 330881-16-00-200-06-02 page
330103-00-25-05-02-00	Related industrial automation product	View 330103-00-25-05-02-00 page
330106-05-30-10-02-IN	Related industrial automation product	View 330106-05-30-10-02-IN page
6RY1243-0EA00	Related industrial automation product	View 6RY1243-0EA00 page
10024-H-I	Related industrial automation product	View 10024-H-I page

MANUFACTURER SOURCE RECORD

B&R official product data sheet

B&R product data

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