

X20CP1584

X20 controller with integrated POWERLINK

PLC logic, Ethernet/POWERLINK data and X20 I/O

PRODUCT SNAPSHOT

BRAND

B&R

FAMILY

B&R X20 PLC

PART

X20CP1584

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PRODUCT OVERVIEW

PLC logic, Ethernet/POWERLINK data and X20 I/O

1

Uses an Intel Atom E620T processor

2

Provides 256 MB DDR2 application memory

3

Integrates POWERLINK and X2X Link

4

Supports CompactFlash application storage

SYSTEM COMPONENTS

X20 bus base and I/O slices

24 V DC controller supply

CompactFlash application media

Automation Studio project

TECHNICAL NOTE

Back up the Automation Studio project and CompactFlash image, then verify interface addresses, POWERLINK node assignments and cycle timing.

TECHNICAL DATA

PARAMETER	VALUE
Processor	Intel Atom E620T; 0.6 GHz
Main memory	256 MB DDR2 SDRAM
Nonvolatile memory	1 MB SRAM

PARAMETER	VALUE
Application storage	CompactFlash slot 1
Shortest task class	400 us
Interfaces	RS232; Ethernet; POWERLINK; 2 USB; X2X Link

ENGINEERING NOTE

Back up the Automation Studio project and CompactFlash image, then verify interface addresses, POWERLINK node assignments and cycle timing.

RELATED PRODUCTS ON APTER POWER

MODEL	SYSTEM ROLE	APTER POWER PAGE
6SL3352-6TE33-8AA3	Related industrial automation product	View 6SL3352-6TE33-8AA3 page
6RY1803-0DA04	Related industrial automation product	View 6RY1803-0DA04 page
C98043-A7106-L4	Related industrial automation product	View C98043-A7106-L4 page
330180-90-CN	Related industrial automation product	View 330180-90-CN page
330106-05-30-10-02-00	Related industrial automation product	View 330106-05-30-10-02-00 page
6RY1803-0DA03	Related industrial automation product	View 6RY1803-0DA03 page
330104-01-05-50-02-00	Related industrial automation product	View 330104-01-05-50-02-00 page
330910-00-24-10-01-CN	Related industrial automation product	View 330910-00-24-10-01-CN page

APPLICATIONS & PRECAUTIONS

COMMON APPLICATIONS

Packaging-machine control

Uses an Intel Atom E620T processor

POWERLINK motion coordination

Provides 256 MB DDR2 application memory

Distributed X20 I/O control

Integrates POWERLINK and X2X Link

Controller replacement

Supports CompactFlash application storage

PRODUCT PRECAUTIONS

- 1 Verify the full X20CP1584 marking and its assigned B&R X20 PLC position before work.
- 2 Place the connected machine or process in the approved maintenance state.
- 3 Isolate the applicable supply and plc logic, ethernet/powerlink data and x20 i/o field energy before disconnecting conductors.
- 4 Record connector, conductor, address and software identities before removal.
- 5 Use ESD controls and protect keyed interfaces, exposed contacts and electronic assemblies.
- 6 Confirm the installed X20 bus base and I/O slices and 24 V DC controller supply against the approved project.
- 7 Restore power in a controlled sequence and review device, network and host diagnostics.
- 8 Prove the intended packaging-machine control function and retain the final test result.

ENGINEER FAQ

Q1 Does X20CP1584 match X20 bus base and I/O slices in a Packaging-machine control installation?

Verify the complete X20CP1584 marking, the documented B&R X20 PLC position and the installed X20 bus base and I/O slices before fitting the assembly.

Q2 How should X20CP1584 be wired when it must uses an intel atom e620t processor?

Isolate the circuit, identify every terminal and reproduce the approved polarity, shield and channel assignment for X20CP1584; then continuity-test the affected path.

Q3 Which B&R X20 PLC records should be captured before removing X20CP1584?

Record the as-found wiring, installed position, active configuration and diagnostic state associated with X20CP1584 before disconnection.

Q4 What controlled test confirms that X20CP1584 can provides 256 mb ddr2 application memory?

Apply the approved field or simulated stimulus, observe the B&R X20 PLC indication and confirm the intended response without bypassing the normal interlocks.

Q5 Which 24 V DC controller supply condition should be checked when X20CP1584 reports an abnormal state?

Check the 24 V DC controller supply supply, seating and connection first, then compare channel diagnostics with the recorded X20CP1584 baseline.

Q6 Can X20CP1584 be moved directly into POWERLINK motion coordination without validating Automation Studio project?

No. Confirm the Automation Studio project, revision, connection scheme and B&R X20 PLC configuration before transferring X20CP1584 to that duty.

Q7 What maintenance evidence is required after X20CP1584 is used for Distributed X20 I/O control?

Retain the nameplate record, corrective action, final diagnostics and a controlled distributed x20 i/o control functional-test result for X20CP1584.

Q8 How should X20CP1584 be returned to service in Controller replacement?

Restore power under control, verify integrates powerlink and x2x link, exercise the intended signal path and release X20CP1584 only after the B&R X20 PLC remains in its approved operating state.

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