

X20CP1686X

High-performance X20 controller

CONTROLLED ENGINEERING COPY

Create a complete project and storage backup, then confirm runtime version, licenses, interface addresses and POWERLINK timing before production release.

INSTALLATION IDENTITY	REQUIRED VALUE
Complete model	X20CP1686X
Manufacturer part	X20CP1686X
Hardware type	Intel Atom; 1.3 GHz
System family	B&R X20 PLC



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01 PRE-INSTALLATION PLAN

- 1

Verify the full X20CP1686X marking and its assigned B&R X20 PLC position before work.
- 2

Place the connected machine or process in the approved maintenance state.
- 3

Isolate the applicable supply and high-performance plc logic and deterministic network data field energy before disconnecting conductors.
- 4

Record connector, conductor, address and software identities before removal.
- 5

Use ESD controls and protect keyed interfaces, exposed contacts and electronic assemblies.

MATCHED HARDWARE

- X20 bus base and I/O
- 24 V DC supply
- CompactFlash or onboard application storage
- Automation Studio project

INSTALLATION CONTROL RECORD

CONTROL ITEM	REQUIRED RECORD
Identity	X20CP1686X / X20CP1686X recorded
Physical interface	X20 bus base and I/O
Companion check	24 V DC supply
Functional proof	Uses a 1.3 GHz Intel Atom processor
Application test	High-speed machine control

02 WIRING & INTERFACE CHECKS

CHECK ITEM	REQUIRED VALUE
Processor	Intel Atom; 1.3 GHz
Main memory	1 GB LPDDR4
Onboard flash	2 GB
Removable media	CompactFlash
Interfaces	Ethernet; POWERLINK; 2 USB; RS232; X2X Link
Power consumption	7.5 W

SYSTEM CONNECTION RECORD

CONNECTION ITEM	DOCUMENTED ASSIGNMENT
Installed host / carrier	X20 bus base and I/O
Companion hardware	24 V DC supply
Field connection	CompactFlash or onboard application storage
Primary system function	Uses a 1.3 GHz Intel Atom processor
Controlled acceptance duty	High-speed machine control

FIELD NOTE

Create a complete project and storage backup, then confirm runtime version, licenses, interface addresses and POWERLINK timing before production release.

03 COMMISSIONING WORKFLOW



COMMISSIONING ACCEPTANCE EVIDENCE

EVIDENCE ITEM	ACCEPTANCE CONDITION
Identity	X20CP1686X / X20CP1686X recorded
Physical interface	X20 bus base and I/O
Companion check	24 V DC supply
Functional proof	Uses a 1.3 GHz Intel Atom processor
Application test	High-speed machine control

04 RETURN-TO-SERVICE RECORD

- ☐
- Confirm the installed X20 bus base and I/O and 24 V DC supply against the approved project.
- ☐
- Restore power in a controlled sequence and review device, network and host diagnostics.
- ☐
- Prove the intended high-speed machine control function and retain the final test result.
- ☐
- Confirm the intended process response before normal operation.

VERIFICATION RECORD

PRODUCT IDENTITY	X20CP1686X
INSTALLED POSITION	Documented B&R X20 PLC position
VERIFICATION METHOD	Controlled inspection and functional test
RELEASE RECORD	Final diagnostic state retained before service

RELATED PRODUCTS ON APTER POWER

MODEL	SYSTEM ROLE	APTER POWER PAGE
750-P5-G5-HI-A1-R-E	Related industrial automation product	View 750-P5-G5-HI-A1-R-E page
1794-OE4XT	Related industrial automation product	View 1794-OE4XT page
6SL3120-2TE21-8AD0	Related industrial automation product	View 6SL3120-2TE21-8AD0 page
6SE7041-8EK85-0LA0	Related industrial automation product	View 6SE7041-8EK85-0LA0 page
6SE7035-1EJ84-1JC2	Related industrial automation product	View 6SE7035-1EJ84-1JC2 page
DSQC332	Related industrial automation product	View DSQC332 page
1794-IE4XOE2	Related industrial automation product	View 1794-IE4XOE2 page
6SE7090-0XX84-1CE0	Related industrial automation product	View 6SE7090-0XX84-1CE0 page

SOURCE IDENTIFIERS

B&R official product data sheet

B&R product data

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