

IC670MFP100

GE Fanuc Series 90 Micro Field Processor

CONTROLLED ENGINEERING COPY

Confirm the IC670MFP100 station position, program checksum, memory references and I/O assignment before transfer; prove output defaults and serial communication before release.

INSTALLATION IDENTITY	REQUIRED VALUE
Complete model	IC670MFP100
Manufacturer part	IC670MFP100
Hardware type	IC670MFP100
System family	GE Fanuc Field Control distributed I/O



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01 PRE-INSTALLATION PLAN

- 1

Verify the complete IC670MFP100 catalog marking and assigned Field Control position before work.
- 2

Place the connected locally controlled process in its approved maintenance state before disturbing the station.
- 3

Isolate BIU and field power, then confirm a safe condition at the IC670MFP100 terminal block.
- 4

Label and record I/O assignments, serial-port wiring and output-state records before disconnecting any conductor from IC670MFP100.
- 5

Use ESD controls and protect the module connector, terminal block keying and exposed electronics.

MATCHED HARDWARE

- Field Control terminal block
- Compatible Field Control I/O modules
- Handheld programmer or host configuration
- 6.5 V DC backplane supply

INSTALLATION CONTROL RECORD

CONTROL ITEM	REQUIRED RECORD
Identity	IC670MFP100 / IC670MFP100 recorded
Physical interface	Field Control terminal block
Companion check	Compatible Field Control I/O modules
Functional proof	Executes Boolean logic at 1 ms per K
Application test	local sequence and distributed I/O control field station

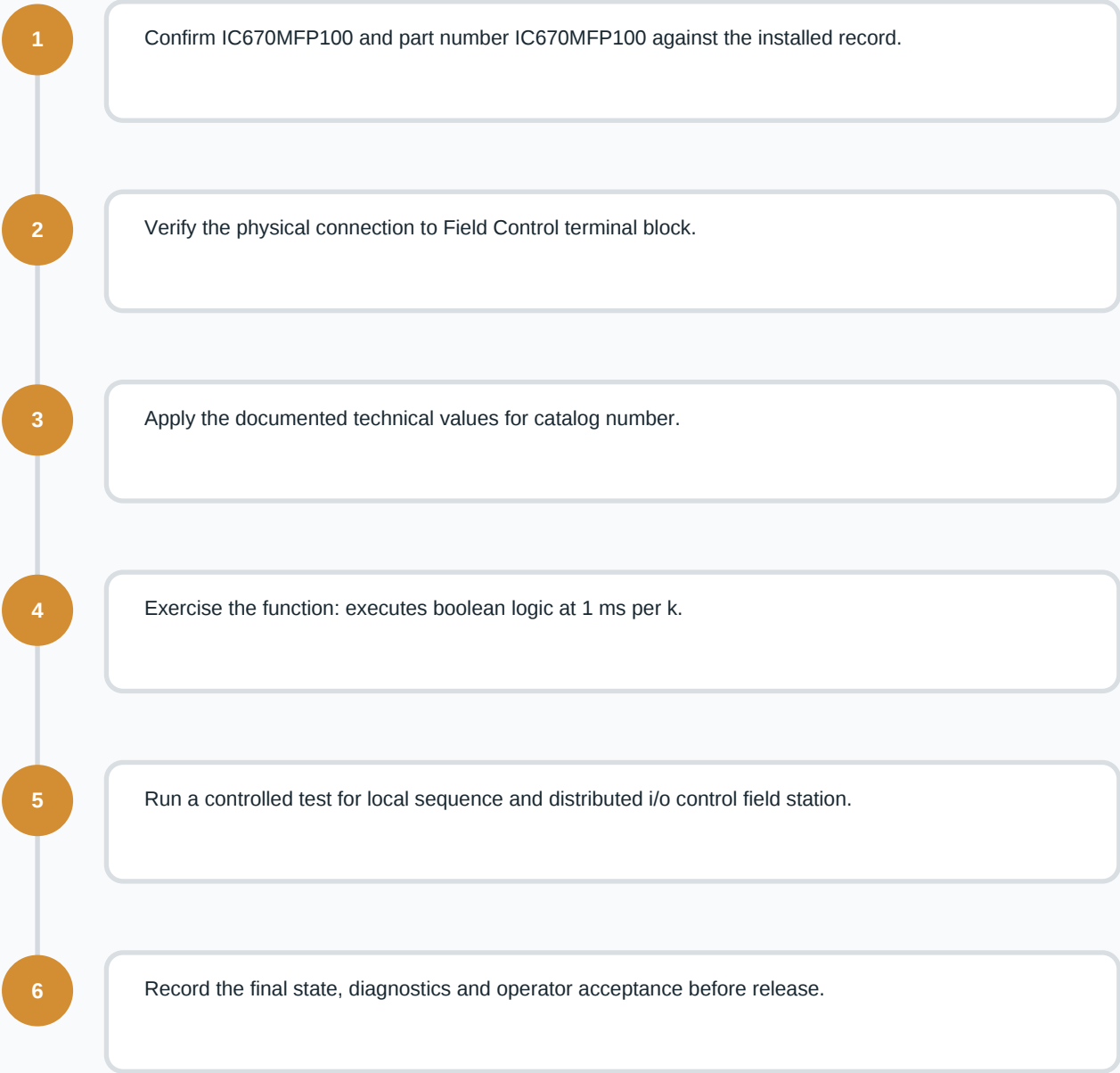
02 WIRING & INTERFACE CHECKS

CHECK ITEM	REQUIRED VALUE
Catalog number	IC670MFP100
Boolean scan rate	1 ms per K of logic
Backplane power	6.5 V DC
Current with handheld programmer	300 mA typical
Current without handheld programmer	110 mA typical
User memory	6 K words
%R memory	2048 words
%AI memory	128 words
%AQ memory	128 words
%I memory	512 bits
%Q memory	512 bits
%G memory	1280 bits
%M memory	1024 bits
%T memory	256 bits

FIELD NOTE

Confirm the IC670MFP100 station position, program checksum, memory references and I/O assignment before transfer; prove output defaults and serial communication before release.

03 COMMISSIONING WORKFLOW



COMMISSIONING ACCEPTANCE EVIDENCE

EVIDENCE ITEM	ACCEPTANCE CONDITION
Identity	IC670MFP100 / IC670MFP100 recorded
Physical interface	Field Control terminal block
Companion check	Compatible Field Control I/O modules
Functional proof	Executes Boolean logic at 1 ms per K
Application test	local sequence and distributed I/O control field station

04 RETURN-TO-SERVICE RECORD

- ☐ Restore only the documented logic program, data references and default states; do not copy settings from an adjacent module.
- ☐ Energize in a controlled sequence and review PWR, channel, module and network diagnostics.
- ☐ Complete program checksum, scan, I/O diagnostics and controlled output tests and retain the final result before returning the process to service.
- ☐ Confirm the intended process response before normal operation.

VERIFICATION RECORD

PRODUCT IDENTITY	IC670MFP100
INSTALLED POSITION	Documented GE Fanuc Field Control distributed I/O position
VERIFICATION METHOD	Controlled inspection and functional test
RELEASE RECORD	Final diagnostic state retained before service

RELATED PRODUCTS ON APTER POWER

MODEL	SYSTEM ROLE	APTER POWER PAGE
VIPC616	Related GE product	View VIPC616 page
IC698CPE020	Related GE product	View IC698CPE020 page
IC697MDL653	Related GE product	View IC697MDL653 page
IC697BEM731	Related GE product	View IC697BEM731 page
IC697ALG320	Related GE product	View IC697ALG320 page
IC693MDL752	Related GE product	View IC693MDL752 page
IC693MDL646	Related GE product	View IC693MDL646 page
IC693MDL330	Related GE product	View IC693MDL330 page

SOURCE IDENTIFIERS

GE Fanuc Series 90 Micro Field Processor User's Manual
GFK-1171, March 1996

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