

KC4011X1-BZ1

DeltaV redundant 48-pin analog I/O mass terminal

CONTROLLED ENGINEERING COPY

Isolate both redundant legs, label each mass cable and field pair, protect the 48-contact interface, reconnect the A/B paths exactly as documented, and verify switchover plus every active channel.

INSTALLATION IDENTITY	REQUIRED VALUE
Complete model	KC4011X1-BZ1
Manufacturer part	KC4011X1-BZ1
Hardware type	KC4011X1-BZ1
System family	DeltaV M-series redundant analog I/O



APTER POWER SALES TEAM

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01 PRE-INSTALLATION PLAN

- 1

Verify the complete KC4011X1-BZ1 marking and the assigned DeltaV M-series redundant analog I/O position before work.
- 2

Place the connected machine or process in the approved maintenance state before disturbing KC4011X1-BZ1.
- 3

Isolate the applicable power and field energy, then confirm a safe state before disconnecting KC4011X1-BZ1.
- 4

Record connector, conductor, slot and configuration identities before removal of KC4011X1-BZ1.
- 5

Use ESD controls and protect keyed interfaces, exposed contacts and electronic assemblies during handling.

MATCHED HARDWARE

- Paired DeltaV analog I/O cards
- 48-pin mass cables
- Redundant carrier arrangement
- Field marshaling and shield terminations

INSTALLATION CONTROL RECORD

CONTROL ITEM	REQUIRED RECORD
Identity	KC4011X1-BZ1 / KC4011X1-BZ1 recorded
Physical interface	Paired DeltaV analog I/O cards
Companion check	48-pin mass cables
Functional proof	Supports redundant analog I/O termination
Application test	Redundant analog transmitter systems

02 WIRING & INTERFACE CHECKS

CHECK ITEM	REQUIRED VALUE
Part number	KC4011X1-BZ1
Terminal type	48-contact mass terminal
I/O application	Redundant analog input/output
Module family	M-series 16-channel AI/AO
Ambient temperature	-40 to +70 degC
Supply voltage	30 V DC
Maximum current per channel	500 mA

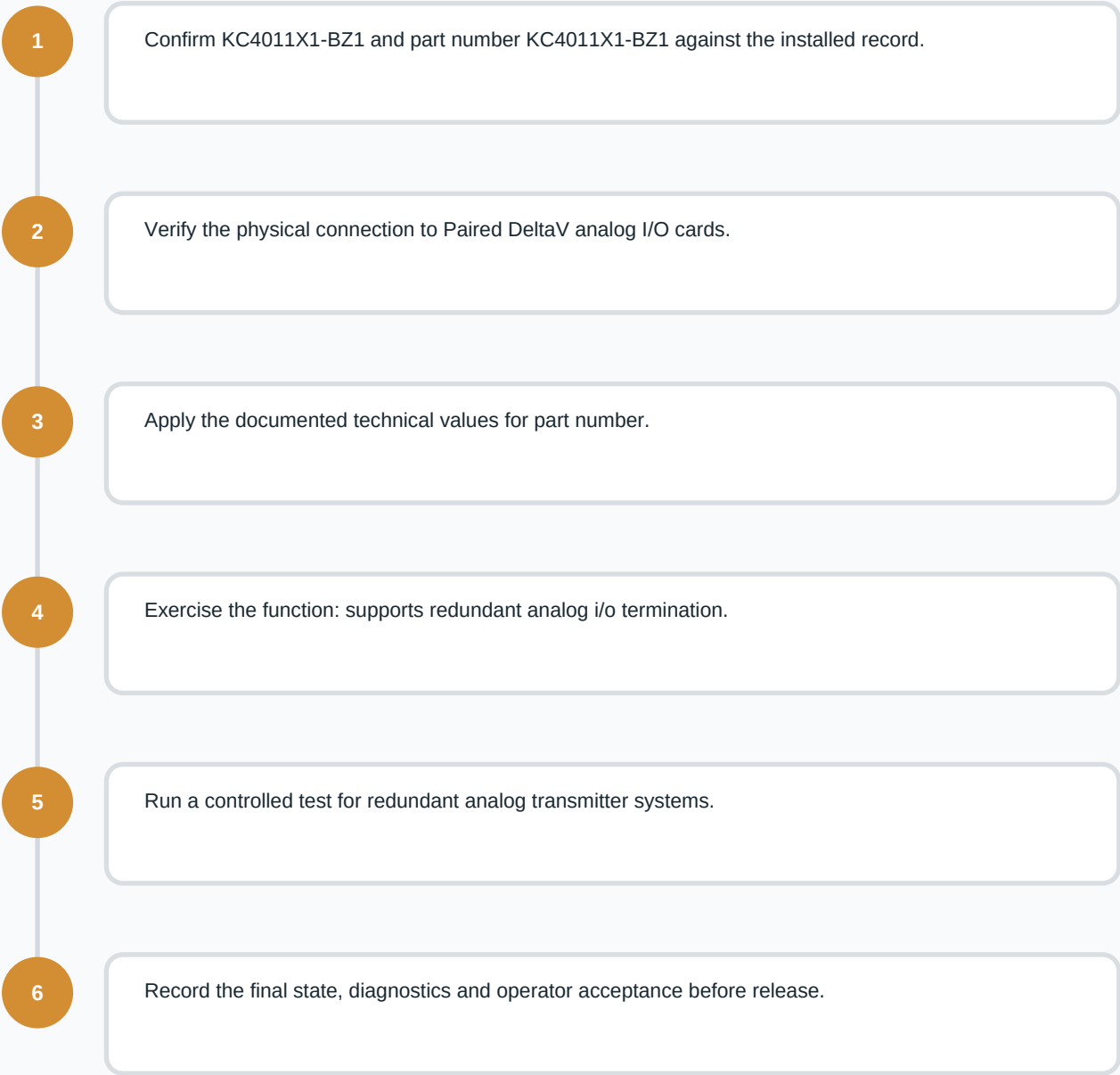
SYSTEM CONNECTION RECORD

CONNECTION ITEM	DOCUMENTED ASSIGNMENT
Installed host / carrier	Paired DeltaV analog I/O cards
Companion hardware	48-pin mass cables
Field connection	Redundant carrier arrangement
Primary system function	Supports redundant analog I/O termination
Controlled acceptance duty	Redundant analog transmitter systems

FIELD NOTE

Isolate both redundant legs, label each mass cable and field pair, protect the 48-contact interface, reconnect the A/B paths exactly as documented, and verify switchover plus every active channel.

03 COMMISSIONING WORKFLOW



COMMISSIONING ACCEPTANCE EVIDENCE

EVIDENCE ITEM	ACCEPTANCE CONDITION
Identity	KC4011X1-BZ1 / KC4011X1-BZ1 recorded
Physical interface	Paired DeltaV analog I/O cards
Companion check	48-pin mass cables
Functional proof	Supports redundant analog I/O termination
Application test	Redundant analog transmitter systems

04 RETURN-TO-SERVICE RECORD

- ☐
- Confirm that the installed companion hardware and approved project match the KC4011X1-BZ1 arrangement.
- ☐
- Restore power in a controlled sequence and review device, rack and host diagnostics.
- ☐
- Prove the intended function and retain the final test result before returning KC4011X1-BZ1 to service.
- ☐
- Confirm the intended process response before normal operation.

VERIFICATION RECORD

PRODUCT IDENTITY	KC4011X1-BZ1
INSTALLED POSITION	Documented DeltaV M-series redundant analog I/O position
VERIFICATION METHOD	Controlled inspection and functional test
RELEASE RECORD	Final diagnostic state retained before service

RELATED PRODUCTS ON APTER POWER

MODEL	SYSTEM ROLE	APTER POWER PAGE
EZ1000	Related Emerson product	View EZ1000 page
KJ3222X1-BA1-12P2532X112 ...	Related Emerson product	View KJ3222X1-BA1-12P2532X112... page
KJ3221X1-BA1-12P2531X122 ...	Related Emerson product	View KJ3221X1-BA1-12P2531X122... page
775XD11NAWA3WK9	Related Emerson product	View 775XD11NAWA3WK9 page
098-01257-01	Related Emerson product	View 098-01257-01 page
5X00167G01	Related Emerson product	View 5X00167G01 page
PR6423-010-040-CON021	Related Emerson product	View PR6423-010-040-CON021 page
CL6821X1-A5-41B5222X422-65	Related Emerson product	View CL6821X1-A5-41B5222X422-65 page

SOURCE IDENTIFIERS

Emerson DeltaV equipment certification and M-series mass-connection documentation
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