

PRODUCT DASHBOARD

T8110B

Trusted Triple Modular Redundant Processor

ICS Triplex T8110B is a Trusted triple modular redundant processor module. It executes the Trusted control application and coordinates I/O through the triple-redundant inter-module bus.

BRAND

ICS Triplex

ORIGIN

USA

HS CODE

8537101190

SHIPS FROM

Xiamen, Fujian, China

Complete public-page information view

Technical data dashboard

Trusted Triple Modular Redundant Processor

SUPPLY VOLTAGE

20 to 32 Vdc

MAXIMUM LOAD

80 W

HEAT DISSIPATION

80 W

REQUIRED CHASSIS

T8100

PROCESSOR CLOCK

100 MHz

DRAM

16 MB EDO, 60 ns

EPROM

512 kB

FLASH MEMORY

2 MB

NVRAM

128 kB

RETAINED VARIABLES

4 kB

SOE BUFFER

1000 events; 4000-event CI buffer

I/O INTERFACE

Triple-redundant inter-module bus

Application and support view

Four engineering use cases plus customer-oriented verification questions.

Safety application execution

Runs validated Trusted application logic for process and machinery protection functions.

Redundant controller operation

Participates in a synchronized TMR processor group that maintains service during a controlled module exchange.

Event sequence recording

Buffers time-ordered controller events for transfer through the communications interface.

Trusted I/O coordination

Exchanges command and response data with configured Trusted I/O and interface modules.

FAQ SNAPSHOT

1

What is the exact system role of T8110B?

2

Which host, rack or carrier is required for T8110B?

3

What wiring or connector checks apply to T8110B?

4

How is T8110B configured for service?

Related-series navigator

Eight unique and verified Apter Power destinations for this dashboard.

- 01

TC-301-02-2M0 Apter Power product page
- 02

TC-301-02-6M5 Apter Power product page
- 03

TC-301-02-10M0 Apter Power product page
- 04

T9482 Apter Power product page
- 05

T9803 Apter Power product page
- 06

TC-001-02-2M5 Apter Power product page
- 07

T8235 Apter Power product page
- 08

TC-301-02-15M0 Apter Power product page

Related pages are navigation aids and are not used as manufacturer parameter sources.

Traceability and commissioning closeout

Confirm the T8110B model and revision, T8100 chassis, partner processor state, application and SYSTEM.INI versions, communications adapters, inter-module bus health, time synchronization, licenses and a controlled functional test before release.

OFFICIAL MANUFACTURER SOURCE

Trusted TMR Processor

PD_T8110B/T8110 Issue 22 | ICSTT-RM251

ENGINEERING RELEASE NOTE

Install T8110B only in a T8100 chassis using ESD controls and the module ejectors; record type, revision and serial number before exchange. After restoring the application and SYSTEM.INI settings, verify processor synchronization, communications, I/O status and event handling before enabling the process.

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