

T8110B

Trusted Triple Modular Redundant Processor

PRODUCT TECHNICAL REFERENCE

ICS Triplex T8110B is a Trusted triple modular redundant processor module. It executes the Trusted control application and coordinates I/O through the triple-redundant inter-module bus.

OFFICIAL SOURCE

Trusted TMR Processor | PD_T8110B/T8110 Issue 22 | ICSTT-RM251

Verified technical profile

Trusted Triple Modular Redundant Processor

Supply voltage	20 to 32 Vdc
Maximum load	80 W
Heat dissipation	80 W
Required chassis	T8100
Processor clock	100 MHz
DRAM	16 MB EDO, 60 ns
EPROM	512 kB
Flash memory	2 MB
NVRAM	128 kB
Retained variables	4 kB
SOE buffer	1000 events; 4000-event CI buffer
I/O interface	Triple-redundant inter-module bus
Operating temperature	0 to 60 deg C
Dimensions	266 x 93 x 303 mm
Weight	2.94 kg

ENGINEERING USE OF THE VERIFIED DATA

The processor occupies the Trusted controller chassis and communicates with Trusted interface and I/O modules over the inter-module bus. Commissioning includes application download, processor synchronization, communication-interface checks, discrepancy settings and validation of the controlled outputs.

Verification focus: Verify the complete T8110B marking, hardware revision and serial number.

Only values traceable to the listed manufacturer source are treated as verified technical data.

System role and engineering boundaries

ICS Triplex T8110B is a Trusted triple modular redundant processor module. It executes the Trusted control application and coordinates I/O through the triple-redundant inter-module bus.

1

Executes the Trusted safety and control application

2

Coordinates triplicated processor paths and inter-module communications

3

Maintains retained variables and sequence-of-events records

4

Supports controlled online diagnostics and processor replacement workflows

ENGINEERING NOTE

Install T8110B only in a T8100 chassis using ESD controls and the module ejectors; record type, revision and serial number before exchange. After restoring the application and SYSTEM.INI settings, verify processor synchronization, communications, I/O status and event handling before enabling the process.

Industrial application matrix

Four source-supported operating contexts for this exact product.

Safety application execution Runs validated Trusted application logic for process and machinery protection functions.	Redundant controller operation Participates in a synchronized TMR processor group that maintains service during a controlled module exchange.
Event sequence recording Buffers time-ordered controller events for transfer through the communications interface.	Trusted I/O coordination Exchanges command and response data with configured Trusted I/O and interface modules.
MODEL-SPECIFIC ACCEPTANCE FOCUS Confirm the T8110B model and revision, T8100 chassis, partner processor state, application and SYSTEM.INI versions, communications adapters, inter-module bus health, time synchronization, licenses and a controlled functional test before release.	

Related-series navigation

Eight verified Apter Power product pages, kept separate from the manufacturer source record.

RELATED APTER POWER SERIES PAGES	
01	T9852 Apter Power product page
02	T9100 Apter Power product page
03	T9193 Apter Power product page
04	MAI32MAD Apter Power product page
05	T8850C Apter Power product page
06	T8850 Apter Power product page
07	T8830 Apter Power product page
08	T8461CX Apter Power product page

Navigation links only - not manufacturer evidence for technical parameters.

SOURCE RECORD

Trusted TMR Processor | PD_T8110B/T8110 Issue 22 | ICSTT-RM251

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