

8C-TDIL11

Redundant Series 8 Digital Input Sequence-of-Events IOTA

Honeywell 8C-TDIL11 is a coated redundant 32-circuit Series 8 SOE digital-input IOTA. It provides the shared field interface for paired SOE input processor modules.

SIGNAL AND EVENT-DATA PATH



PRODUCT SNAPSHOT

MANUFACTURER

Honeywell

PART NUMBER

51306858-175

PLATFORM

Experion Series 8 I/O

FUNCTION

Sequence-of-events digital input IOTA

TRADE ORIGIN

USA

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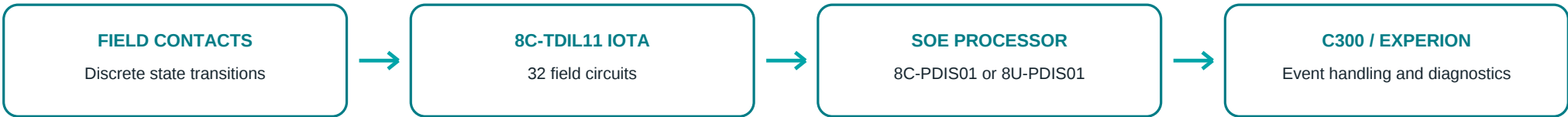
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8C-TDIL11

System role and event-data path

8C-TDIL11 is the field-termination interface in a Series 8 sequence-of-events input arrangement. It receives discrete field-contact wiring and connects it to the paired SOE processor modules for ordered event acquisition.



Four model-specific functions

1

FUNCTION 1
Terminates redundant sequence-of-events input hardware

2

FUNCTION 2
Maintains two processor-module positions for one field interface

3

FUNCTION 3
Transfers time-ordered discrete transitions

4

FUNCTION 4
Supports availability-focused event acquisition

EVENT-INTEGRITY CHECKS

●

Preserve field-channel identity

●

Synchronize the event timebase

●

Verify the paired SOE processors

●

Prove ordered input transitions

Matched installation components

●

Series 8 redundant carrier

●

Primary and secondary SOE processors

●

Field-contact terminal block

●

C300 redundancy configuration

ENGINEERING CONTEXT

Preserve channel identity from the field terminal through the paired SOE processors. Event chronology depends on the complete installed arrangement, including the processor-pair assignment and the C300 event-processing configuration.

Verified technical data

TECHNICAL PARAMETER	VERIFIED VALUE
IOTA function	Digital input sequence of events
I/O circuits	32
IOTA configuration	Redundant
IOTA size class	12 inch / 304 mm
Coating	Conformal
Compatible I/O modules	8C-PDIS01; 8U-PDIS01

TECHNICAL PARAMETER	VERIFIED VALUE
Field-power bus	24 VDC
Terminal style	Tool-detachable
Field termination	On-board
Signal conditioning	On-board
Redundancy interconnection	Integral on IOTA
Redundancy external cabling	Not required

TECHNICAL NOTE AND CONTROLLED ACCEPTANCE

Verify both processor identities and the redundancy role before removal; commission the restored pair with controlled input transitions and a documented failover check.

Acceptance: verify full model marking, processor pairing, 32-channel identity, synchronized SOE timebase and chronological transition order.

Application map

1 REDUNDANT TRIP-EVENT CAPTURE

Terminates redundant sequence-of-events input hardware

2 HIGH-AVAILABILITY SWITCHGEAR MONITORING

Maintains two processor-module positions for one field interface

3 SAFETY-SUPPORT EVENT CHRONOLOGY

Transfers time-ordered discrete transitions

4 CRITICAL COMPRESSOR STATE RECORDING

Supports availability-focused event acquisition

Diagnostic workflow

IDENTIFY

Confirm IOTA, part marking and processor-pair assignment.

ISOLATE

Control field energy and protect the electronic assembly.

RECORD

Capture channel identity and as-found connections.

RESTORE

Reinstall the matched Series 8 hardware arrangement.

PROVE

Inject controlled transitions and verify event order.

Installation and replacement controls

1 Verify the complete 8C-TDIL11 marking and its assigned Experion Series 8 I/O position before work.

2 Isolate the energy associated with Series 8 redundant carrier before disconnecting 8C-TDIL11.

3 Protect the 8C-TDIL11 connectors and electronic assembly using approved ESD controls.

4 Record the as-found Primary and secondary SOE processors connections and configured assignment before replacement.

5 Confirm Field-contact terminal block matches the approved installation record for 8C-TDIL11.

6 Restore 8C-TDIL11 under controlled conditions and review its model-specific diagnostics.

7 Complete a documented functional test for redundant trip-event capture before release.

IMPORTANT NOTICE

Confirm the complete 8C-TDIL11 marking, Series 8 carrier position, processor pairing, field connections and event-processing configuration before commissioning or replacement. Preserve the documented channel identity and validate transition order before release.

Engineer FAQ

Q1 Which processor modules pair with 8C-TDIL11?

Use the documented Series 8 SOE processor pairing: 8C-PDIS01 or 8U-PDIS01.

Q2 How many field circuits does 8C-TDIL11 terminate?

The official Series 8 I/O specification lists 32 I/O circuits for this IOTA.

Q3 What signal role does 8C-TDIL11 perform?

It terminates discrete sequence-of-events input circuits and connects them to the paired SOE processors.

Q4 Is 8C-TDIL11 a redundant IOTA?

8C-TDIL11 is the redundant variant; verify the installed architecture before selecting a replacement.

Q5 What should be recorded before replacing 8C-TDIL11?

Record the complete marking, carrier position, processor-pair assignment, terminal wiring and channel identity.

Q6 How is 8C-TDIL11 commissioned after installation?

Synchronize the event timebase, apply controlled input transitions and confirm their chronological order.

Q7 What hardware must be checked with 8C-TDIL11?

Check the Series 8 carrier, paired SOE processors, field-contact wiring and C300 event-processing configuration.

Q8 What coating is specified for 8C-TDIL11?

The directly related Series 8 I/O specification lists conformal coating.

Related Series 8 products on Apter Power

MODEL	SERIES ROLE	APTER POWER PRODUCT PAGE
8C-PDIL51 51454359-175	Series 8 digital-input processor module	Open product page
8C-PDILA1	Series 8 digital-input processor module	Open product page
8C-PDOD51 51454361-175	Series 8 digital-output processor module	Open product page
8C-PDODA1 51454472-175	Series 8 24 Vdc digital-output module	Open product page
8C-TDODA1 51307149-175	Series 8 digital-output IOTA	Open product page
8C-TDODB1	Series 8 redundant digital-output IOTA	Open product page